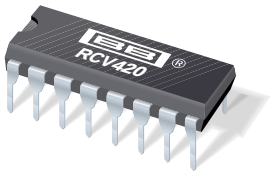
BURR - BROWN®



RCV420

Precision 4mA to 20mA CURRENT LOOP RECEIVER

FEATURES

- COMPLETE 4-20mA TO 0-5V CONVERSION
- INTERNAL SENSE RESISTORS
- PRECISION 10V REFERENCE
- BUILT-IN LEVEL-SHIFTING
- ±40V COMMON-MODE INPUT RANGE
- 0.1% OVERALL CONVERSION ACCURACY
- HIGH NOISE IMMUNITY: 86dB CMR

APPLICATIONS

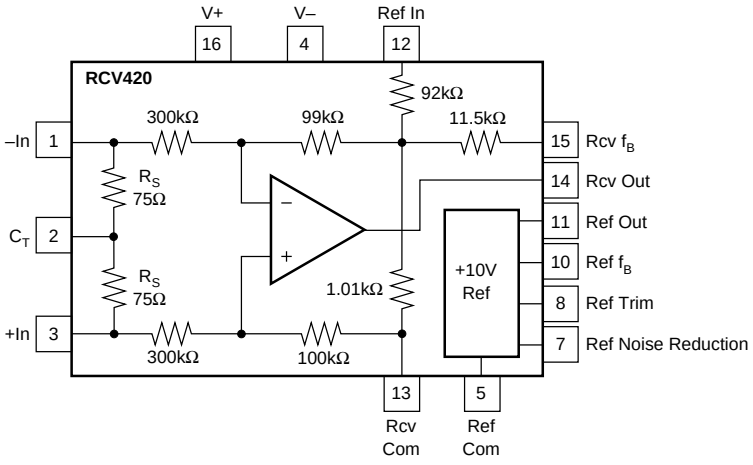
- PROCESS CONTROL
- INDUSTRIAL CONTROL
- FACTORY AUTOMATION
- DATA ACQUISITION
- SCADA
- RTUs
- ESD
- MACHINE MONITORING

DESCRIPTION

The RCV420 is a precision current-loop receiver designed to convert a 4–20mA input signal into a 0–5V output signal. As a monolithic circuit, it offers high reliability at low cost. The circuit consists of a premium grade operational amplifier, an on-chip precision resistor network, and a precision 10V reference. The RCV420 features 0.1% overall conversion accuracy, 86dB CMR, and ±40V common-mode input range.

The circuit introduces only a 1.5V drop at full scale, which is useful in loops containing extra instrument burdens or in intrinsically safe applications where transmitter compliance voltage is at a premium. The 10V reference provides a precise 10V output with a typical drift of 5ppm/°C.

The RCV420 is completely self-contained and offers a highly versatile function. No adjustments are needed for gain, offset, or CMR. This provides three important advantages over discrete, board-level designs: 1) lower initial design cost, 2) lower manufacturing cost, and 3) easy, cost-effective field repair of a precision circuit.



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SBVS019

SPECIFICATIONS

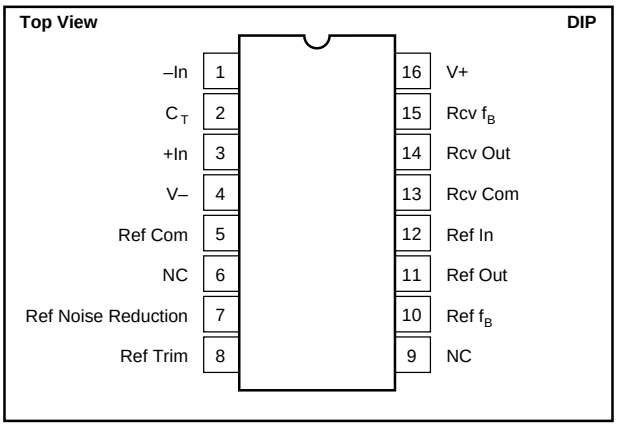
ELECTRICAL

At T = +25°C and V_S = ±15V, unless otherwise noted.

CHARACTERISTICS	RCV420KP, JP MIN	RCV420KP, JP TYP	RCV420KP, JP MAX	UNITS
GAIN				
Initial		0.3125		V/mA
Error		0.05	0.15	% of span
Error—JP Grade			0.25	% of span
vs Temp		15		ppm/°C
Nonlinearity(1)		0.0002	0.002	% of span
OUTPUT				
Rated Voltage (I_O = +10mA, –5mA)	10	12		V
Rated Current (E_O = 10V)	+10, –5			mA
Impedance (Differential)		0.01		Ω
Current Limit (To Common)		+49, –13		mA
Capacitive Load (Stable Operation)		1000		pF
INPUT				
Sense Resistance	74.25	75	75.75	Ω
Input Impedance (Common-Mode)		200		kΩ
Common-Mode Voltage			±40	V
CMR(2)	70	80		dB
vs Temp (DC) (T_A = T_{MIN} to T_{MAX})		76		dB
AC 60Hz		80		dB
OFFSET VOLTAGE (RTO)(3)				
Initial			1	mV
vs Temp		10		µV/°C
vs Supply (±11.4V to ±18V)	74	90		dB
vs Time		200		µV/mo
ZERO ERROR(4)				
Initial		0.025	0.075	% of span
Initial—JP Grade			0.15	% of span
vs Temp		10		ppm of span/°C
OUTPUT NOISE VOLTAGE				
f_B = 0.1Hz to 10Hz		50		µVp-p
f_O = 10kHz		800		nV/√Hz
DYNAMIC RESPONSE				
Gain Bandwidth		150		kHz
Full Power Bandwidth		30		kHz
Slew Rate		1.5		V/µs
Settling Time (0.01%)		10		µs
VOLTAGE REFERENCE				
Initial	9.99		10.01	V
Trim Range(5)		±4		%
vs Temp		5		ppm/°C
vs Supply (±11.4V to ±18V)		0.0002		%/V
vs Output Current (I_O = 0 to +10mA)		0.0002		%/mA
vs Time		15		ppm/kHz
Noise (0.1Hz to 10Hz)		5		µVp-p
Output Current	+10, –2			mA
POWER SUPPLY				
Rated		±15		V
Voltage Range(6)	–5, +11.4		±18	V
Quiescent Current (V_O = 0V)		3	4	mA
TEMPERATURE RANGE				
Specification	0		+70	°C
Operation	–25		+85	°C
Storage	–40		+85	°C
Thermal Resistance, θ_{JA}		80		°C/W

NOTES: (1) Nonlinearity is the max peak deviation from best fit straight line. (2) With 0 source impedance on Rcv Com pin. (3) Referred to output with all inputs grounded including Ref In. (4) With 4mA input signal and Voltage Reference connected (includes V_{OS}, Gain Error, and Voltage Reference Errors). (5) External trim slightly affects drift. (6) I_O Ref = 5mA, I_O Rcv = 2mA.

PIN CONFIGURATION



ABSOLUTE MAXIMUM RATINGS[1]

Supply	±22V
Input Current, Continuous	40mA
Input Current Momentary, 0.1s	250mA, 1% Duty Cycle
Common-Mode Input Voltage, Continuous	±40V
Lead Temperature (soldering, 10s)	+300°C
Output Short Circuit to Common (Rcv and Ref)	Continuous

NOTE: (1) Stresses above these ratings may cause permanent damage.

PACKAGE INFORMATION

PRODUCT	PACKAGE	PACKAGE DRAWING NUMBER[1]
RCV420KP	16-Pin Plastic DIP	180
RCV420JP	16-Pin Plastic DIP	180

NOTE: (1) For detailed drawing and dimension table, please see end of data sheet, or Appendix C of Burr-Brown IC Data Book.

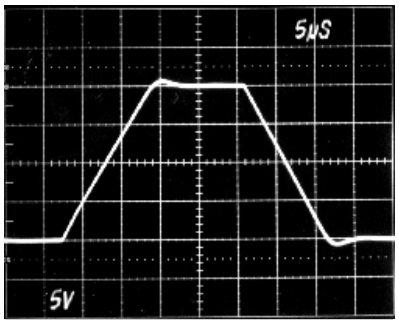
ORDERING INFORMATION

PRODUCT	PERFORMANCE GRADE	PACKAGE
RCV420KP	0°C to +70°C	16-Pin Plastic DIP
RCV420JP	0°C to +70°C	16-Pin Plastic DIP

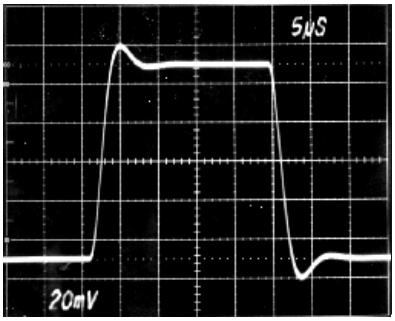
TYPICAL PERFORMANCE CURVES

At T_A = +25°C, V_S = ±15V, unless otherwise noted.

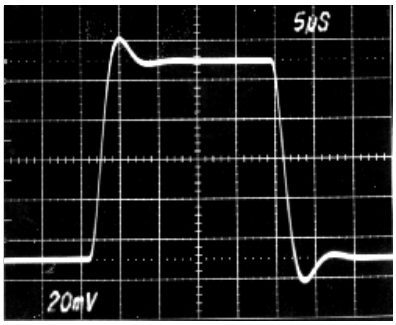
STEP RESPONSE
NO LOAD



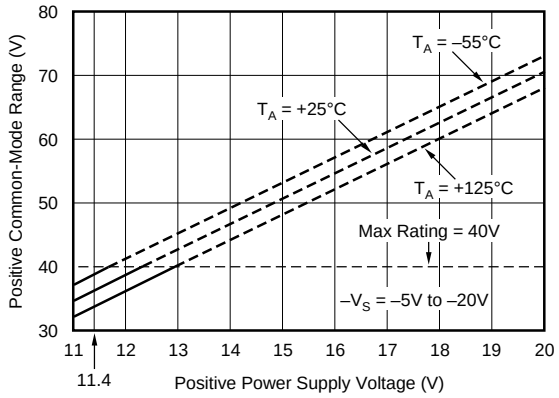
SMALL SIGNAL RESPONSE
NO LOAD



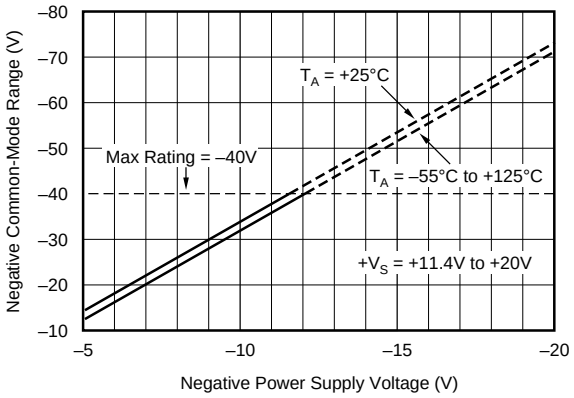
SMALL SIGNAL RESPONSE
R_L = ∞, C_L = 1000pF



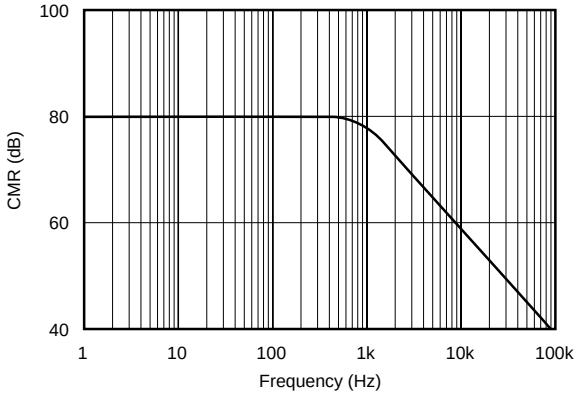
POSITIVE COMMON-MODE VOLTAGE RANGE
vs POSITIVE POWER SUPPLY VOLTAGE



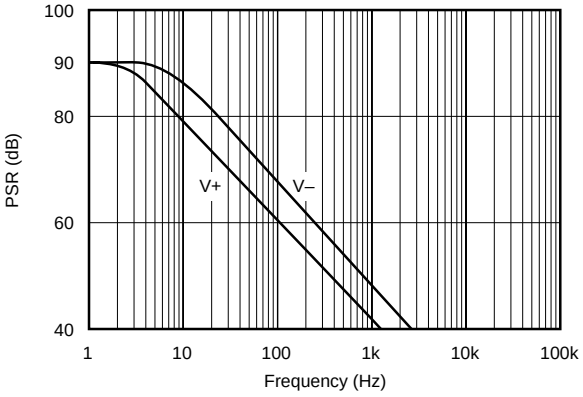
NEGATIVE COMMON-MODE VOLTAGE RANGE
vs NEGATIVE POWER SUPPLY VOLTAGE



COMMON-MODE REJECTION
vs FREQUENCY



POWER-SUPPLY REJECTION
vs FREQUENCY



THEORY OF OPERATION

Refer to the figure on the first page. For 0 to 5V output with 4–20mA input, the required transimpedance of the circuit is:

$$V_{OUT}/I_{IN} = 5V/16mA = 0.3125V/mA.$$

To achieve the desired output (0V for 4mA and 5V for 20mA), the output of the amplifier must be offset by an amount:

$$V_{OS} = –(4mA)(0.3125V/mA) = –1.25V.$$

The input current signal is connected to either +In or –In, depending on the polarity of the signal, and returned to ground through the center tap, C_T. The balanced input—two matched 75Ω sense resistors, R_S—provides maximum rejection of common-mode voltage signals on C_T and true differential current-to-voltage conversion. The sense resistors convert the input current signal into a proportional voltage, which is amplified by the differential amplifier. The voltage gain of the amplifier is:

$$A_D = 5V/(16mA)(75Ω) = 4.1667V/V.$$

The tee network in the feedback path of the amplifier provides a summing junction used to generate the required –1.25V offset voltage. The input resistor network provides high-input impedance and attenuates common-mode input voltages to levels suitable for the operational amplifier's common-mode signal capabilities.

BASIC POWER SUPPLY AND SIGNAL CONNECTIONS

Figure 1 shows the proper connections for power supply and signal. Both supplies should be decoupled with 1µF tantalum capacitors as close to the amplifier as possible. To avoid gain and CMR errors introduced by the external circuit, connect grounds as indicated, being sure to minimize ground resistance. The input signal should be connected to either +In or –In, depending on its polarity, and returned to ground through the center tap, C_T. The output of the voltage reference, Ref Out, should be connected to Ref In for the necessary level shifting. If the Ref In pin is not used for level shifting, then it must be grounded to maintain high CMR.

GAIN AND OFFSET ADJUSTMENT

Figure 2 shows the circuit for adjusting the RCV420 gain. Increasing the gain of the RCV420 is accomplished by inserting a small resistor in the feedback path of the amplifier. Increasing the gain using this technique results in CMR degradation, and therefore, gain adjustments should be kept as small as possible. For example, a 1% increase in gain is typically realized with a 125Ω resistor, which degrades CMR by about 6dB.

A decrease in gain can be achieved by placing matched resistors in parallel with the sense resistors, also shown in Figure 2. The adjusted gain is given by the following expression

$$V_{OUT}/I_{IN} = 0.3125 \times R_X/(R_X + R_S).$$

A 1% decrease in gain can be achieved with a 7.5kΩ resistor. It is important to match the parallel resistance on each sense resistor to maintain high CMR. The TCR mismatch between the two external resistors will effect gain error drift and CMR drift.

There are two methods for nulling the RCV420 output offset voltage. The first method applies to applications using the internal 10V reference for level shifting. For these applica-

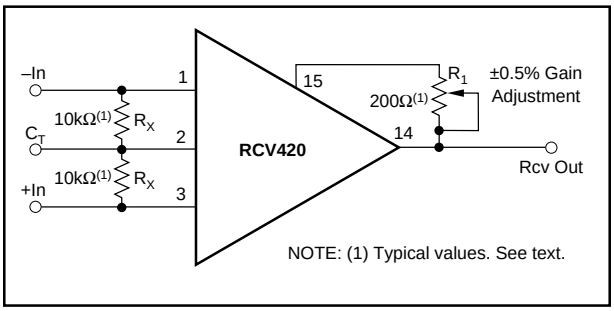


FIGURE 2. Optional Gain Adjustment.

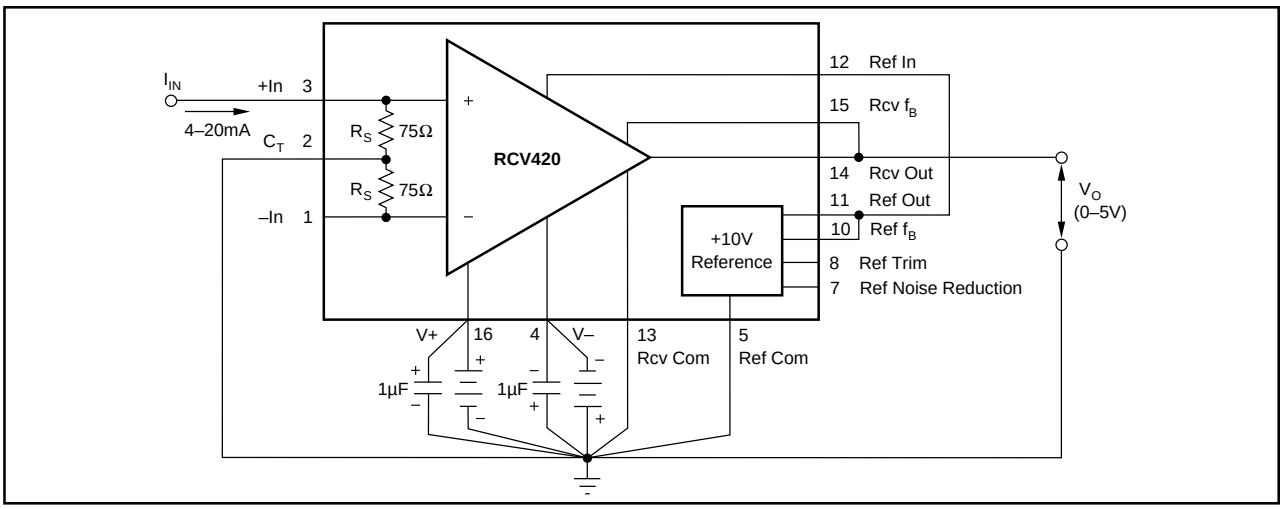


FIGURE 1. Basic Power Supply and Signal Connections.

tions, the voltage reference output trim procedure can be used to null offset errors at the output of the RCV420. The voltage reference trim circuit is discussed under "Voltage Reference."

When the voltage reference is not used for level shifting or when large offset adjustments are required, the circuit in Figure 3 can be used for offset adjustment. A low impedance on the Rcv Com pin is required to maintain high CMR.

ZERO ADJUSTMENT

Level shifting the RCV420 output voltage can be achieved using either the Ref In pin or the Rcv Com pin. The disadvantage of using the Ref In pin is that there is an 8:1 voltage attenuation from this pin to the output of the RCV420. Thus, use the Rcv Com pin for large offsets, because the voltage on this pin is seen directly at the output. Figure 4 shows the circuit used to level-shift the output of the RCV420 using the Rcv Com pin. It is important to use a low-output impedance amplifier to maintain high CMR. With this method of zero adjustment, the Ref In pin must be connected to the Rcv Com pin.

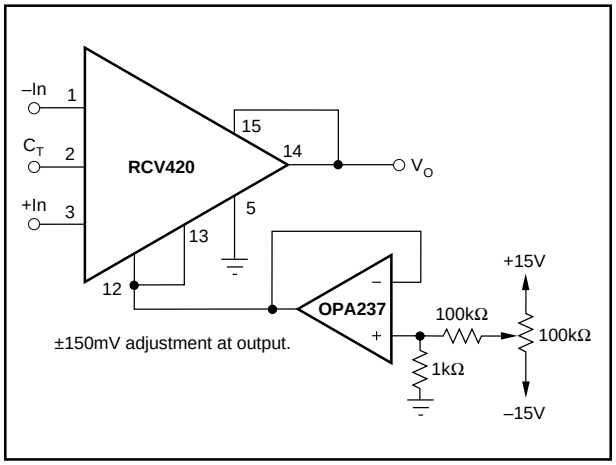


FIGURE 3. Optional Output Offset Nulling Using External Amplifier.

MAINTAINING COMMON-MODE REJECTION

Two factors are important in maintaining high CMR: (1) resistor matching and tracking (the internal resistor network does this) and (2) source impedance. CMR depends on the accurate matching of several resistor ratios. The high accuracies needed to maintain the specified CMR and CMR temperature coefficient are difficult and expensive to reliably achieve with discrete components. Any resistance imbalance introduced by external circuitry directly affects CMR. These imbalances can occur by: mismatching sense resistors when gain is decreased, adding resistance in the feedback path when gain is increased, and adding series resistance on the Rcv Com pin.

The two sense resistors are laser-trimmed to typically match within 0.01%; therefore, when adding parallel resistance to decrease gain, take care to match the parallel resistance on each sense resistor. To maintain high CMR when increasing the gain of the RCV420, keep the series resistance added to the feedback network as small as possible. Whether the Rcv Com pin is grounded or connected to a voltage reference for level shifting, keep the series resistance on this pin as low as possible. For example, a resistance of 20Ω on this pin degrades CMR from 86dB to approximately 80dB. For applications requiring better than 86dB CMR, the circuit shown in Figure 5 can be used to adjust CMR.

PROTECTING THE SENSE RESISTOR

The 75Ω sense resistors are designed for a maximum continuous current of 40mA, but can withstand as much as 250mA for up to 0.1s (see absolute maximum ratings). There are several ways to protect the sense resistor from

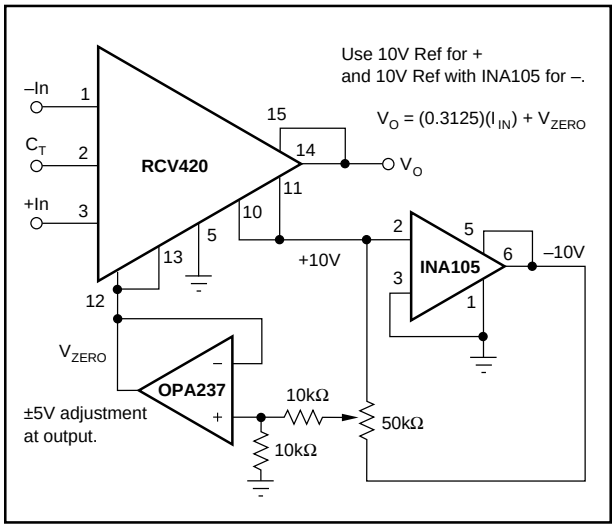


FIGURE 4. Optional Zero Adjust Circuit.

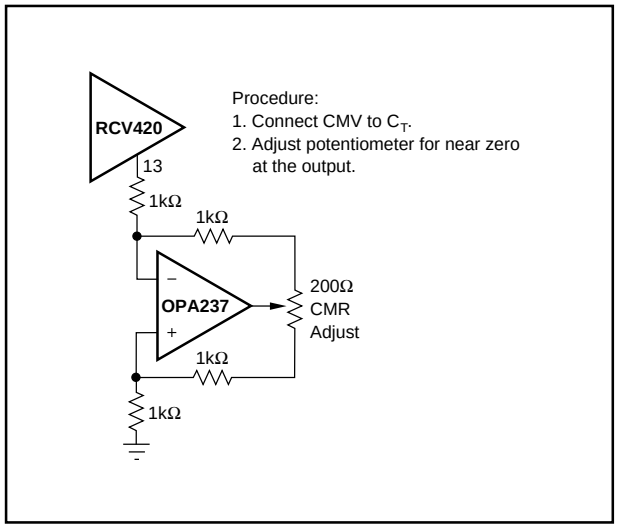


FIGURE 5. Optional Circuit for Externally Trimming CMR.

overcurrent conditions exceeding these specifications. Refer to Figure 6. The simplest and least expensive method is a resistor as shown in Figure 6a. The value of the resistor is determined from the expression

$$R_X = V_{CC}/40mA - 75\Omega$$

and the full scale voltage drop is

$$V_{RX} = 20mA \times R_X.$$

For a system operating off of a 32V supply $R_X = 725\Omega$ and $V_{RX} = 14.5V$. In applications that cannot tolerate such a large voltage drop, use circuits 6b or 6c. In circuit 6b a power JFET and source resistor are used as a current limit. The 200Ω potentiometer, R_X, is adjusted to provide a current limit of approximately 30mA. This circuit introduces a 1–4V drop at full scale. If only a very small series voltage drop at full scale can be tolerated, then a 0.032A series 217 fast-acting fuse should be used, as shown in Figure 6c.

For automatic fold-back protection, use the circuit shown in Figure 15.

VOLTAGE REFERENCE

The RCV420 contains a precision 10V reference. Figure 8 shows the circuit for output voltage adjustment. Trimming the output will change the voltage drift by approximately 0.007ppm/°C per mV of trimmed voltage. Any mismatch in TCR between the two sides of the potentiometer will also affect drift, but the effect is divided by approximately 5. The trim range of the voltage reference using this method is typically ±400mV. The voltage reference trim can be used to trim offset errors at the output of the RCV420. There is an 8:1 voltage attenuation from Ref In to Rcv Out, and thus the trim range at the output of the receiver is typically ±50mV.

The high-frequency noise (to 1MHz) of the voltage reference is typically 1mVp-p. When the voltage reference is used for level shifting, its noise contribution at the output of the receiver is typically 125µVp-p due to the 8:1 attenuation from Ref In to Rcv Out. The reference noise can be reduced by connecting an external capacitor between the Noise Reduction pin and ground. For example, 0.1µF capacitor reduces the high-frequency noise to about 200µVp-p at the output of the reference and about 25µVp-p at the output of the receiver.

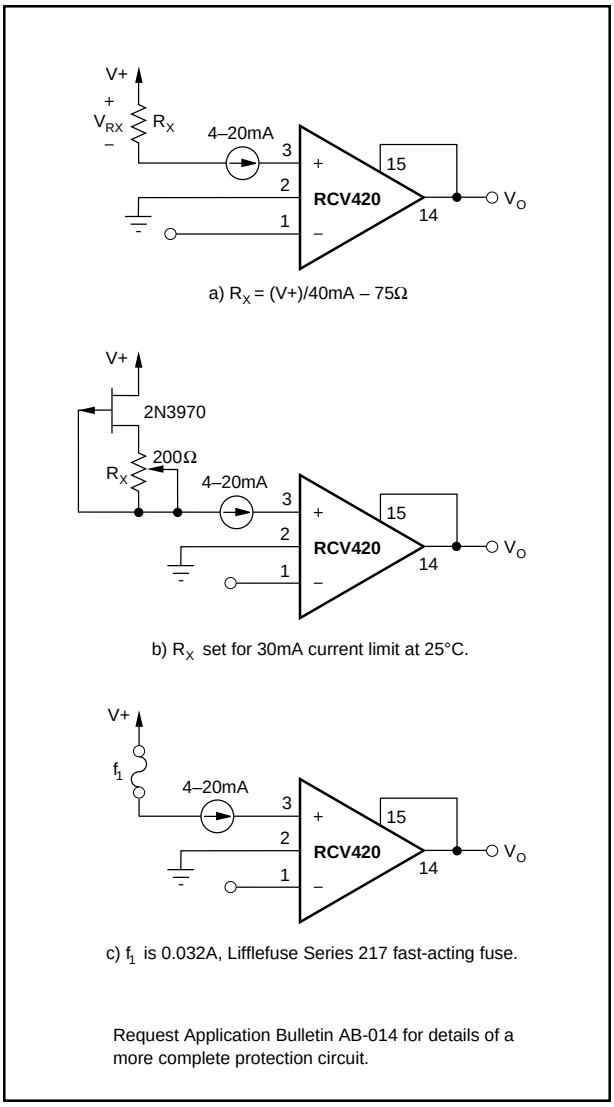


FIGURE 6. Protecting the Sense Resistors.

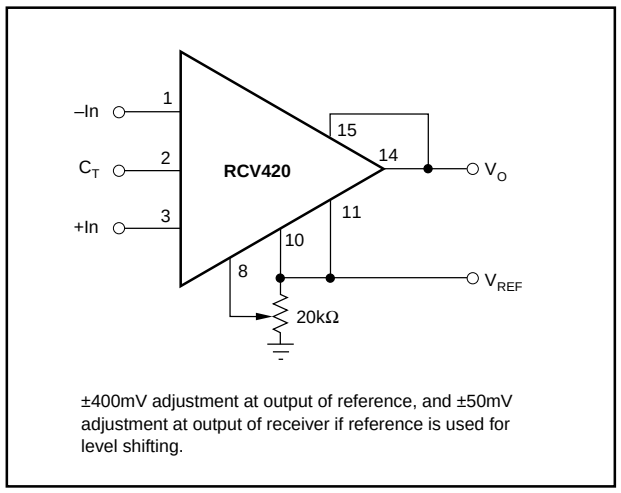


FIGURE 7. Optional Voltage Reference External Trim Circuit.

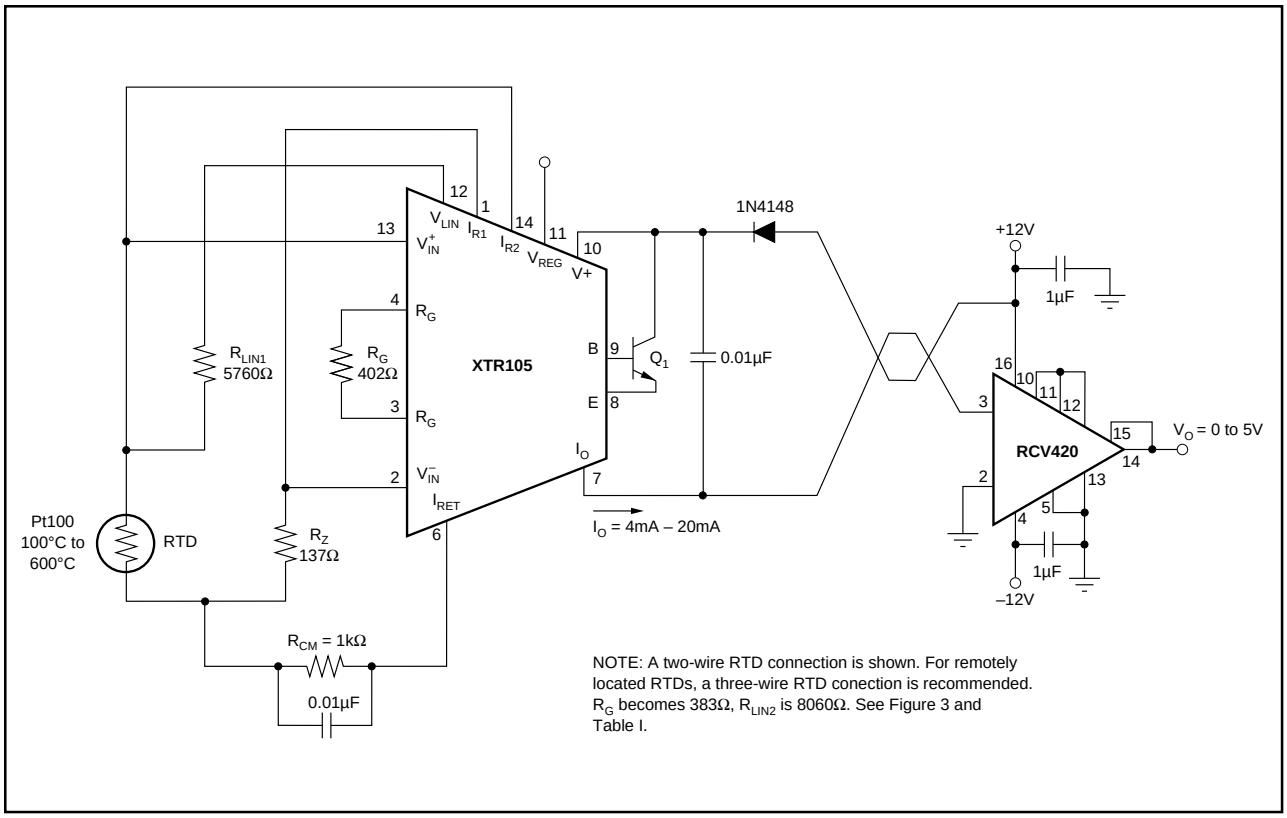


FIGURE 8. RCV420 Used in Conjunction with XTR101 to Form a Complete Solution for 4-20mA Loop.

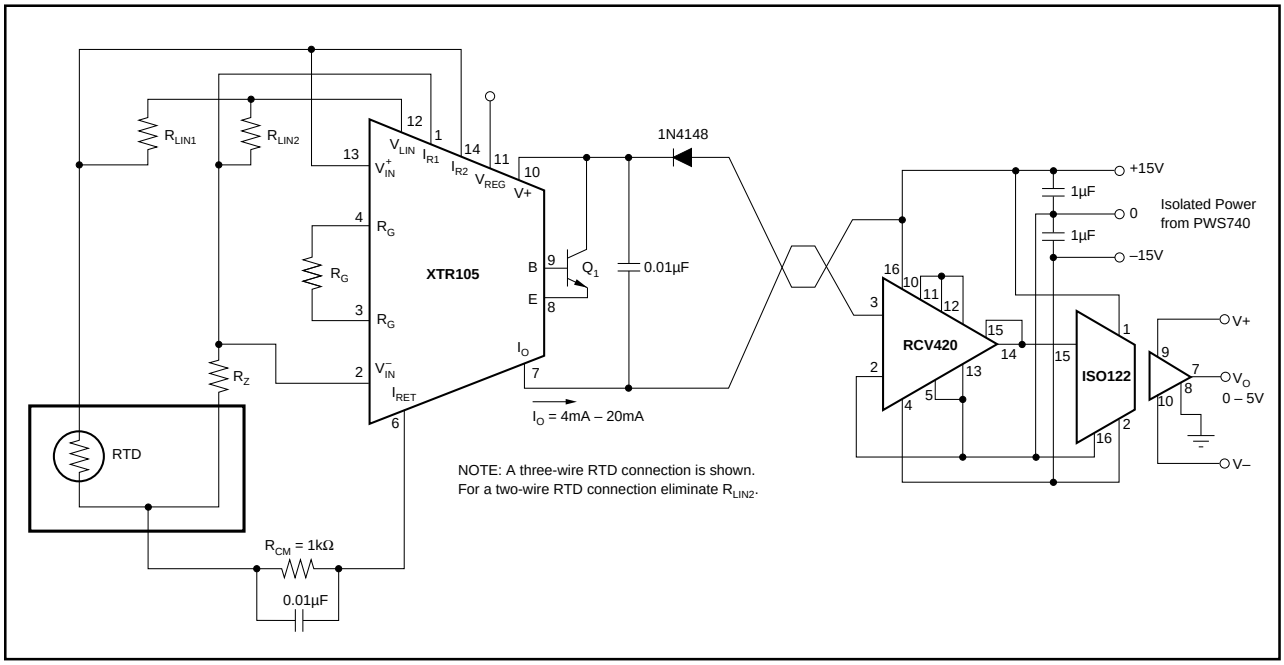
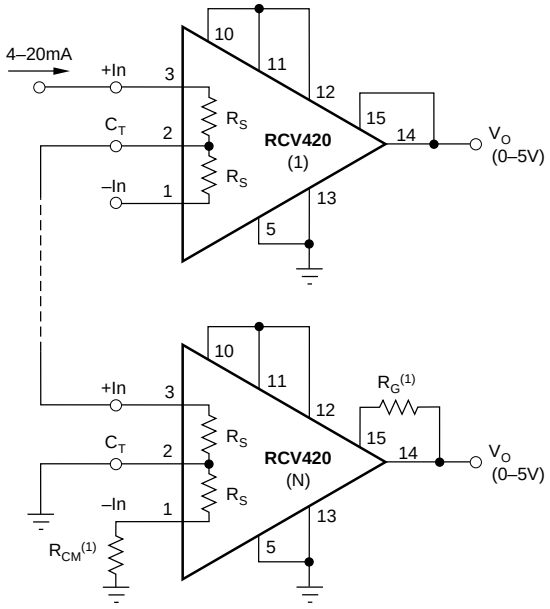


FIGURE 9. Isolated 4-20mA Instrument Loop (RTD shown).



NOTE: (1) R_{CM} and R_G are used to provide a first order correction of CMR and Gain Error, respectively. Table 1 gives typical resistor values for R_{CM} and R_G when as many as three RCV420s are stacked. Table II gives typical CMR and Gain Error with no correction. Further improvement in CMR and Gain Error can be achieved using a 500kΩ potentiometer for R_{CM} and a 100Ω potentiometer for R_G.

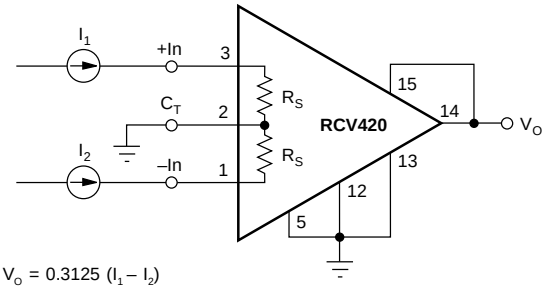
RCV420	R_{CM} (kΩ)	R_G (Ω)
1	∞	0
2	200	7
3	67	23

TABLE 1. Typical Values for R_{CM} and R_G.

RCV420	CMR (dB)	GAIN ERROR %
1	94	0.025
2	68	0.075
3	62	0.200

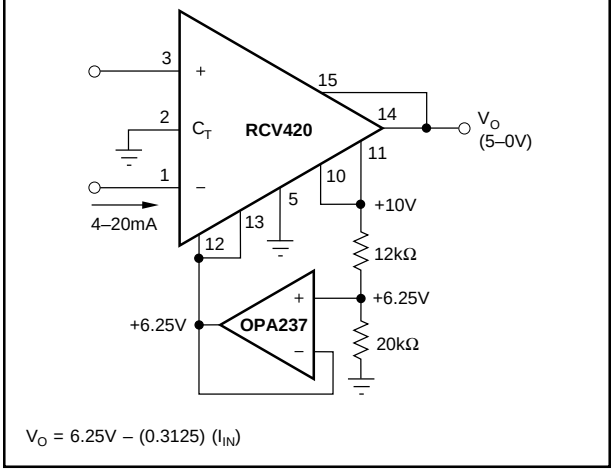
TABLE II. Typical CMR and Gain Error Without Correction.

FIGURE 10. Series 4-20mA Receivers.



$V_O = 0.3125\ (I_1 - I_2)$
Max Gain Error = 0.1% (RCV420BG)

FIGURE 11. Differential Current-to-Voltage Converter.



$V_O = 6.25V - (0.3125)\ (I_{IN})$

FIGURE 12. 4-20mA to 5-0V Conversion.

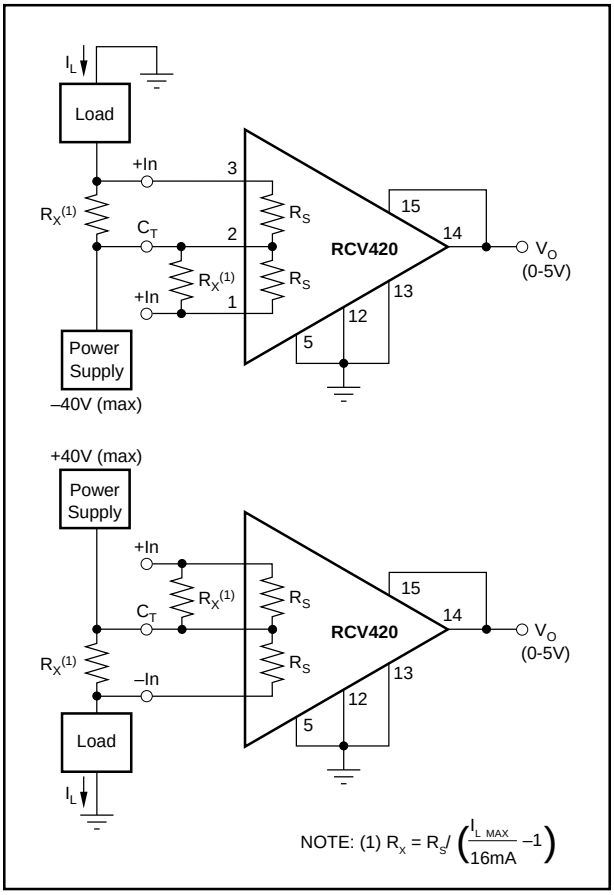
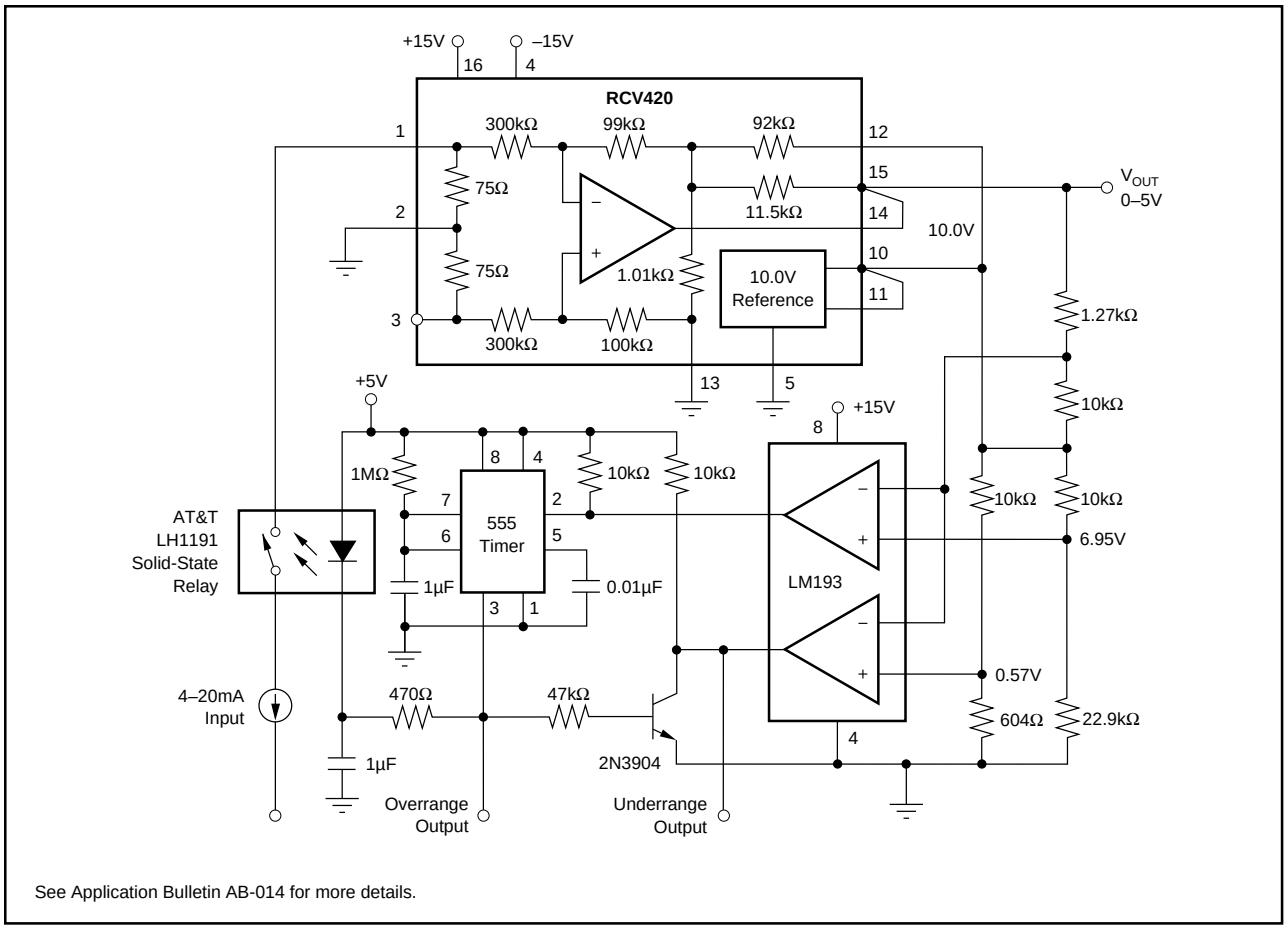
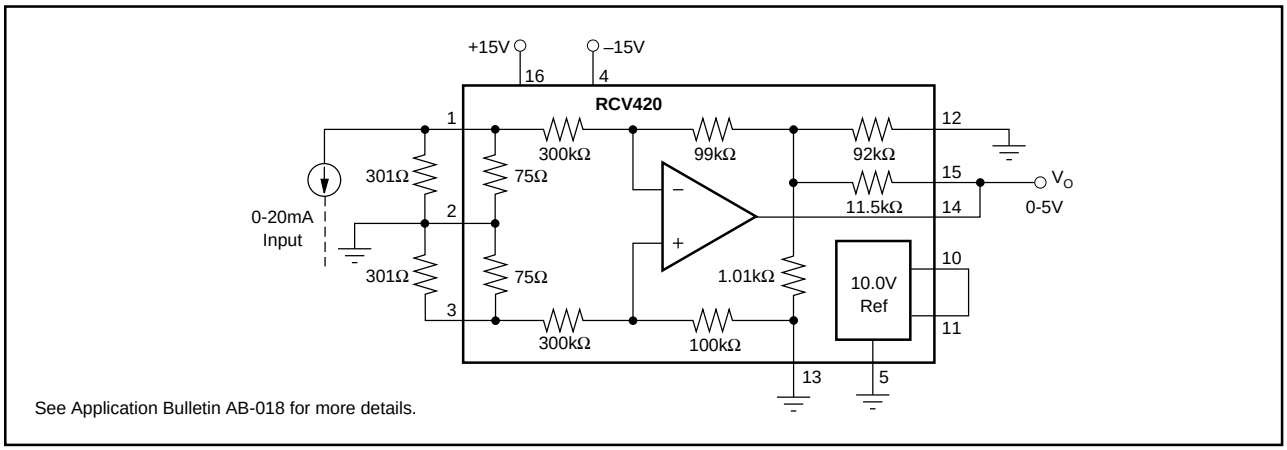


FIGURE 13. Power Supply Current Monitor Circuit.



See Application Bulletin AB-014 for more details.

FIGURE 14. 4-20mA Current Loop Receiver with Input Overload Protection.



See Application Bulletin AB-018 for more details.

FIGURE 15. 0-20mA/0-5V Receiver Using RCV420.

PACKAGING INFORMATION

Orderable part number	Status (1)	Material type (2)	Package | Pins	Package qty | Carrier	RoHS (3)	Lead finish/ Ball material (4)	MSL rating/ Peak reflow (5)	Op temp (°C)	Part marking (6)
RCV420JP	Active	Production	PDIP (N) | 16	25 | TUBE	Yes	Call TI	N/A for Pkg Type	0 to 70	RCV420JP
RCV420JP.A	Active	Production	PDIP (N) | 16	25 | TUBE	Yes	Call TI	N/A for Pkg Type	0 to 70	RCV420JP
RCV420KP	Active	Production	PDIP (N) | 16	25 | TUBE	Yes	Call TI	N/A for Pkg Type	0 to 70	RCV420KP
RCV420KP.A	Active	Production	PDIP (N) | 16	25 | TUBE	Yes	Call TI	N/A for Pkg Type	0 to 70	RCV420KP

(1) **Status:** For more details on status, see our product life cycle.

(2) **Material type:** When designated, preproduction parts are prototypes/experimental devices, and are not yet approved or released for full production. Testing and final process, including without limitation quality assurance, reliability performance testing, and/or process qualification, may not yet be complete, and this item is subject to further changes or possible discontinuation. If available for ordering, purchases will be subject to an additional waiver at checkout, and are intended for early internal evaluation purposes only. These items are sold without warranties of any kind.

(3) **RoHS values:** Yes, No, RoHS Exempt. See the TI RoHS Statement for additional information and value definition.

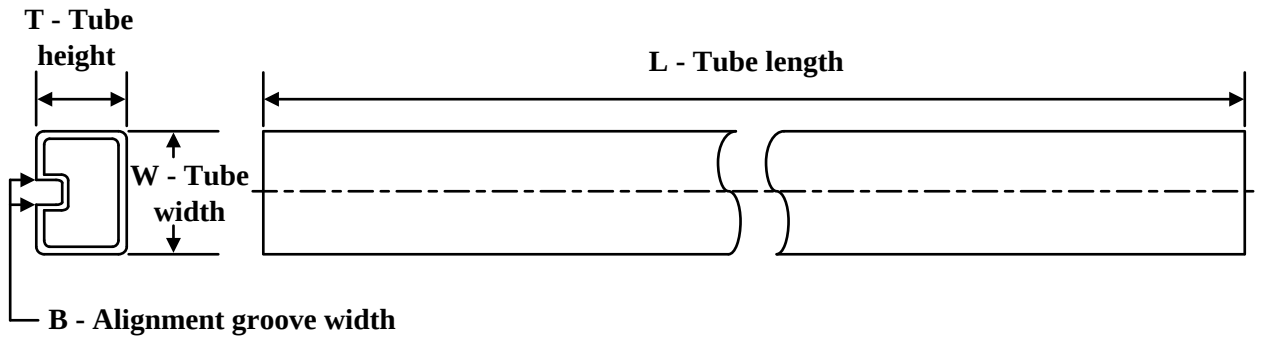
(4) **Lead finish/Ball material:** Parts may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

(5) **MSL rating/Peak reflow:** The moisture sensitivity level ratings and peak solder (reflow) temperatures. In the event that a part has multiple moisture sensitivity ratings, only the lowest level per JEDEC standards is shown. Refer to the shipping label for the actual reflow temperature that will be used to mount the part to the printed circuit board.

(6) **Part marking:** There may be an additional marking, which relates to the logo, the lot trace code information, or the environmental category of the part.

Multiple part markings will be inside parentheses. Only one part marking contained in parentheses and separated by a "~" will appear on a part. If a line is indented then it is a continuation of the previous line and the two combined represent the entire part marking for that device.

TUBE



*All dimensions are nominal

Device	Package Name	Package Type	Pins	SPQ	L (mm)	W (mm)	T (µm)	B (mm)
RCV420JP	N	PDIP	16	25	506	13.97	11230	4.32
RCV420JP.A	N	PDIP	16	25	506	13.97	11230	4.32
RCV420KP	N	PDIP	16	25	506	13.97	11230	4.32
RCV420KP.A	N	PDIP	16	25	506	13.97	11230	4.32

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