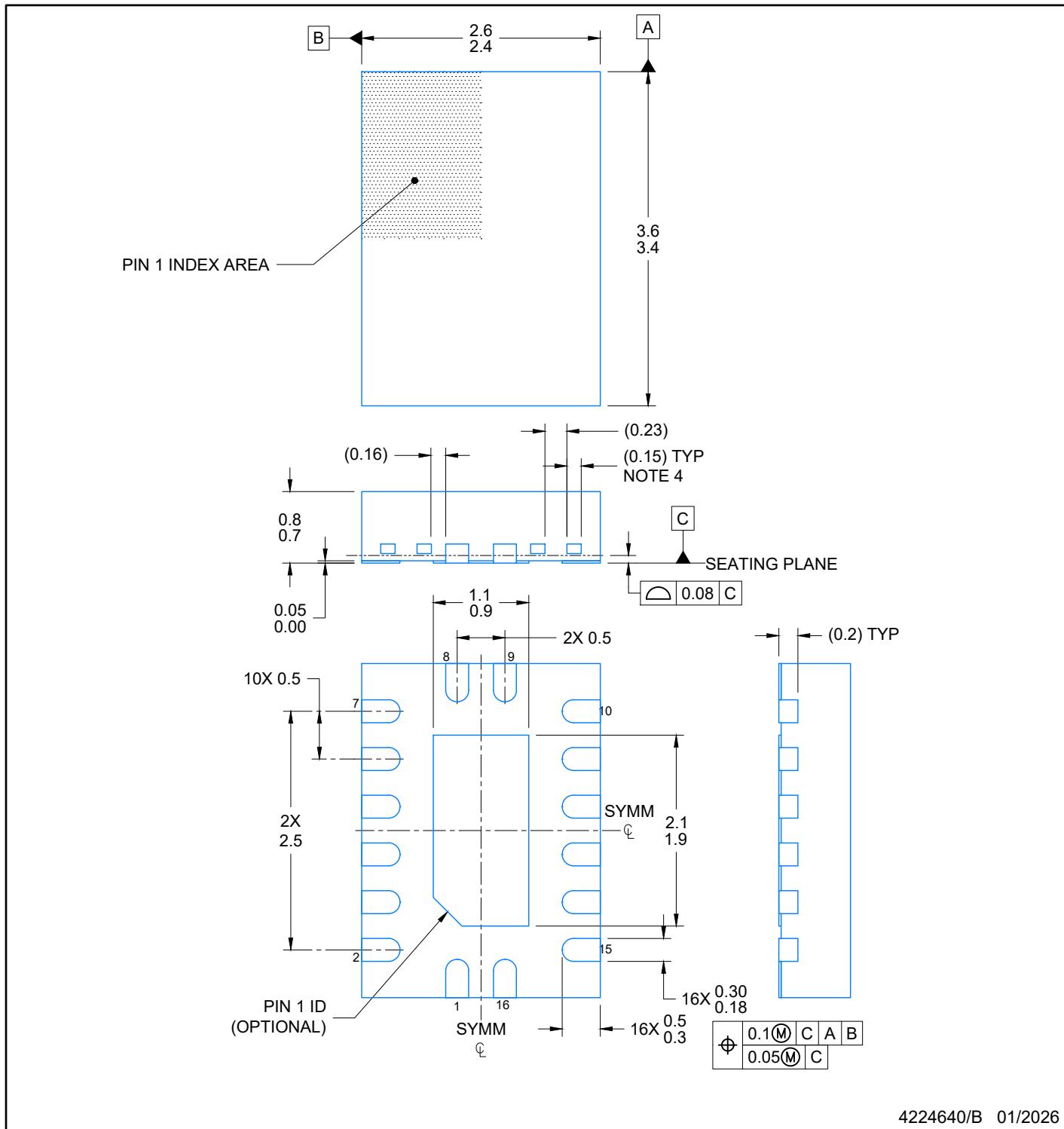


## PACKAGE OUTLINE

## **WQFN - 0.8 mm max height**

## PLASTIC QUAD FLAT PACK-NO LEAD

**BQB0016A**



## NOTES:

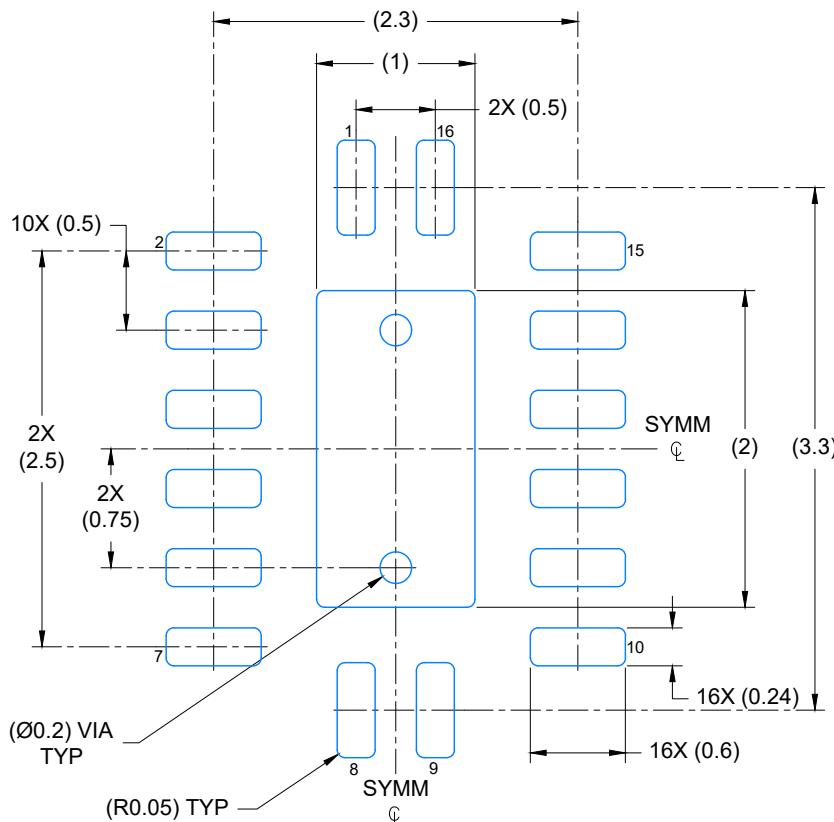
1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. The package thermal pad must be soldered to the printed circuit board for optimal thermal and mechanical performance.
4. Features may differ or may not be present

## EXAMPLE BOARD LAYOUT

## **WQFN - 0.8 mm max height**

## PLASTIC QUAD FLAT PACK-NO LEAD

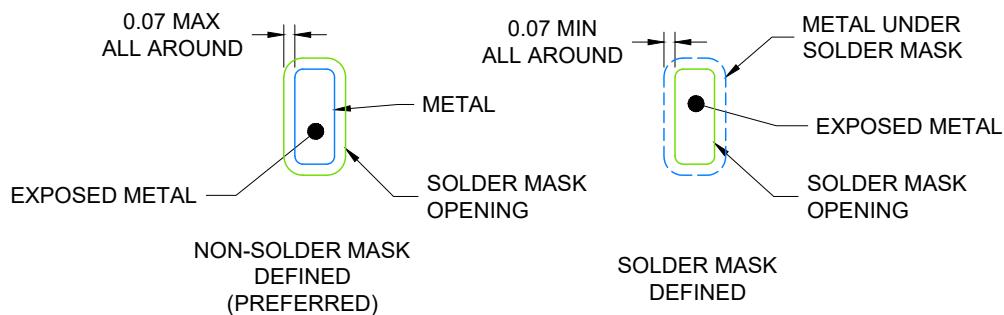
**BQB0016A**



## LAND PATTERN EXAMPLE

#### EXPOSED METAL SHOWN

SCALE: 20X



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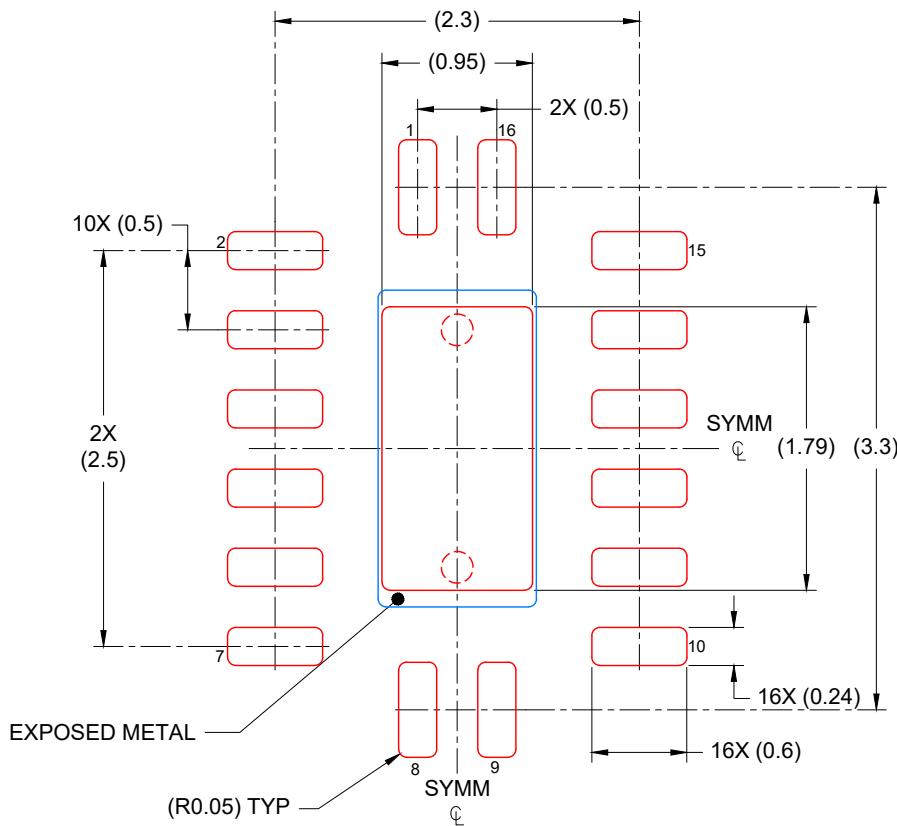
1. NOTES: (continued)
  5. This package is designed to be soldered to a thermal pad on the board. For more information, see Texas Instruments literature number SLUA271 ([www.ti.com/lit/slua271](http://www.ti.com/lit/slua271)).
  6. Vias are optional depending on application, refer to device data sheet. If any vias are implemented, refer to their locations shown on this view. It is recommended that vias under paste be filled, plugged or tented.

## EXAMPLE STENCIL DESIGN

## **WQFN - 0.8 mm max height**

## PLASTIC QUAD FLAT PACK-NO LEAD

**BQB0016A**



## SOLDER PASTE EXAMPLE BASED ON 0.125 mm THICK STENCIL

EXPOSED PAD  
85% PRINTED COVERAGE BY AREA  
SCALE: 20X

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#### NOTES: (continued)

7. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.

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