

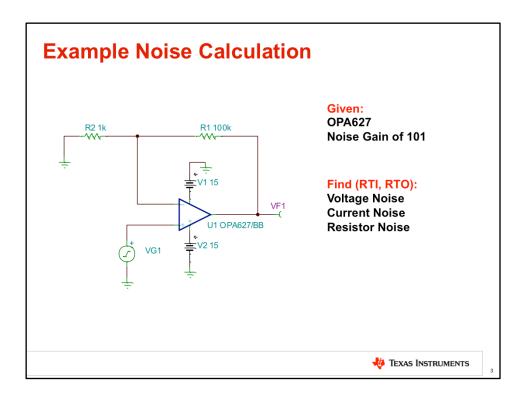
Hello, and welcome to the TI Precision Lab discussing intrinsic op amp noise, part 3.

In this video we'll continue the noise discussion by doing a full noise calculation for a simple amplifier circuit.

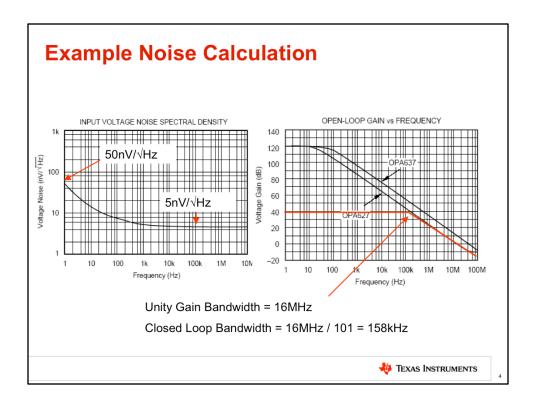
Tech	I <sub>Q</sub> (mA)	e <sub>n</sub> (nV/√Hz)	i <sub>n</sub> (fA/√Hz)	
Micropower CMOS	0.0008	220	1	
Micropower, Zero Drift CMOS	0.017	55	100	
Precision Bipolar	0.79	8	200	
A129 Ultra Low Bias Current CMOS		17	0.1	
Low Noise, Precision JFET	4.8	3.8	1 100 200	
Wide BW, ADC Drive CMOS	5.2	5.0	4.0	
Wide BW, ADC Drive Bipolar	3.6	2.0	3200	
High Speed	18.1	0.85	2700	
	Micropower CMOS Micropower, Zero Drift CMOS Precision Bipolar Ultra Low Bias Current CMOS Low Noise, Precision JFET Wide BW, ADC Drive CMOS Wide BW, ADC Drive Bipolar	Micropower CMOS 0.0008 Micropower, Zero Drift CMOS 0.017 Precision Bipolar 0.79 Ultra Low Bias Current CMOS 1.2 Low Noise, Precision JFET 4.8 Wide BW, ADC Drive CMOS 5.2 Wide BW, ADC Drive Bipolar 3.6	(mA)         (nV/√Hz)           Micropower CMOS         0.0008         220           Micropower, Zero Drift CMOS         0.017         55           Precision Bipolar         0.79         8           Ultra Low Bias Current CMOS         1.2         17           Low Noise, Precision JFET         4.8         3.8           Wide BW, ADC Drive CMOS         5.2         5.0           Wide BW, ADC Drive Bipolar         3.6         2.0	

Before we start to look at the hand calculation, however, let's take a look at a range of different amplifiers and the associated current and voltage noise. Voltage noise is closely related to the device's quiescent current. Voltage noise and quiescent current are inversely proportionate, so amplifiers with high quiescent current tend to have lower noise. For example, comparing the OPA349 to the OPA333 you can see that the amplifier with higher quiescent current has lower noise. Furthermore, Bipolar amplifiers tend to have lower noise then CMOS amplifiers for a given current. For example, compare the OPA350 CMOS amplifier to the OPA211 bipolar amplifier. Notice that the bipolar amplifier has lower noise then the CMOS amplifier even though the quiescent current is higher.

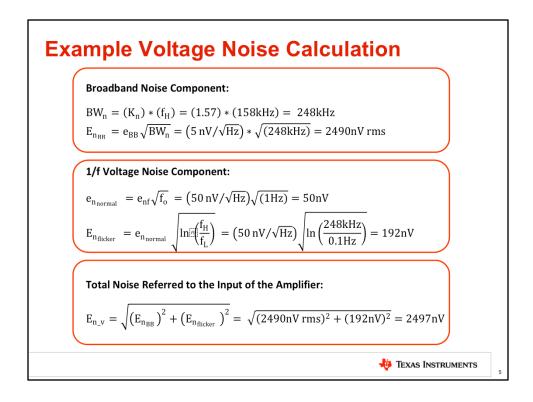
Current noise, on the other hand, is not related to quiescent current. Current noise is lower for CMOS amplifiers than for Bipolar amplifiers. Generally, you will notice amplifiers that have low bias current also have low current noise. This table gives examples that represent the extreme range of noise values for amplifiers. In other words, most amplifiers be in the range of hundreds of nV/rtHz to one or fewer nV/Hz for voltage noise, and thousands of fA/rtHz to one or less fA/rtHz.



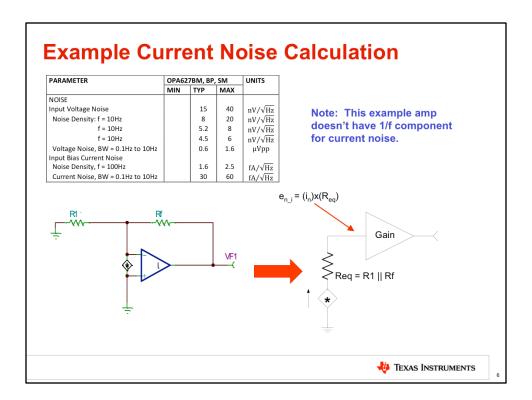
In this calculation, we will examine an OPA627 in a non-inverting configuration with a gain of 101 V/V. The total noise at the output will be the sum of opamp voltage noise, op-amp current noise, and resistor noise. We will consider both the 1/f region and the broadband region in the spectral density curve. We will also have to consider the noise bandwidth and the noise gain of the circuit.



The left hand curve is the voltage spectral density curve. Remember from earlier videos that it has a 1/f and broadband region. The right hand curve is the open loop gain (AOL) curve. The bandwidth of the our circuit is determined by the AOL curve because there is no other filter. Dividing the OPA637's unity gain bandwidth of 16MHz by our gain of 101, we get a closed-loop bandwidth of 158kHz. This can also be seen graphically.



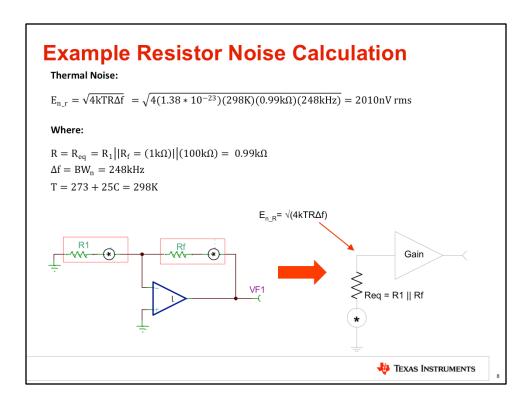
Now that we have learned all the equations for the op-amp voltage noise in the previous videos, let's compute it for this example. Inspection of the results shows that the 1/f noise component of 192nV is not significant in this example compared to the broadband noise of 2490nV. This is fairly typical of wide bandwidth examples. Also note that the results are added using root sum of squares, for a total inputreferred noise of 2497nV.



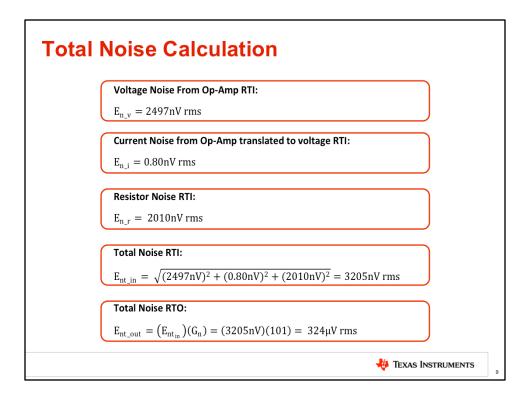
Now that we have the op-amp voltage noise component, let's compute the effect of current noise. For a non-inverting amplifier, the current noise flows through the parallel combination of R1 | Rf. This relationship can be derived using the same op amp analysis techniques that would be used for a dc current source. The current noise is multiplied by the equivalent resistance to generate an input referred noise voltage. Let's look at the numbers for this example.

	PARAMETER	OPA62	7BM, BP	UNITS		
		MIN	TYP	MAX		
	NOISE					
	Input Voltage Noise		15	40	nV/√Hz	
	Noise Density: f = 10Hz		8	20	nV/√Hz	
	f = 10Hz		5.2	8	nV/√Hz	
	f = 10Hz		4.5	6	nV/√Hz	
	Voltage Noise, BW = 0.1Hz to 10Hz		0.6	1.6	μVpp	
	Input Bias Current Noise					
	Noise Density, f = 100Hz		1.6	2.5	fA/√Hz	
	Current Noise, BW = 0.1Hz to 10Hz		30	60	fA/√Hz	
e <sub>n_i</sub> =	$(i_{\text{nBB}})(R_{\text{eq}}) = (1.6 \text{fA}/\sqrt{\text{Hz}})$	(0.9	9kΩ)	= 0.0	016 nV/	$\sqrt{\text{Hz}}$
Conve	ert Spectral Density to rms:					
_	$e_{n r} \sqrt{BW_n} = (0.0016 \text{ nV}/\sqrt{V_n})$	$\overline{\text{Hz}}$ $\sqrt{2}$	248kF	$\overline{\text{Iz}} = 0$	).80nV r	ms
E <sub>n_i</sub> =						

Here are the numbers. For this example the noise current density is very small, at just 1.6 fA/rtHz. The equivalent input resistance is also small, at approximately 1k. Multiplying these together, we get an extremely small noise voltage density of 0.0016nV/rtHz. Converting to RMS using the noise bandwidth, we get 0.8 nVrms. For all practical purposes we could neglect this number since it is insignificant compared to the voltage noise of 2497nV, but we will include it for the sake of completeness. Later we will see an example where current noise dominates.

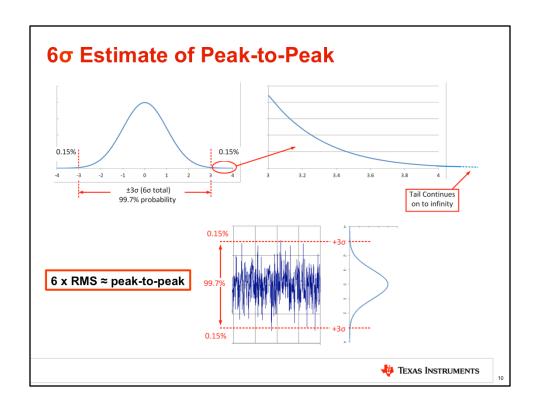


Let's now finish up with calculating the circuit's resistor thermal noise, also called Johnson noise. We use the equivalent resistance to do this, which is just under 1k in our example. After plugging our values into the equation from the previous video, we get a result of 2010nVrms. This is a significant amount of noise!

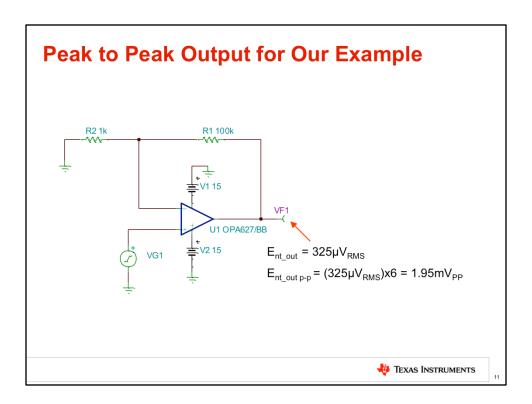


Now that we have the op amp voltage noise, the op amp current noise translated to voltage, and the op amp resistor noise, we can add them together using the root sum of squares. This gives us the total output noise voltage in RMS. Notice that the current noise does not contribute significantly to the total noise. The OPA627 is a JFET input op amp, which typically have very low input current noise densities.

In this example, the total input-referred noise calculates out to 3205nVrms. Multiplying by the gain of 101, we get an output noise voltage of 324uVrms. Frequently engineers want to know the peak-to-peak noise. How do we compute this?



Multiplying the rms noise by 6, or even 6.6, is a common estimate for peak-to-peak noise. Remember that noise has a Gaussian distribution. The Gaussian distribution tells us that there is a 99.7% probability that any reading in time is within the limits of +/- 3 standard deviation or 6 standard deviations total. This means that there is a finite probability of 0.3% that a noise reading will be outside of this limit. Sometimes 6.6 standard deviations is used, because the probability of noise being inside of the limits is increased to 99.9%. It is important to realize that the tails of the Gaussian distribution extend infinitely, so there is no number of standard deviations that will produce a 100% probability that all noise is inside of the bounds. Thus, 6 or 6.6. are used as good estimates. One final thing to keep in mind is that RMS and standard deviation are equivalent for noise signals with no mean value. This is generally true for the intrinsic noise that we are considering.



Multiplying the RMS output by 6 gives us the estimate of peak-to-peak output. In this example the peak-to-peak estimate is 1.95mVpp. In later videos we will simulate and measure this circuit with the same results.



That concludes this video – thank you for watching! Please try the quiz to check your understanding of this video's content.



1.	Which	type	of	amplifier	has	the	lowest	current	noise?	)
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- a. CMOS
- b. Bipolar
- 2. (T/F) Current noise is related to an amplifiers quiescent current.
- a. True
- b. False
- 3. (T/F) Voltage noise is related to an amplifiers quiescent current.
- a. True
- b. False
- 4. Amplifiers with high quiescent current have \_\_\_\_\_.
- a. High voltage noise.
- b. Low voltage noise.
- c. Quiescent current has no relationship to noise.

- 5. Why isn't there an exact relationship between rms and peak-to-peak?
- a. Because noise constantly changes.
- b. Because a dc offset can affect the rms value.
- c. The Gaussian curve tails introduce a small probability of a peak outside the 6 sigma limit.

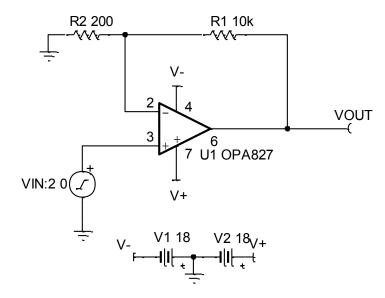


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- 1. Find the total noise due to:
  - a. Resistance
  - b. Op amp broadband voltage noise
  - c. Op amp 1/f voltage noise
  - d. Op amp current noise
  - e. Compute the total combined noise.





### 1. Find the total noise due to:

#### a. Resistance

$$\begin{split} G_n &= \frac{R_f}{R_1} + 1 = \frac{10 k \Omega}{200 \Omega} + 1 = 51 \\ f_c &= \frac{GBW}{G_n} = \frac{22 MHz}{51} = 431 kHz \\ BW_n &= K_n \cdot f_c = (1.57)(431 kHz) = 677 kHz \\ E_N &= \sqrt{4k \cdot T \cdot R \cdot BW_n} = \sqrt{4(1.38 \cdot 10^{-23} \, \text{J/K})(298 \text{K})(200 \Omega)(677 \text{kHz})} = 1.49 \mu \text{V} \end{split}$$

## b. Op amp broadband voltage noise

$$E_{nBB} = (e_n)\sqrt{BW_n} = (3.8 \text{ nV}/\sqrt{Hz})\sqrt{(677 \text{kHz})} = 3.13 \mu\text{V rms}$$

### c. Op amp 1/f voltage noise

$$\begin{split} e_{n_{normal}} &= e_{nf} \sqrt{f_o} = \left(60 \, \text{nV} / \sqrt{\text{Hz}}\right) \sqrt{0.1 \text{Hz}} = 19 \text{nV} \\ E_{n_{flicker}} &= e_{n_{normal}} \, \sqrt{\ln \left(\frac{f_H}{f_L}\right)} = (15.8 \text{nV}) \sqrt{\ln \left(\frac{677 \text{kHz}}{0.1 \text{Hz}}\right)} = 75.3 \text{nV rms} \end{split}$$

### d. Op amp current noise

$$\begin{array}{l} e_{ni} = R_{eq} \cdot i_n = (196\Omega) \big( 2.2 \, fA/\sqrt{Hz} \big) = \, 0.00043 \, nV/\sqrt{Hz} \\ E_{ni} = \, e_{ni} \sqrt{BW_n} \ = \big( 0.00043 \, nV/\sqrt{Hz} \big) \sqrt{677 kHz} = 0.353 nV \, rms \end{array}$$

### d. Total combined noise

$$\begin{split} E_{n\_total} &= \sqrt{{E_{nr}}^2 + {E_{nBB}}^2 + {E_{nflicker}}^2 + {E_{ni}}^2} \\ E_{n\_total\_in} &= \sqrt{(1.477\mu V)^2 + (3.13\mu V)^2 + (75.3nV)^2 + (0.353nV)^2} = 3.46\mu V \text{ rms} \\ E_{n\_total\_out} &= G_n E_{n\_total\_in} = (51)(3.56\mu V) = 177\mu V \text{ rms} \\ E_{n\_total\_out\_pp} &= 6E_{n\_total\_out} = 6(177\mu V) = 1.06mVpp \end{split}$$