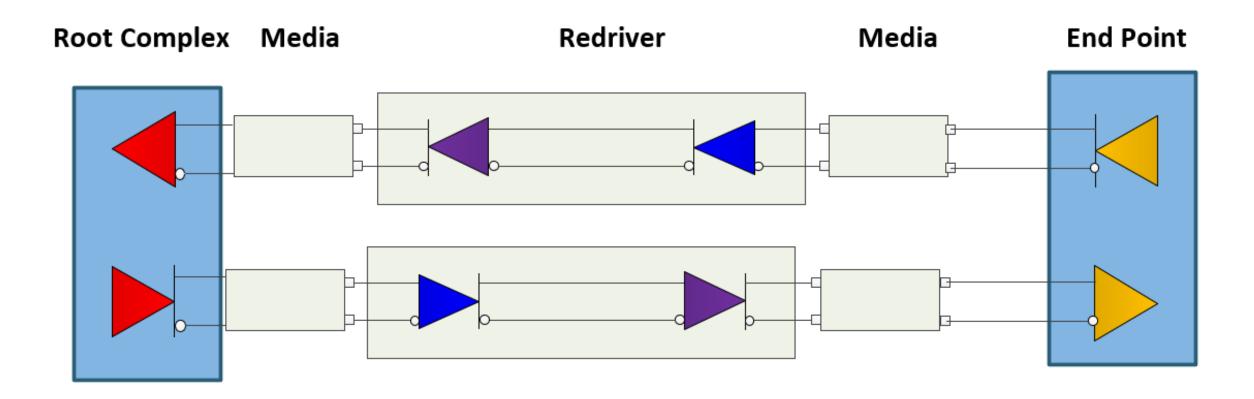
Retimer vs. Redrivers in PCIe

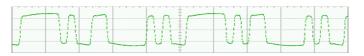
TI Precision Labs – PCIe

Presented by Nicholaus Malone Prepared by Nasser Mohammadi





Signal waveform before transmission media

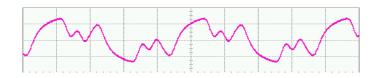




Signal waveform before transmission media

Signal waveform at the end of the transmission media





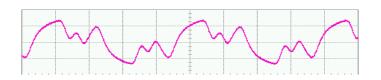


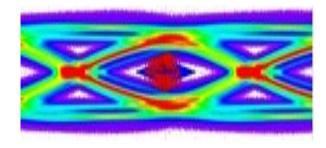
Signal waveform before transmission media

Signal waveform at the end of the transmission media

Eye closure at the end of the transmission media









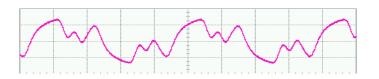
Signal waveform before transmission media

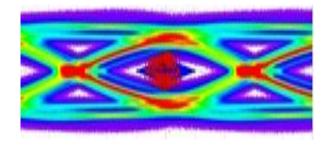
Signal waveform at the end of the transmission media

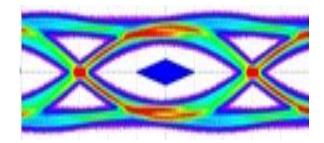
Eye closure at the end of the transmission media

Equalized eye diagram



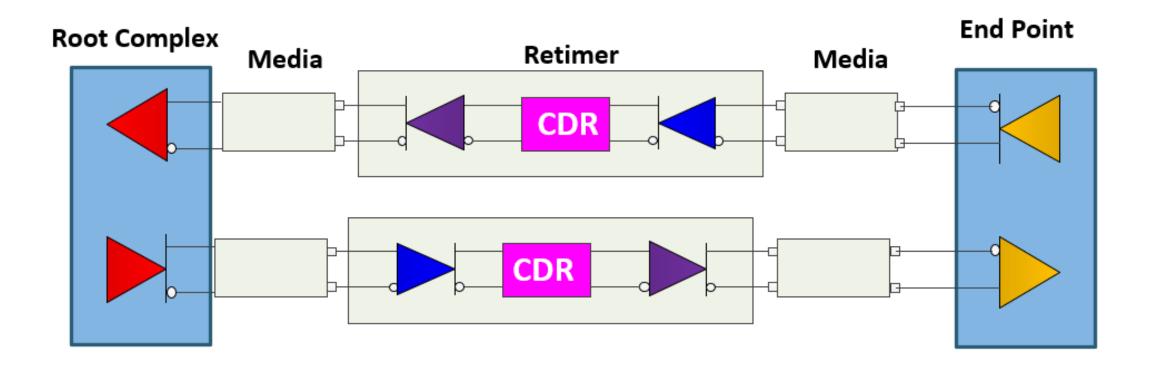






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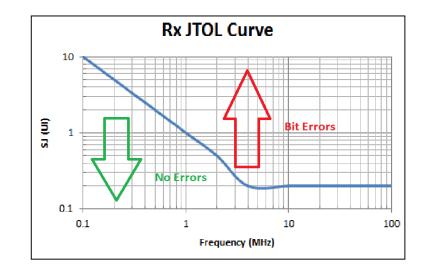
How does a retimer work?

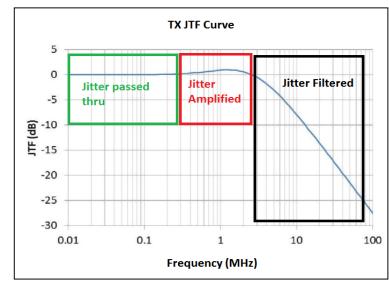




Retimer jitter tolerance

- Retimers compensate for random and deterministic:
 - Too much random jitter will result in bit errors
 - Retimer periodic and sinusoidal jitter tracking capability: Too much Pj/Sj will also produce errors
- Retimer's output is not jitter free
 - There will be some random jitter. If the retimer's own jitter is not superior to the source, it may cause link issues
 - Jitter Transfer Function
 - There will be some deterministic jitter from input passed to output
 - It is possible for jitter to be amplified







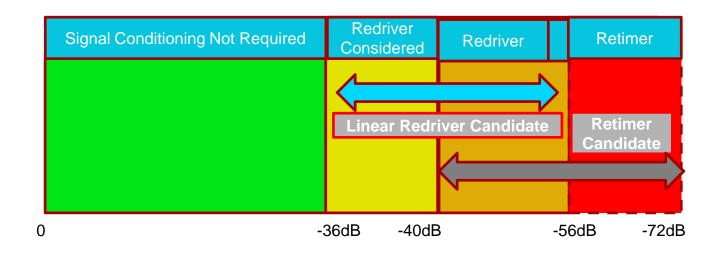
Redrivers vs Retimers implementations

Feature	Redriver	Retimer	Comments
Board design/ implementation cost	Low	High	 Redriver is a simple component acting like wire in the link Retimer higher cost, requires reference clock, and larger footprint Retimer may require thermal management to meet long term reliability
Random jitter (RJ) cleaning	No	Yes	 Redrivers can not clean RJ and thus does not reset jitter budget Retimers can clean RJ and should to have superior performance compared to source
Deterministic jitter (DJ) cleaning	Yes (ISI)	Yes	 Redrivers typically only clean ISI (optionally cleans DCD & PJ with analog DFE) Retimer cleans ISI, DCD & PJ
Real life functional Inter-op	Good	Mixed	 Linear redriver's simplistic ISI jitter cleaning provides best inter-op Retimer provides very powerful signal integrity tools but often its complexity brings inter-op issues Retimers and linear redrivers work well with end-to-end DP link training
Latency	Low	High	High latency can cause inter-op issues



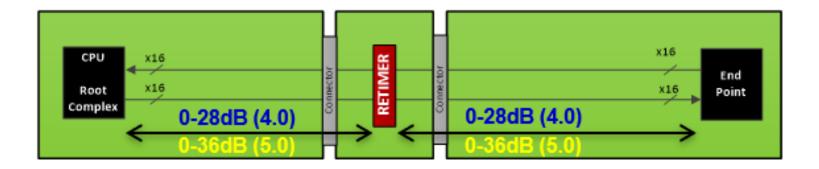
Gen5 Link Budget Feasibility

- Per PCIe specification, root complex and end point should be able to achieve Gen5 error free operation at up to -36dB total channel loss
- In some cases total channel loss is higher than recommended (-36dB)
- Many applications have a total channel loss between (-40 to -56dB)
- In a case where we have over -56dB loss, a retimer could the only option



Retimer placement guideline

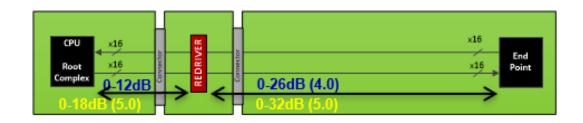
- Per PCIe specification:
 - At Gen4, retimer should be able to compensate for 28dB Nyquist signal loss
 - At Gen5, retimer should be able to compensate for 36dB Nyquist signal loss





Placement options for redriver placement

• Near root complex redriver



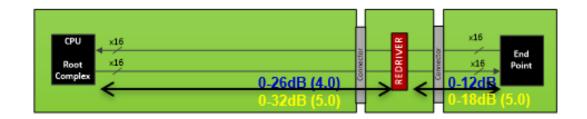


Placement options for redriver placement

• Near root complex redriver



• Near end point redriver



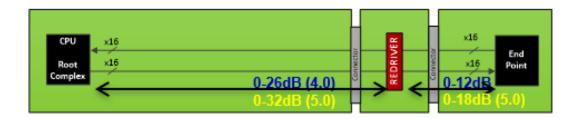


Placement options for redriver placement

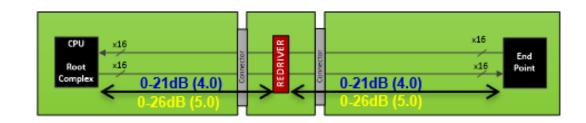
• Near root complex redriver



• Near end point redriver

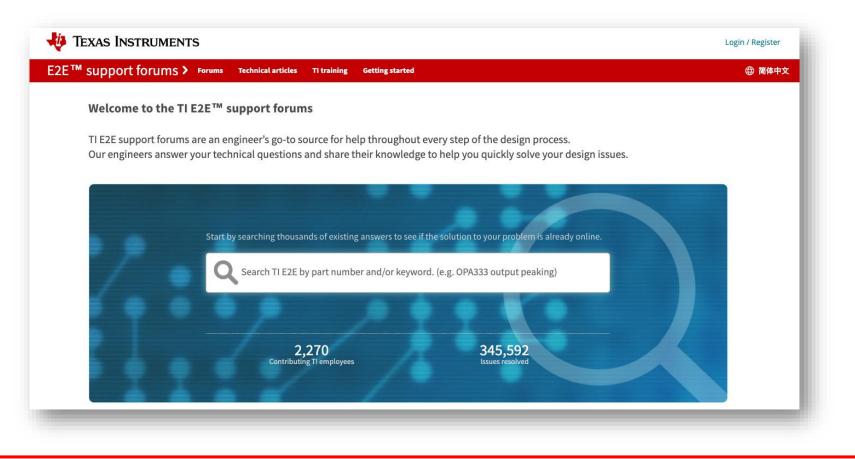


• Mid-link placement



Thank you

<u>Texas Instruments Precision Labs Website</u>







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