EVM User's Guide: TCAN24XXEVM

Evaluation Module with Buck Regulator Output, WAKE/ID, HSS, LIMP, and CAN Transceiver

Description

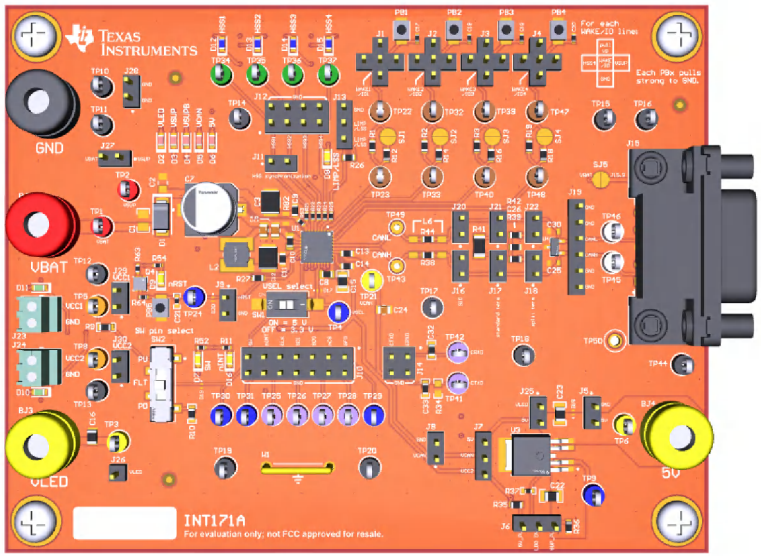
The TCAN24XXEVM helps designers evaluate device performance, support fast development, and analyze automotive Controller Area Network (CAN) systems using the TCAN24xx-Q1 family of CAN system basis chips (SBCs), which include integrated 1A buck regulator output (3.3V or 5V), LDO regulator output (5V), WAKE/ID functionality, HSS, watchdog, and LIMP, along with optional partial networking and Signal Improvement Capable (SIC) capability.

Features

- Buck regulator interface with selectable 3.3V or 5V output capability.
- EMI filtration option for input VSUPB voltage supply.
- Onboard external 5V LDO supply for easy second-supply evaluation.
- VCC output terminals for SBC power regulators.
- CAN FD physical layer with selectable termination and protection options.
- High-side switch interfaces and synchronization option.
- nRST button input and LIMP/LSS output.
- Versatile WAKE/ID functionality and mapping.
- Digital interface input/output connection.
- Multiple onboard connections and options for easy device configuration.
- Indicator LEDs for supplies, outputs, and logic signals.

Applications

- Body electronics and lighting
- Car access and security
- Hybrid, electric and powertrain systems
- Industrial transportation



TCAN24XXEVM (Top View)

1 Evaluation Module Overview

1.1 Introduction

This EVM user's guide describes the TCAN24XXEVM evaluation module (EVM). The previous image shows the TCAN24XXEVM including all populated components, without any shunts on the headers.

1.2 Kit Contents

This kit contains 1 unit of TCAN24XXEVM. By default, the EVM is populated with TCAN2451MRHBRQ1 at position U1.

2 Hardware

2.1 Additional Images

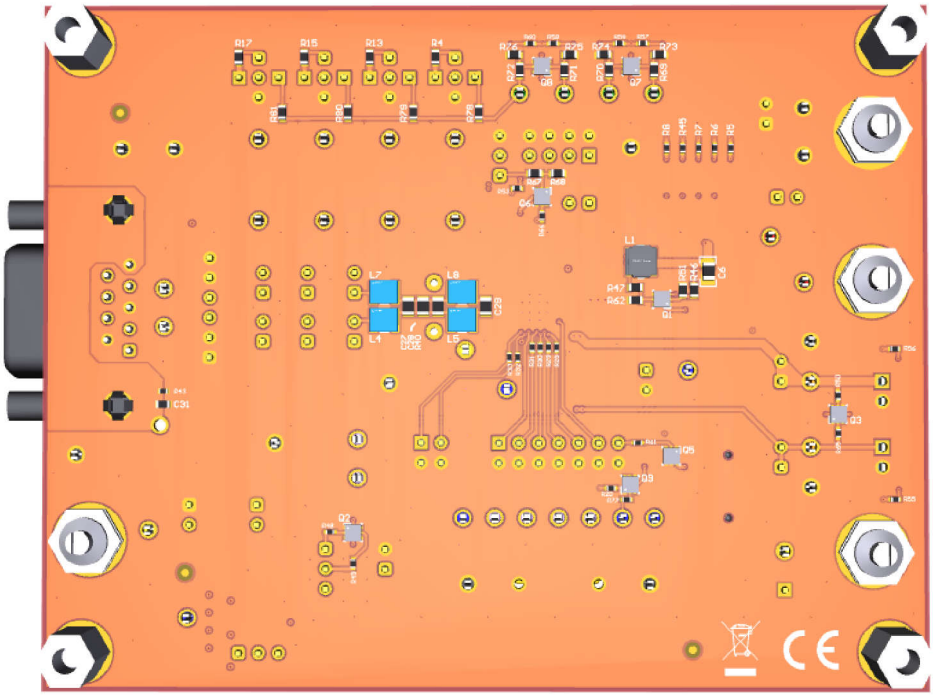


Figure 2-1. TCAN24XXEVM (Bottom View)

2.2 Power and Integrated Regulators

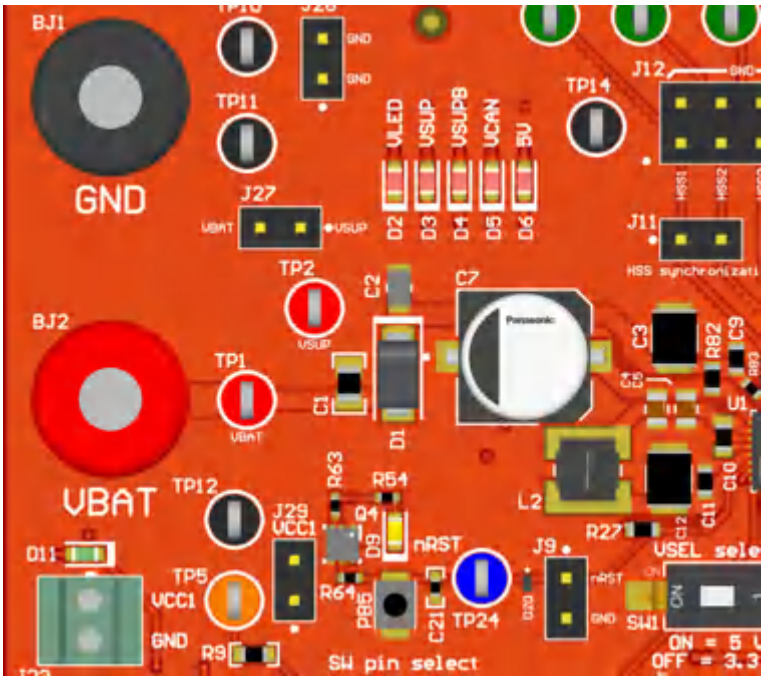


Figure 2-2. VBAT and GND Connections

VBAT is the external power supply for the board (typically 12V). VBAT can be connected to the DC Barrel Jack BJ2. After VBAT passes through a reverse-polarity protection diode, VBAT is called VSUP. The polarity protection diode can be bypassed by connection the input power directly to TP2.

VSUP and VSUPB are separated by an EMI pi-filter. To bypass the EMI filter, install a zero-ohm resistor at position R82.

VHSS and VSUP are connected together on the EVM via a zero-ohm resistor at position R83 and can be separated by removing R83.

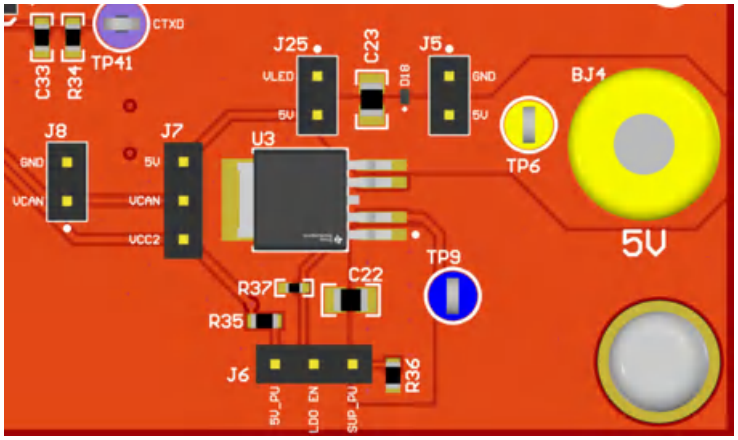


Figure 2-3. 5V LDO Regulator U3

VSUP supplies a 5V LDO that is a utility voltage for the board peripherals, such as LEDs, and can also supply VCAN. Using J6, this LDO can either be disabled or enabled. The enable signal source is easily selectable as either a pull-up to VCC2 or a pull-up to VSUP. The 5V rail is available on header J5. If the LDO is disabled, the 5V rail can be externally supplied via J5 or BJ4.

VCAN can be shunted to the 5V LDO or to VCC2 using J7.

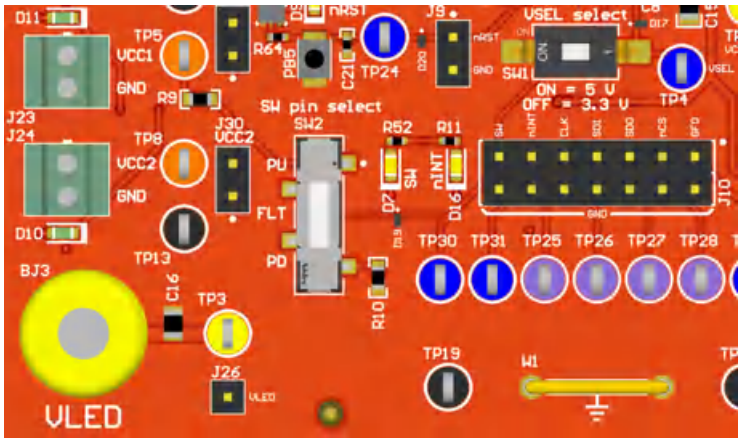


Figure 2-4. VCC1, VCC2, VLED, and VSEL

VSEL can be connected to ground by flipping the switch to the ON position. In the OFF position, the VSEL pin is floating, resulting in a 3.3V output from VCC1.

VCC1 integrated buck regulator output from TCAN24xx-Q1 is available on J23 or TP5. The VCC2 integrated LDO regulator output from TCAN24xx-Q1 is available on J24 or TP8.

Each power rail has an LED indicator that is sourced through the 5V LED input supply, which can be connected to BJ3 if not being sourced from BJ4 or U3 via the J25 header.

Note

Without VLED present, the onboard LED indicator circuits for high-voltage signals VSUP, VSUPB, HSS1, HSS2, HSS3, HSS4, and LIMP/LSS sink a small amount of current from the respective signals due to voltage divider circuits. To disconnect these circuits, remove the following resistors:

Table 2-1. LED Voltage Divider Resistors

Signal Rail	Resistor
VSUP	R46
VSUPB	R47
HSS1	R69
HSS2	R70
HSS3	R71
HSS4	R72
LIMP/LSS	R67

2.3 CAN FD

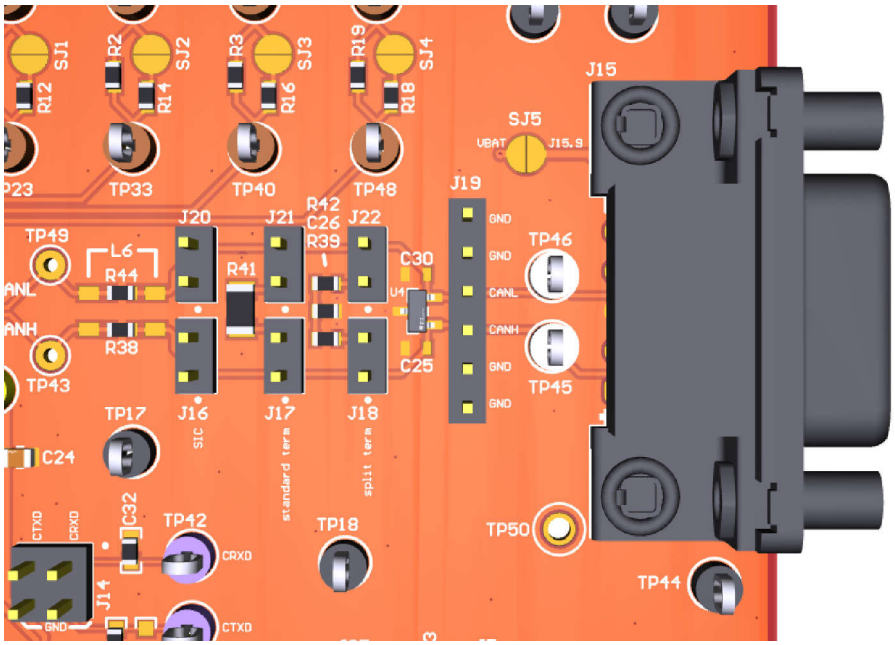


Figure 2-5. CAN FD Circuitry

The CAN FD transceiver has a configurable interface circuit consisting of optional features:

- Paired zero-ohm resistors that can be replaced with a common-mode choke.
- Single 120Ω termination or split termination. Use both for single-board operation bu placing shunts on headers J17, J18, J21, and J22.
- Filter capacitors (not populated by default).
- ESD diode ESD2CAN24-Q1.
- Monitor header (J19).
- DB9 wiring harness connector (J15).
- VSUP connection option through SB9 connector by closing solder joint SJ5.

2.4 High-Side Switches

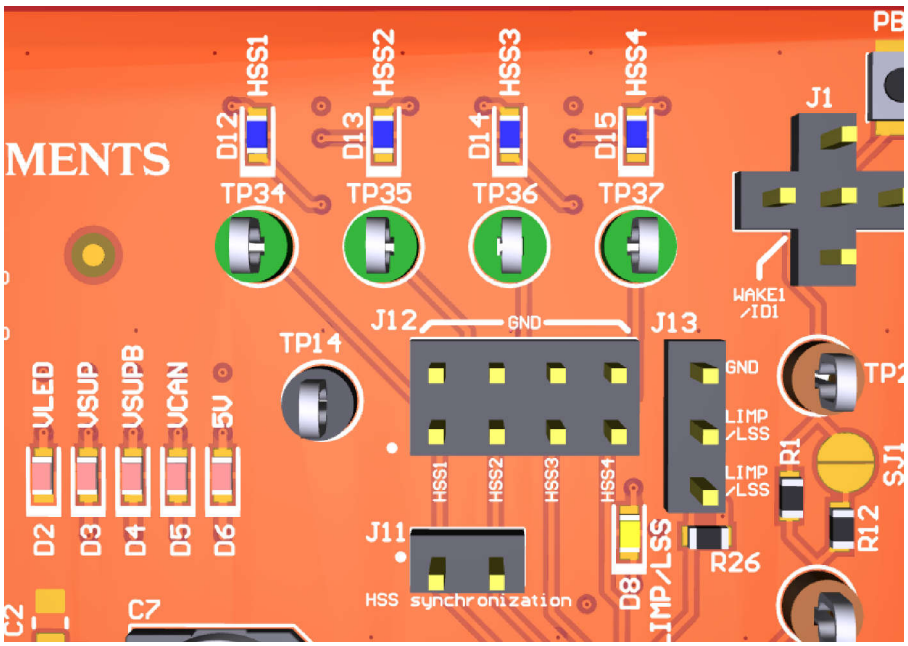


Figure 2-6. High-Side Switch Circuitry

The high-side switches are available on header J12. Each switch has an LED indicator.

HSS1 and HSS2 can be synchronized and configured to share a larger current load by placing a shunt on header J11. Some register configuration of the TCAN24xx-Q1 is required for load sharing. See the TCAN24xx-Q1 data sheet for additional information.

HSS4 output is also made available near the WAKE/ID pins to enable cyclic sensing wake for any of the wake pins.

2.5 nRST and LIMP/LSS

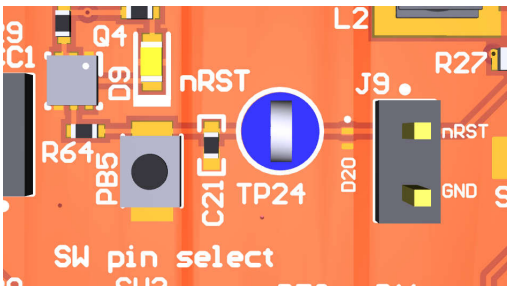


Figure 2-7. nRST Button and Connector

The nRST pin is a dual-use pin that functions as an input pin for an external reset signal or as a VCC1 monitor output to indicate under-voltage and reset events. An external signal can be applied through header J9 or by pressing pushbutton PB5.

An LED indicator lights up during reset events.

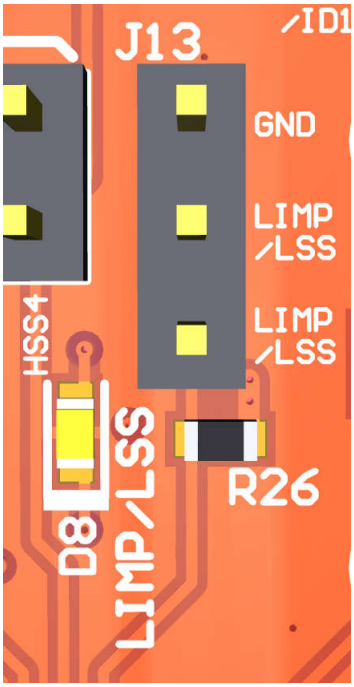


Figure 2-8. LIMP/LSS

The LIMP/LSS output is available on header J13 and pulled-up to VSUP via a 10kΩ resistor on the board. An LED indicator lights up to indicate LIMP activation or to show LSS output.

2.6 WAKE/ID Function

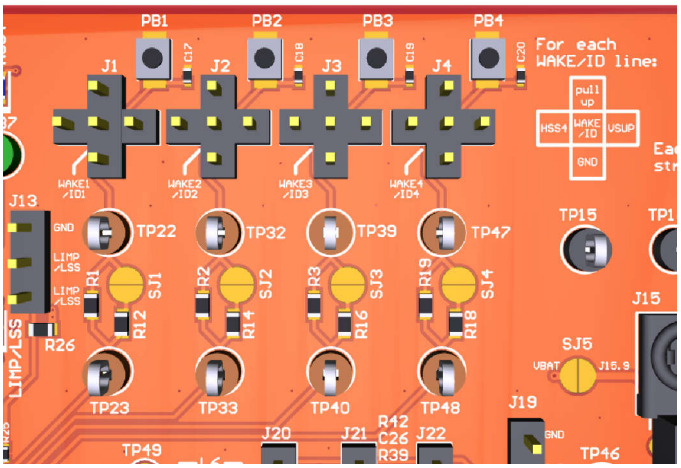


Figure 2-9. WAKE, ID Circuits and Connections

Each WAKE/ID input circuit is shown above. Each pin can be configured either as a WAKE pin or as an ID pin on the board. When using a pin as a static WAKE pin, keep the solder joints SJx open and connect the WAKEx/IDx pin to JxA (pull-up to VSUP). The pushbuttons PB1-PB4 can be used to provide a strong pull-down to GND, creating a local wake signal on the respective lines.

To use cyclic sensing wake, connect the respective pin to HSS4_OUT by connecting the WAKEx/IDx pin to JxB.

To use as an ID pin, first enable the ID function via SPI register configuration as outlined in the TCAN24xx-Q1 data sheet. Then, short the respective solder joint SJx to get a series resistance of 510Ω between the IC pin and the external pin. Then, you can connect the pin to GND, connect the pin to VSUP, or leave the pin floating.

2.7 Digital Interface

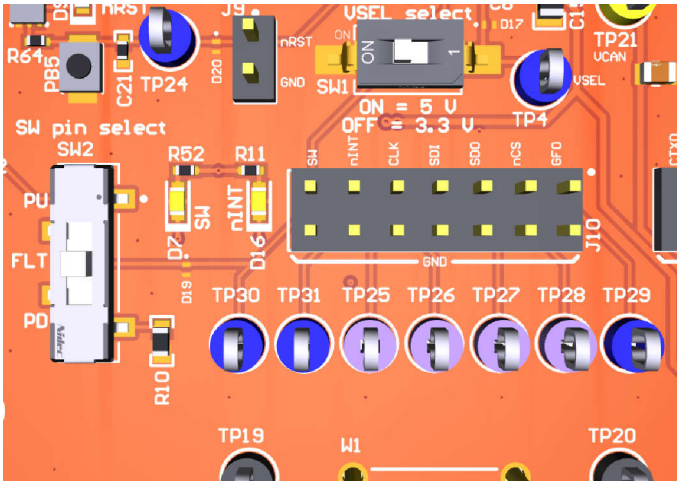


Figure 2-10. Digital Interface

The digital signals are available on header J10 and an external microcontroller or USB2SPI software must be used to communicate with the device via SPI. The software development (SW) pin can be configured to active-high or active-low. The pin can be pulled high, pulled low, or left floating by using switch SW2.

By default, the SW pin is active-high. TI recommends to pull the SW pin high when beginning to work with the EVM to prevent watchdog errors from causing the device to repeatedly enter *Restart Mode*.

The nINT and GFO pins are also available on header J10.

2.8 Header Information

There are multiple headers on the TCAN24XXEVM which allow for easy control of device functions and access to the inputs and outputs.

2.8.1 WAKE/ID Headers

There are four headers dedicated to controlling the biasing of the WAKE/ID pins on the TCAN24xx-Q1. These headers are arranged in a *plus* formation, allowing for the center pin (WAKEx/IDx) to be biased in different ways:

- Pull-up to VSUP via a 3.0kΩ resistor (using pin *pull up*)
- Pulled to HSS4 via a 3.0kΩ resistor (using pin *HSS4*)
- Strongly tied to VSUP (using pin *VSUP*)
- Strongly tied to GND (using pin *GND*)
- Floating

CAUTION

Do not use pushbuttons PB1/PB2/PB3/PB4 if the respective WAKE/ID line is connected strongly to VSUP using the *VSUP* connection mentioned above. This can create a low-impedance path between VSUP and GND.

Table 2-2. WAKEx/IDx Headers

Header	Function
J1 (J1A / J1B / J1C)	Biasing control for WAKE1/ID1.
J2 (J2A / J2B / J2C)	Biasing control for WAKE2/ID2.
J3 (J3A / J3B / J3C)	Biasing control for WAKE3/ID3.
J4 (J4A / J4B / J4C)	Biasing control for WAKE4/ID4.

2.8.2 Other Control Headers

Other headers on the EVM can be used to easily control specific functions of the TCAN24xx-Q1 or to interface with the outputs.

Table 2-3. Control Headers

Header	Function
J6	Enables the 5V LDO (U3). Use a shunt to pull the EN pin to either VSUP or VCC2 via a 10kΩ resistor.
J7	VCAN supply selector. Use a shunt to connect the VCAN rail to either VCC2 or the 5V output from the LDO (U3).
J11	Apply a shunt on this header to synchronize HSS1 and HSS2.
J16/J20	Apply a shunt on these headers to connect a subcircuit that allows for simulated bus loading.
J17/J21	Apply a shunt on these headers to connect a 120Ω standard termination resistor to the bus (R41).
J18/J22	Apply a shunt on these headers to connect split termination to the bus using two 60Ω resistors (R39/R42) and a 4.7nF capacitor (C26).
J25	Apply a shunt to connect the VLED and 5V rails, which allows the onboard LEDs to be powered using the 5V LDO (U3).
SJ5	Close this solder jumper to allow VBAT to be connected to pin 5 of the DB9 CAN connector (J15).

2.9 Push Buttons

There are five buttons on the EVM which allow for triggering different functions.

Table 2-4. Push Button Functions

Button	Function
PB1	Trigger a GND pulse on WAKE1/ID1. Do not press if J1 is connected in *VSUP* configuration.
PB2	Trigger a GND pulse on WAKE2/ID2. Do not press if J2 is connected in *VSUP* configuration.
PB3	Trigger a GND pulse on WAKE3/ID3. Do not press if J3 is connected in "*VSUP* configuration.
PB4	Trigger a GND pulse on WAKE4/ID4. Do not press if J4 is connected in *VSUP* configuration.
PB5	Trigger a GND pulse on nRST.

2.10 Switches

There are two switches on the EVM which allow for selection of device settings.

Table 2-5. Switch Functions

Switch	Function
SW1	VSEL selector. • ON: VCC1 = 5V • OFF: VCC1 = 3.3V
SW2	SW pin selector. • PU: SW pin pulled to VCC1 via a 1kΩ resistor. • FLT: SW pin floating • PD: SW pin pulled to GND via a 1kΩ resistor.

3 Hardware Design Files

The PCB layout and bill of materials (BOM) will be available at RTM.

3.1 Schematics

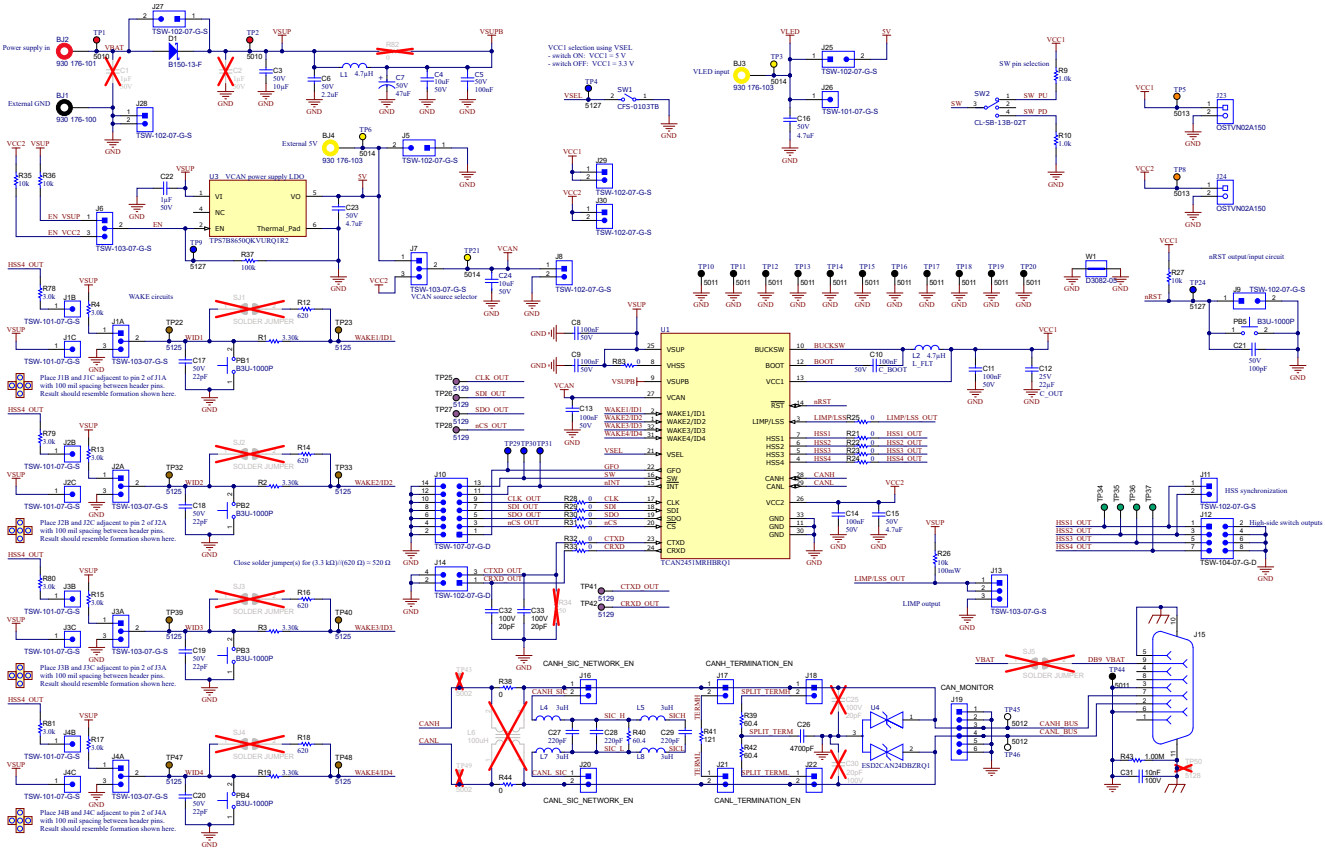


Figure 3-1. TCAN24XXEVM Schematic (Page 1)

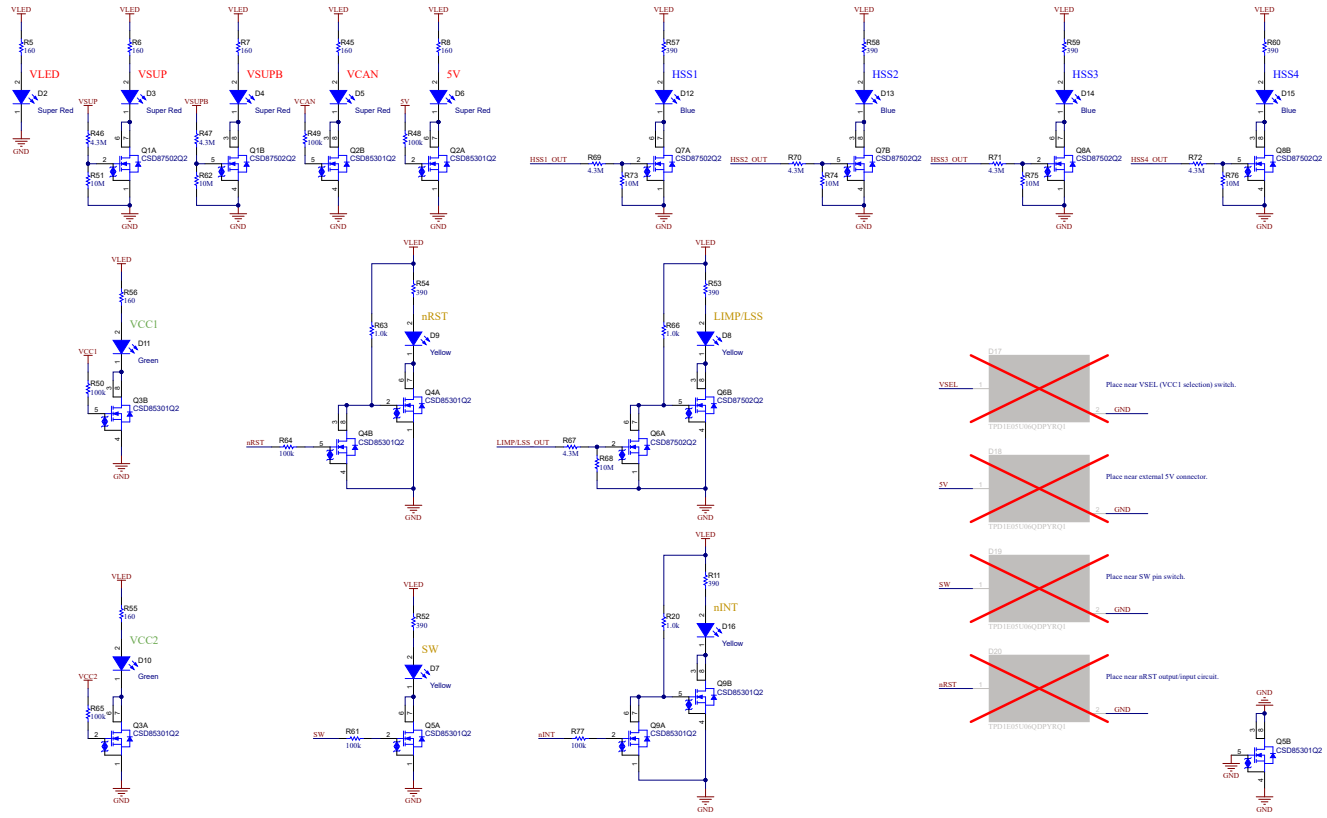


Figure 3-2. TCAN24XXEVM Schematic (Page 2)

4 Additional Information

4.1 Trademarks

All trademarks are the property of their respective owners.

5 References

See the device data sheet: TCAN245x-Q1 Automotive Signal Improvement Capable CAN FD System Basis Chip (SBC) with Integrated Buck Regulator and Watchdog

6 Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from April 30, 2024 to December 31, 2025 (from Revision * (April 2024) to Revision A (December 2025))	Page
• Changed part number from TCAN2451RHBRQ1 to TCAN2451MRHBRQ1 in Kit Contents section	2

STANDARD TERMS FOR EVALUATION MODULES

1. *Delivery:* TI delivers TI evaluation boards, kits, or modules, including any accompanying demonstration software, components, and/or documentation which may be provided together or separately (collectively, an “EVM” or “EVMs”) to the User (“User”) in accordance with the terms set forth herein. User's acceptance of the EVM is expressly subject to the following terms.

   1.1 EVMs are intended solely for product or software developers for use in a research and development setting to facilitate feasibility evaluation, experimentation, or scientific analysis of TI semiconductors products. EVMs have no direct function and are not finished products. EVMs shall not be directly or indirectly assembled as a part or subassembly in any finished product. For clarification, any software or software tools provided with the EVM (“Software”) shall not be subject to the terms and conditions set forth herein but rather shall be subject to the applicable terms that accompany such Software

   1.2 EVMs are not intended for consumer or household use. EVMs may not be sold, sublicensed, leased, rented, loaned, assigned, or otherwise distributed for commercial purposes by Users, in whole or in part, or used in any finished product or production system.

2 *Limited Warranty and Related Remedies/Disclaimers*:

   2.1 These terms do not apply to Software. The warranty, if any, for Software is covered in the applicable Software License Agreement.

   2.2 TI warrants that the TI EVM will conform to TI's published specifications for ninety (90) days after the date TI delivers such EVM to User. Notwithstanding the foregoing, TI shall not be liable for a nonconforming EVM if (a) the nonconformity was caused by neglect, misuse or mistreatment by an entity other than TI, including improper installation or testing, or for any EVMs that have been altered or modified in any way by an entity other than TI, (b) the nonconformity resulted from User's design, specifications or instructions for such EVMs or improper system design, or (c) User has not paid on time. Testing and other quality control techniques are used to the extent TI deems necessary. TI does not test all parameters of each EVM. User's claims against TI under this Section 2 are void if User fails to notify TI of any apparent defects in the EVMs within ten (10) business days after delivery, or of any hidden defects with ten (10) business days after the defect has been detected.

   2.3 TI's sole liability shall be at its option to repair or replace EVMs that fail to conform to the warranty set forth above, or credit User's account for such EVM. TI's liability under this warranty shall be limited to EVMs that are returned during the warranty period to the address designated by TI and that are determined by TI not to conform to such warranty. If TI elects to repair or replace such EVM, TI shall have a reasonable time to repair such EVM or provide replacements. Repaired EVMs shall be warranted for the remainder of the original warranty period. Replaced EVMs shall be warranted for a new full ninety (90) day warranty period.

WARNING

Evaluation Kits are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems.

User shall operate the Evaluation Kit within TI’s recommended guidelines and any applicable legal or environmental requirements as well as reasonable and customary safeguards. Failure to set up and/or operate the Evaluation Kit within TI’s recommended guidelines may result in personal injury or death or property damage. Proper set up entails following TI’s instructions for electrical ratings of interface circuits such as input, output and electrical loads.

NOTE:

EXPOSURE TO ELECTROSTATIC DISCHARGE (ESD) MAY CAUSE DEGREDATION OR FAILURE OF THE EVALUATION KIT; TI RECOMMENDS STORAGE OF THE EVALUATION KIT IN A PROTECTIVE ESD BAG.

3 *Regulatory Notices:*

3.1 *United States*

3.1.1 *Notice applicable to EVMs not FCC-Approved:*

FCC NOTICE: This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.

3.1.2 *For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:*

CAUTION

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

FCC Interference Statement for Class A EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

FCC Interference Statement for Class B EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- *Reorient or relocate the receiving antenna.*
- *Increase the separation between the equipment and receiver.*
- *Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.*
- *Consult the dealer or an experienced radio/TV technician for help.*

3.2 *Canada*

3.2.1 *For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210 or RSS-247*

Concerning EVMs Including Radio Transmitters:

This device complies with Industry Canada license-exempt RSSs. Operation is subject to the following two conditions:

(1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concernant les EVMs avec appareils radio:

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Concerning EVMs Including Detachable Antennas:

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur

3.3 *Japan*

3.3.1 *Notice for EVMs delivered in Japan:* Please see http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_01.page 日本国内に輸入される評価用キット、ボードについては、次のところをご覧ください。

https://www.ti.com/ja-jp/legal/notice-for-evaluation-kits-delivered-in-japan.html

3.3.2 *Notice for Users of EVMs Considered "Radio Frequency Products" in Japan:* EVMs entering Japan may not be certified by TI as conforming to Technical Regulations of Radio Law of Japan.

If User uses EVMs in Japan, not certified to Technical Regulations of Radio Law of Japan, User is required to follow the instructions set forth by Radio Law of Japan, which includes, but is not limited to, the instructions below with respect to EVMs (which for the avoidance of doubt are stated strictly for convenience and should be verified by User):

1. Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

【無線電波を送信する製品の開発キットをお使いになる際の注意事項】 開発キットの中には技術基準適合証明を受けていないものがあります。 技術適合証明を受けていないもののご使用に際しては、電波法遵守のため、以下のいずれかの措置を取っていただく必要がありますのでご注意ください。

1. 電波法施行規則第6条第1項第1号に基づく平成18年3月28日総務省告示第173号で定められた電波暗室等の試験設備でご使用いただく。
2. 実験局の免許を取得後ご使用いただく。
3. 技術基準適合証明を取得後ご使用いただく。

なお、本製品は、上記の「ご使用にあたっての注意」を譲渡先、移転先に通知しない限り、譲渡、移転できないものとします。

上記を遵守頂けない場合は、電波法の罰則が適用される可能性があることをご留意ください。 日本テキサス・インスツルメンツ株式会社

東京都新宿区西新宿 6 丁目 2 4 番 1 号

西新宿三井ビル

3.3.3 *Notice for EVMs for Power Line Communication:* Please see http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_02.page

電力線搬送波通信についての開発キットをお使いになる際の注意事項については、次のところをご覧ください。https://www.ti.com/ja-jp/legal/notice-for-evaluation-kits-for-power-line-communication.html

3.4 *European Union*

3.4.1 *For EVMs subject to EU Directive 2014/30/EU (Electromagnetic Compatibility Directive)*:

This is a class A product intended for use in environments other than domestic environments that are connected to a low-voltage power-supply network that supplies buildings used for domestic purposes. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.

4 *EVM Use Restrictions and Warnings:*

4.1 EVMS ARE NOT FOR USE IN FUNCTIONAL SAFETY AND/OR SAFETY CRITICAL EVALUATIONS, INCLUDING BUT NOT LIMITED TO EVALUATIONS OF LIFE SUPPORT APPLICATIONS.

4.2 User must read and apply the user guide and other available documentation provided by TI regarding the EVM prior to handling or using the EVM, including without limitation any warning or restriction notices. The notices contain important safety information related to, for example, temperatures and voltages.

4.3 *Safety-Related Warnings and Restrictions:*

4.3.1 User shall operate the EVM within TI's recommended specifications and environmental considerations stated in the user guide, other available documentation provided by TI, and any other applicable requirements and employ reasonable and customary safeguards. Exceeding the specified performance ratings and specifications (including but not limited to input and output voltage, current, power, and environmental ranges) for the EVM may cause personal injury or death, or property damage. If there are questions concerning performance ratings and specifications, User should contact a TI field representative prior to connecting interface electronics including input power and intended loads. Any loads applied outside of the specified output range may also result in unintended and/or inaccurate operation and/or possible permanent damage to the EVM and/or interface electronics. Please consult the EVM user guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative. During normal operation, even with the inputs and outputs kept within the specified allowable ranges, some circuit components may have elevated case temperatures. These components include but are not limited to linear regulators, switching transistors, pass transistors, current sense resistors, and heat sinks, which can be identified using the information in the associated documentation. When working with the EVM, please be aware that the EVM may become very warm.

4.3.2 EVMs are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems. User assumes all responsibility and liability for proper and safe handling and use of the EVM by User or its employees, affiliates, contractors or designees. User assumes all responsibility and liability to ensure that any interfaces (electronic and/or mechanical) between the EVM and any human body are designed with suitable isolation and means to safely limit accessible leakage currents to minimize the risk of electrical shock hazard. User assumes all responsibility and liability for any improper or unsafe handling or use of the EVM by User or its employees, affiliates, contractors or designees.

4.4 User assumes all responsibility and liability to determine whether the EVM is subject to any applicable international, federal, state, or local laws and regulations related to User's handling and use of the EVM and, if applicable, User assumes all responsibility and liability for compliance in all respects with such laws and regulations. User assumes all responsibility and liability for proper disposal and recycling of the EVM consistent with all applicable international, federal, state, and local requirements.

5. *Accuracy of Information:* To the extent TI provides information on the availability and function of EVMs, TI attempts to be as accurate as possible. However, TI does not warrant the accuracy of EVM descriptions, EVM availability or other information on its websites as accurate, complete, reliable, current, or error-free.

6. *Disclaimers:*

6.1 EXCEPT AS SET FORTH ABOVE, EVMS AND ANY MATERIALS PROVIDED WITH THE EVM (INCLUDING, BUT NOT LIMITED TO, REFERENCE DESIGNS AND THE DESIGN OF THE EVM ITSELF) ARE PROVIDED "AS IS" AND "WITH ALL FAULTS." TI DISCLAIMS ALL OTHER WARRANTIES, EXPRESS OR IMPLIED, REGARDING SUCH ITEMS, INCLUDING BUT NOT LIMITED TO ANY EPIDEMIC FAILURE WARRANTY OR IMPLIED WARRANTIES OF MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF ANY THIRD PARTY PATENTS, COPYRIGHTS, TRADE SECRETS OR OTHER INTELLECTUAL PROPERTY RIGHTS.

6.2 EXCEPT FOR THE LIMITED RIGHT TO USE THE EVM SET FORTH HEREIN, NOTHING IN THESE TERMS SHALL BE CONSTRUED AS GRANTING OR CONFERRING ANY RIGHTS BY LICENSE, PATENT, OR ANY OTHER INDUSTRIAL OR INTELLECTUAL PROPERTY RIGHT OF TI, ITS SUPPLIERS/LICENSORS OR ANY OTHER THIRD PARTY, TO USE THE EVM IN ANY FINISHED END-USER OR READY-TO-USE FINAL PRODUCT, OR FOR ANY INVENTION, DISCOVERY OR IMPROVEMENT, REGARDLESS OF WHEN MADE, CONCEIVED OR ACQUIRED.

7. *USER'S INDEMNITY OBLIGATIONS AND REPRESENTATIONS.* USER WILL DEFEND, INDEMNIFY AND HOLD TI, ITS LICENSORS AND THEIR REPRESENTATIVES HARMLESS FROM AND AGAINST ANY AND ALL CLAIMS, DAMAGES, LOSSES, EXPENSES, COSTS AND LIABILITIES (COLLECTIVELY, "CLAIMS") ARISING OUT OF OR IN CONNECTION WITH ANY HANDLING OR USE OF THE EVM THAT IS NOT IN ACCORDANCE WITH THESE TERMS. THIS OBLIGATION SHALL APPLY WHETHER CLAIMS ARISE UNDER STATUTE, REGULATION, OR THE LAW OF TORT, CONTRACT OR ANY OTHER LEGAL THEORY, AND EVEN IF THE EVM FAILS TO PERFORM AS DESCRIBED OR EXPECTED.

8. *Limitations on Damages and Liability:*

   8.1 *General Limitations*. IN NO EVENT SHALL TI BE LIABLE FOR ANY SPECIAL, COLLATERAL, INDIRECT, PUNITIVE, INCIDENTAL, CONSEQUENTIAL, OR EXEMPLARY DAMAGES IN CONNECTION WITH OR ARISING OUT OF THESE TERMS OR THE USE OF THE EVMS , REGARDLESS OF WHETHER TI HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES. EXCLUDED DAMAGES INCLUDE, BUT ARE NOT LIMITED TO, COST OF REMOVAL OR REINSTALLATION, ANCILLARY COSTS TO THE PROCUREMENT OF SUBSTITUTE GOODS OR SERVICES, RETESTING, OUTSIDE COMPUTER TIME, LABOR COSTS, LOSS OF GOODWILL, LOSS OF PROFITS, LOSS OF SAVINGS, LOSS OF USE, LOSS OF DATA, OR BUSINESS INTERRUPTION. NO CLAIM, SUIT OR ACTION SHALL BE BROUGHT AGAINST TI MORE THAN TWELVE (12) MONTHS AFTER THE EVENT THAT GAVE RISE TO THE CAUSE OF ACTION HAS OCCURRED.

   8.2 *Specific Limitations.* IN NO EVENT SHALL TI'S AGGREGATE LIABILITY FROM ANY USE OF AN EVM PROVIDED HEREUNDER, INCLUDING FROM ANY WARRANTY, INDEMITY OR OTHER OBLIGATION ARISING OUT OF OR IN CONNECTION WITH THESE TERMS, , EXCEED THE TOTAL AMOUNT PAID TO TI BY USER FOR THE PARTICULAR EVM(S) AT ISSUE DURING THE PRIOR TWELVE (12) MONTHS WITH RESPECT TO WHICH LOSSES OR DAMAGES ARE CLAIMED. THE EXISTENCE OF MORE THAN ONE CLAIM SHALL NOT ENLARGE OR EXTEND THIS LIMIT.

9. *Return Policy.* Except as otherwise provided, TI does not offer any refunds, returns, or exchanges. Furthermore, no return of EVM(s) will be accepted if the package has been opened and no return of the EVM(s) will be accepted if they are damaged or otherwise not in a resalable condition. If User feels it has been incorrectly charged for the EVM(s) it ordered or that delivery violates the applicable order, User should contact TI. All refunds will be made in full within thirty (30) working days from the return of the components(s), excluding any postage or packaging costs.

10. *Governing Law:* These terms and conditions shall be governed by and interpreted in accordance with the laws of the State of Texas, without reference to conflict-of-laws principles. User agrees that non-exclusive jurisdiction for any dispute arising out of or relating to these terms and conditions lies within courts located in the State of Texas and consents to venue in Dallas County, Texas. Notwithstanding the foregoing, any judgment may be enforced in any United States or foreign court, and TI may seek injunctive relief in any United States or foreign court.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265

IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATASHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES “AS IS” AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, regulatory or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you fully indemnify TI and its representatives against any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI’s products are provided subject to TI’s Terms of Sale, TI’s General Quality Guidelines, or other applicable terms available either on ti.com or provided in conjunction with such TI products. TI’s provision of these resources does not expand or otherwise alter TI’s applicable warranties or warranty disclaimers for TI products. Unless TI explicitly designates a product as custom or customer-specified, TI products are standard, catalog, general purpose devices.

TI objects to and rejects any additional or different terms you may propose.

Last updated 10/2025