# TPS2552/3, Power-Distribution Switch With Adjustable Current-Limit EVM

This user's guide describes the TPS2552/3DBVEVM and TPS2552/3DRVEVM evaluation modules (EVM). This guide contains the EVM schematic, bill of materials, assembly drawing, and top and bottom board layouts.

## 1 Introduction

The TPS2552/3DBVEVM and TPS2552/3DRVEVM are evaluation modules (EVM) for Texas Instruments' family of power-distribution switches with adjustable current-limit. These EVMs operate over a 2.5-V to 6.5-V range. An onboard jumper sets the output current-limit to either 0.5 A or 1 A. Test points provide convenient access to all critical node voltages.

The silkscreen outline on the PCB top-side encloses components found in a typical USB application.

The PCB top-side accepts a power-distribution switch in a SOT23-6 package; the PCB bottom side accepts a power-distribution switch in the smaller SON package with a thermal pad. These switches have an enable input, an overcurrent status output, and overtemperature shutdown.

### 1.1 Related Documentation from Texas Instruments

TPS2552, TPS2553 data sheet (TI Literature Number SLVS841)

# 2 Electrical Specifications

The EVM meets the electrical specifications in Table 1 over the recommended operating junction-temperature range of -40°C ≤ $T_J$ ≤ 105°C for the DRV (SON) package and -40°C ≤ $T_J$ ≤ 125°C for the DBV (SOT23-6) package.

**Table 1. EVM Electrical Specifications**

| PARAMETER | CONDITION | MIN | TYP | MAX | UNIT |
|---|---|---|---|---|---|
| Input voltage, $V_{IN}$ | J1 | 2.5 | – | 6.5 | V |
| Short-circuit output current-limit, $I_{LIMIT}$ | J2 shorting-jumper is absent, J3 is short circuited, TPS2552/3 is enabled | 0.28 | 0.47 | 0.61 | A |
| | J2 shorting-jumper is present, J3 is short circuited, TPS2552/3 is enabled | 0.70 | 1.02 | 1.24 | |

## 2.1 Electrostatic Discharge

The EVM has been tested to IEC 61000-4-2. The level used was 8-kV contact discharge and 15-kV air discharge. Surges were applied to the EVM input and output. No damage to the TPS2552/3 was observed.

# 3 Schematic

## 3.1 EVM Options

**Table 2. EVM Options**

| HPA364 | DEVICE | DEVICE PACKAGE | ENABLE |
|---|---|---|---|
| -1 | TPS2552DBV | SOT-23-6 | Active Low |
| -2 | TPS2553DBV | SOT-23-6 | Active High |
| -3 | TPS2552DBV-1 | SOT-23-6 | Active Low |
| -4 | TPS2553DBV-1 | SOT-23-6 | Active High |
| -5 | TPS2552DRV | SON | Active Low |
| -6 | TPS2553DRV | SON | Active High |
| -7 | TPS2552DRV-1 | SON | Active Low |
| -8 | TPS2553DRV-1 | SON | Active High |

## 3.2 Schematic

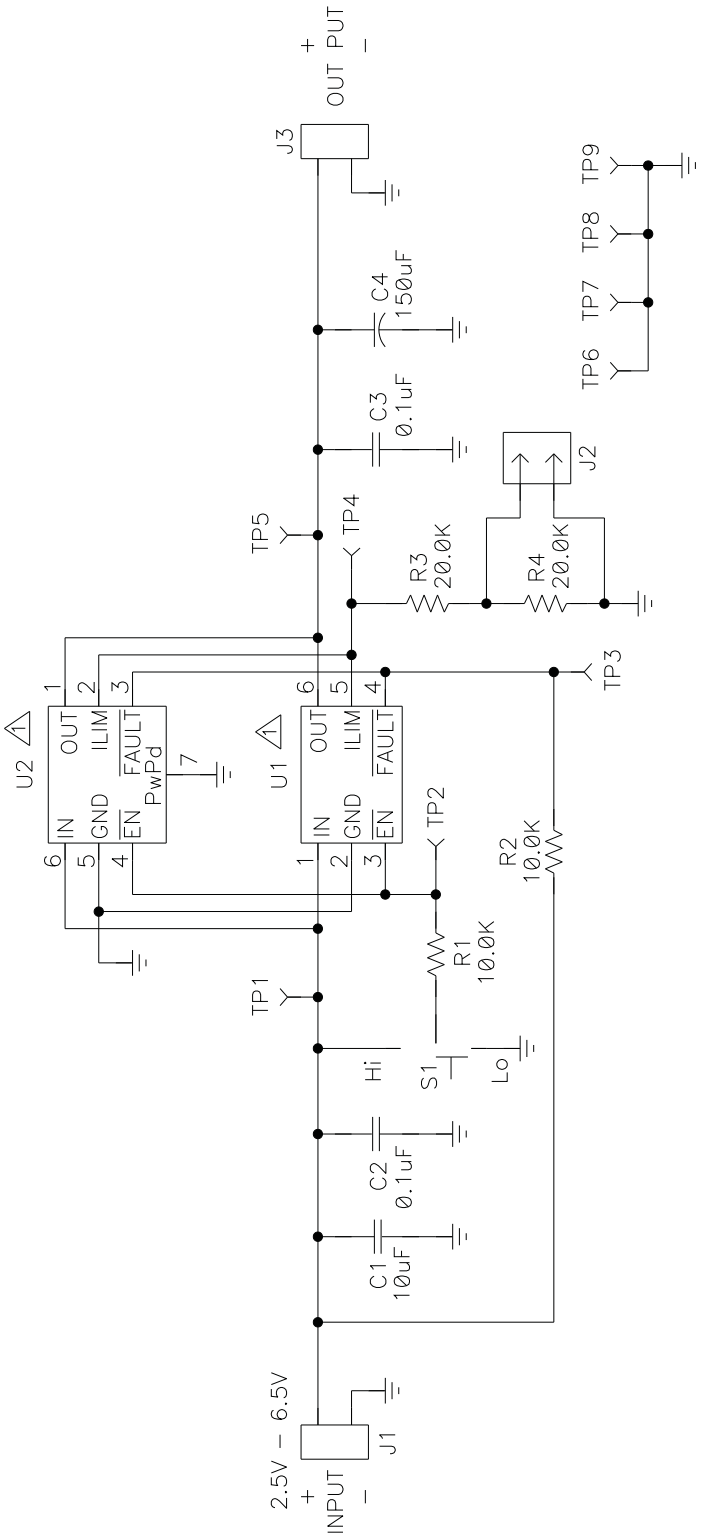


**Figure 1. EVM Schematic**

# 4 EVM Setup

## 4.1 Recommended Test Equipment

The following test equipment is recommended:

- Two-channel storage oscilloscope
- Current probe
- Voltage probe
- An adjustable power supply with a 2.5-V to 6.5-V output and a 10-A output current-limit
- Volt-ohm meter
- A passive or active load capable of handling 3 A.

## 4.2 Measuring the Short-Circuit Output Current-Limit

The user should read the TPS2552/3 data sheet before using the EVM.

Figure 2 shows the EVM test setup for measuring current-limit. Switch S1 enables the power-distribution switch into a short circuit for this measurement. For retry controllers, Figure 3 shows the current waveform for the TPS2552/3DRVEVM with a shorting jumper populating header J2; Figure 4 shows the current waveform with header J2 unpopulated.

For latch-off controllers Fig5 shows the current waveform for the TPS2552/3DRV EVM with a shorting jumper populating header J2; Figure 6 shows the current waveform with header J2 unpopulated.

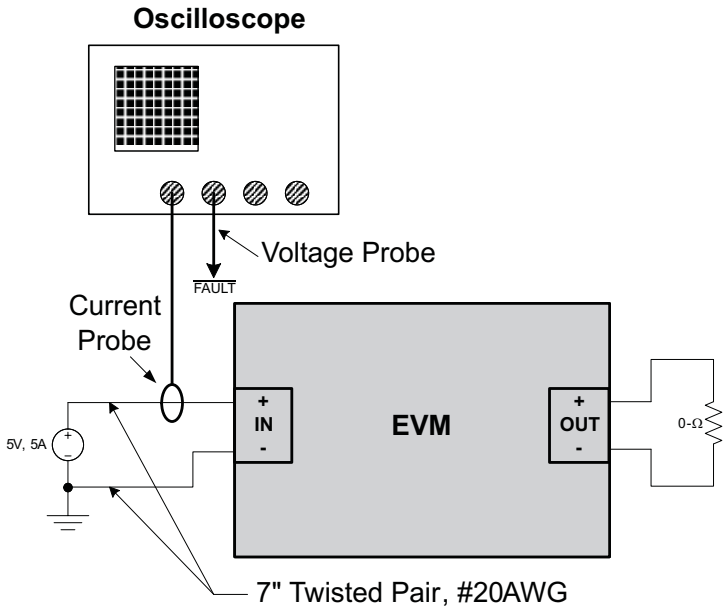


**Figure 2. EVM Setup For Measuring Current-Limit**

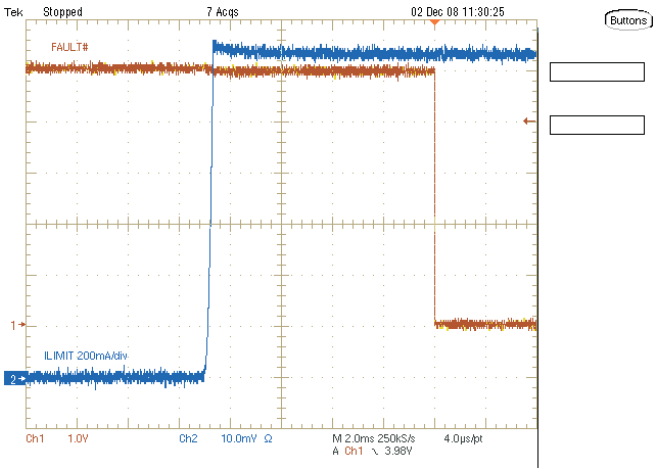


**Figure 3. TPS2552DRVEVM Short-Circuit Output Current and $\overline{\text{FAULT}}$ Status With J2 Shorting Jumper Present.**

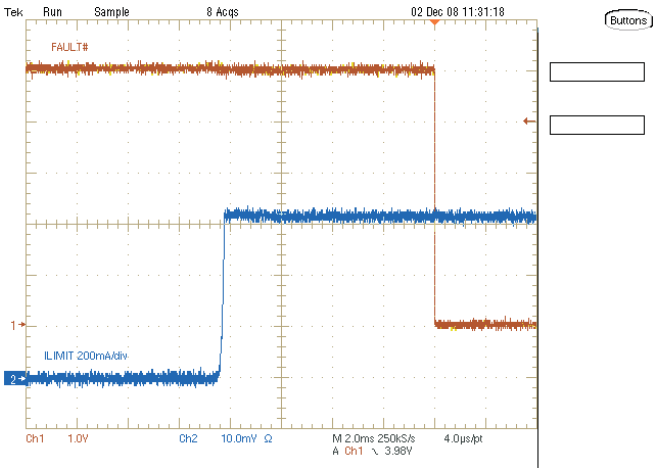


**Figure 4. TPS2552DRVEVM Short-Circuit Output Current and $\overline{\text{FAULT}}$ Status With J2 Shorting Jumper Absent.**

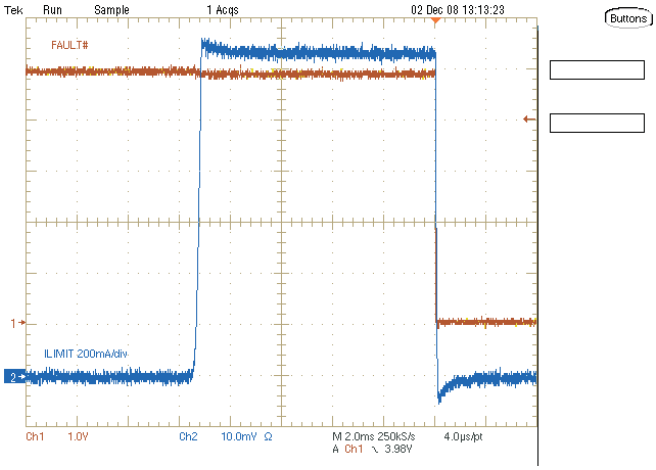


**Figure 5. PS2552DBVEVM Short-Circuit Output Current and $\overline{\text{FAULT}}$ Status With J2 Shorting Jumper Present.**

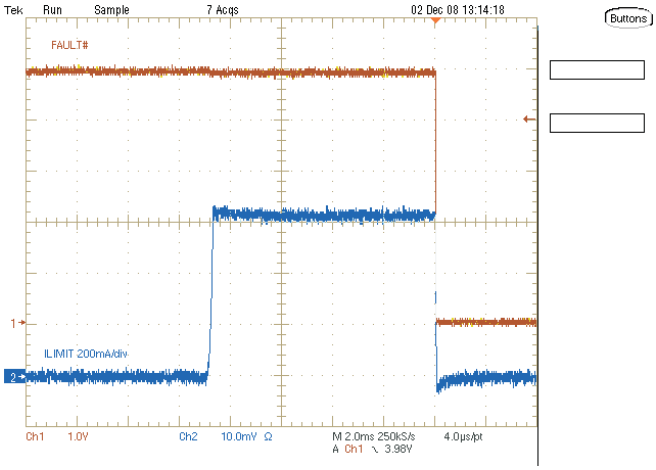


**Figure 6. TPS2552DBVEVM Short-Circuit Output Current and $\overline{\text{FAULT}}$ Status With J2 Shorting Jumper Absent.**

## *4.3 Adjusting the Short-Circuit Output Current-Limit*

The EVM provides two current-limit settings. If a different setting is required, then populate header J2 with a shorting jumper and modify resistor R3 using the current limit resistor calculator, TI Literature Number SLVC163.

See the TPS2552/3 data sheet for the worst-case current-limit variation.

## 5 Board Layout

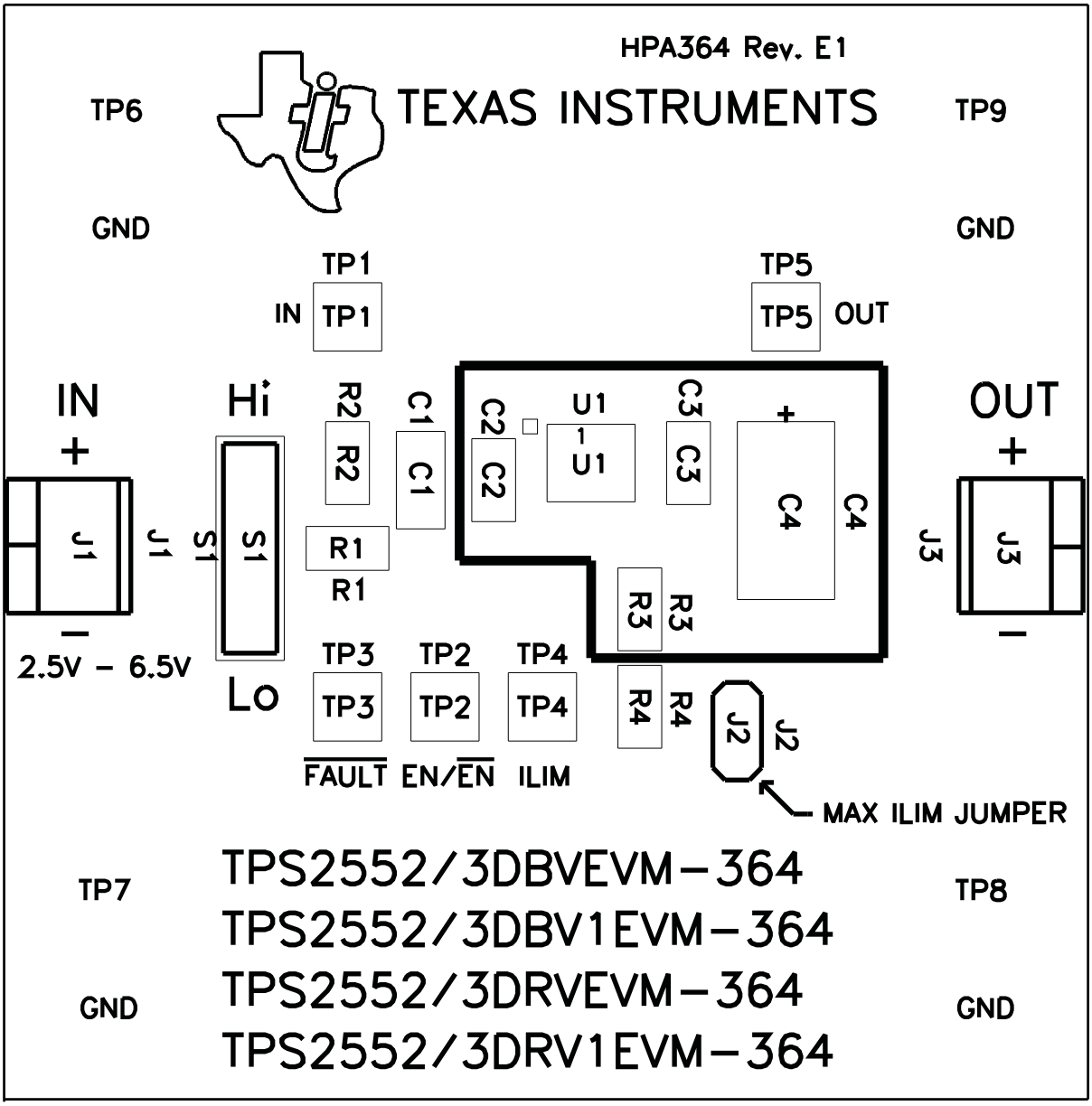


**Figure 7. EVM Top Assembly**

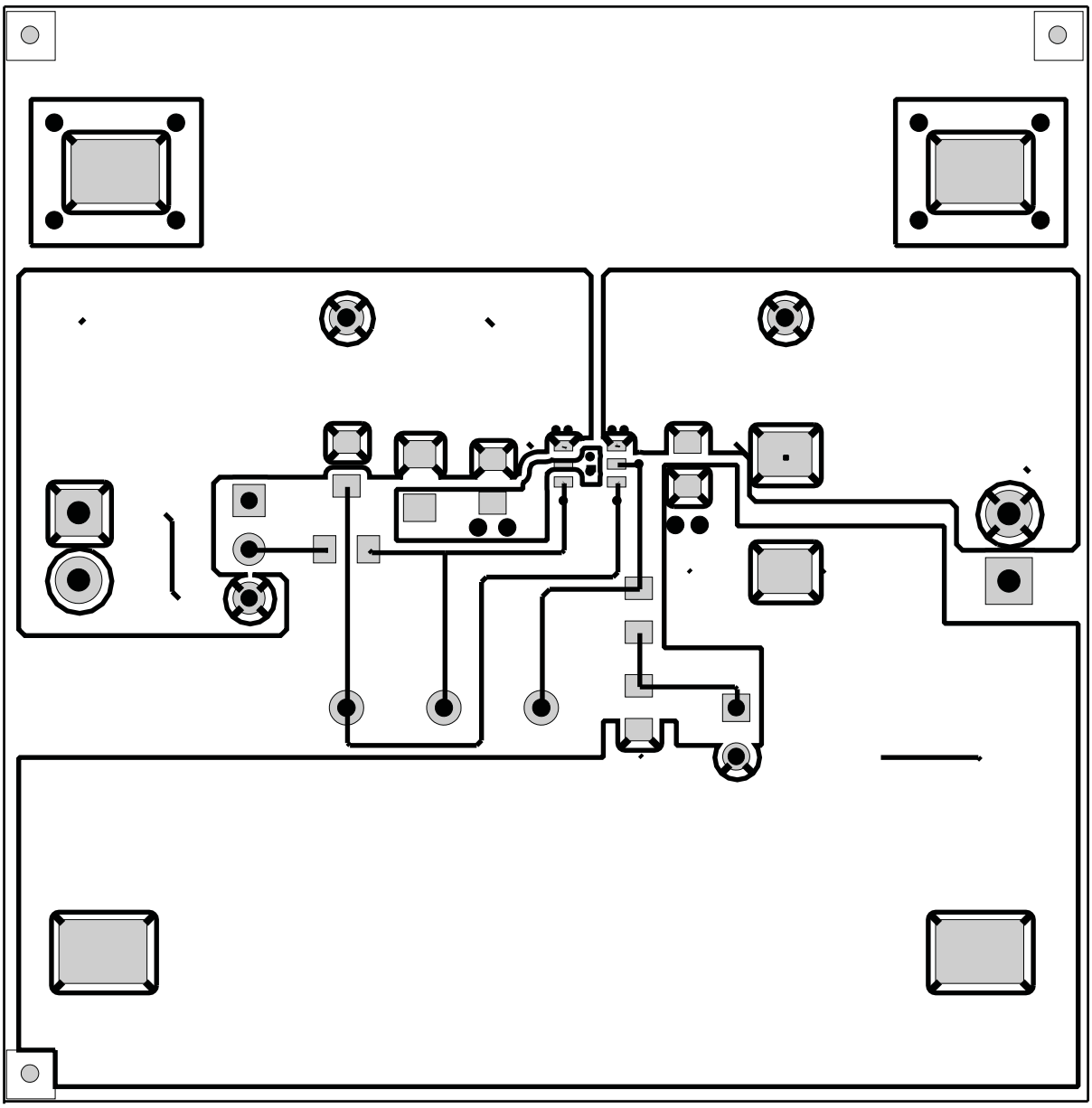

**Figure 8. EVM Top-Side Layout**

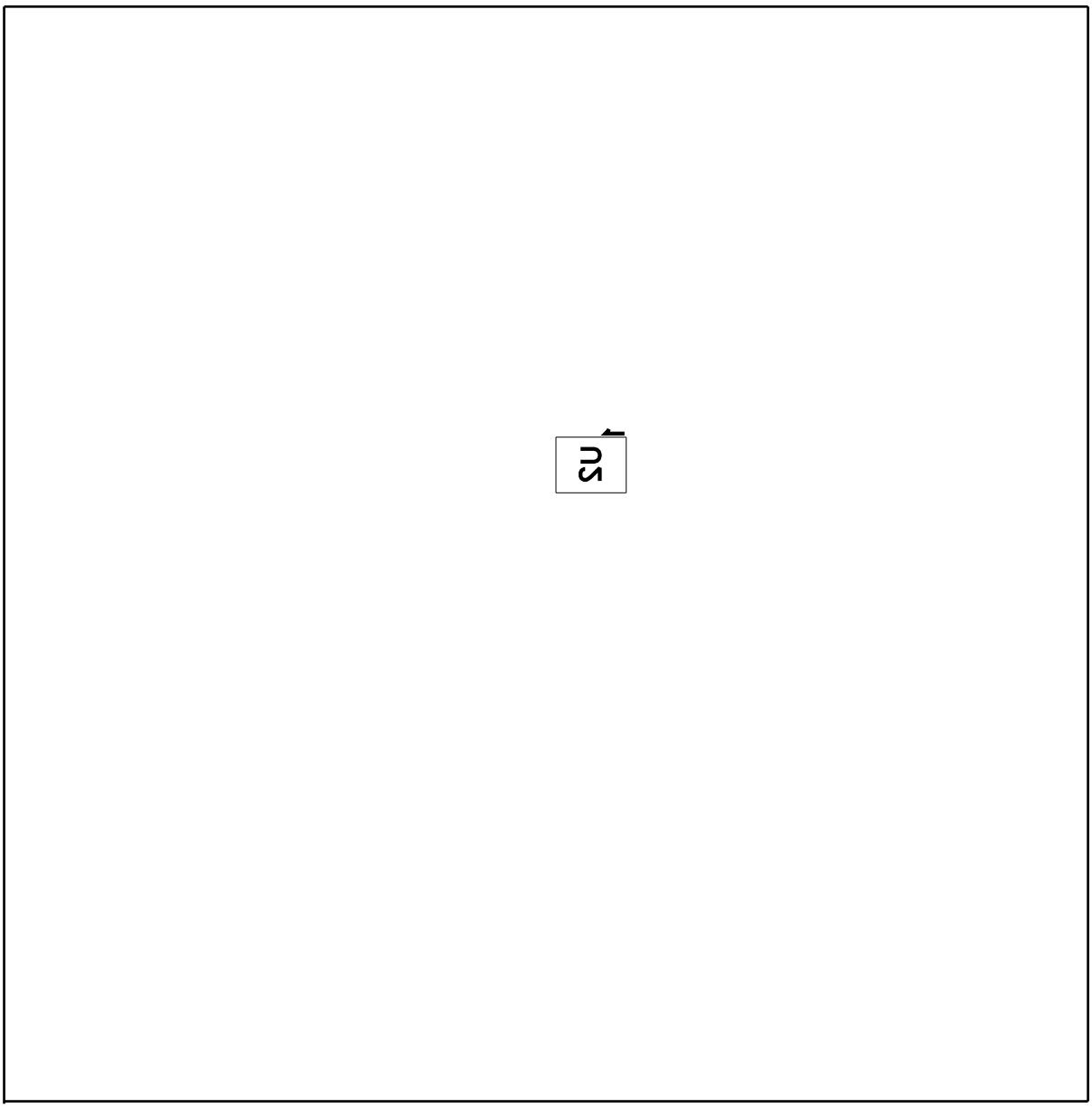

**Figure 9. EVM Bottom Assembly**

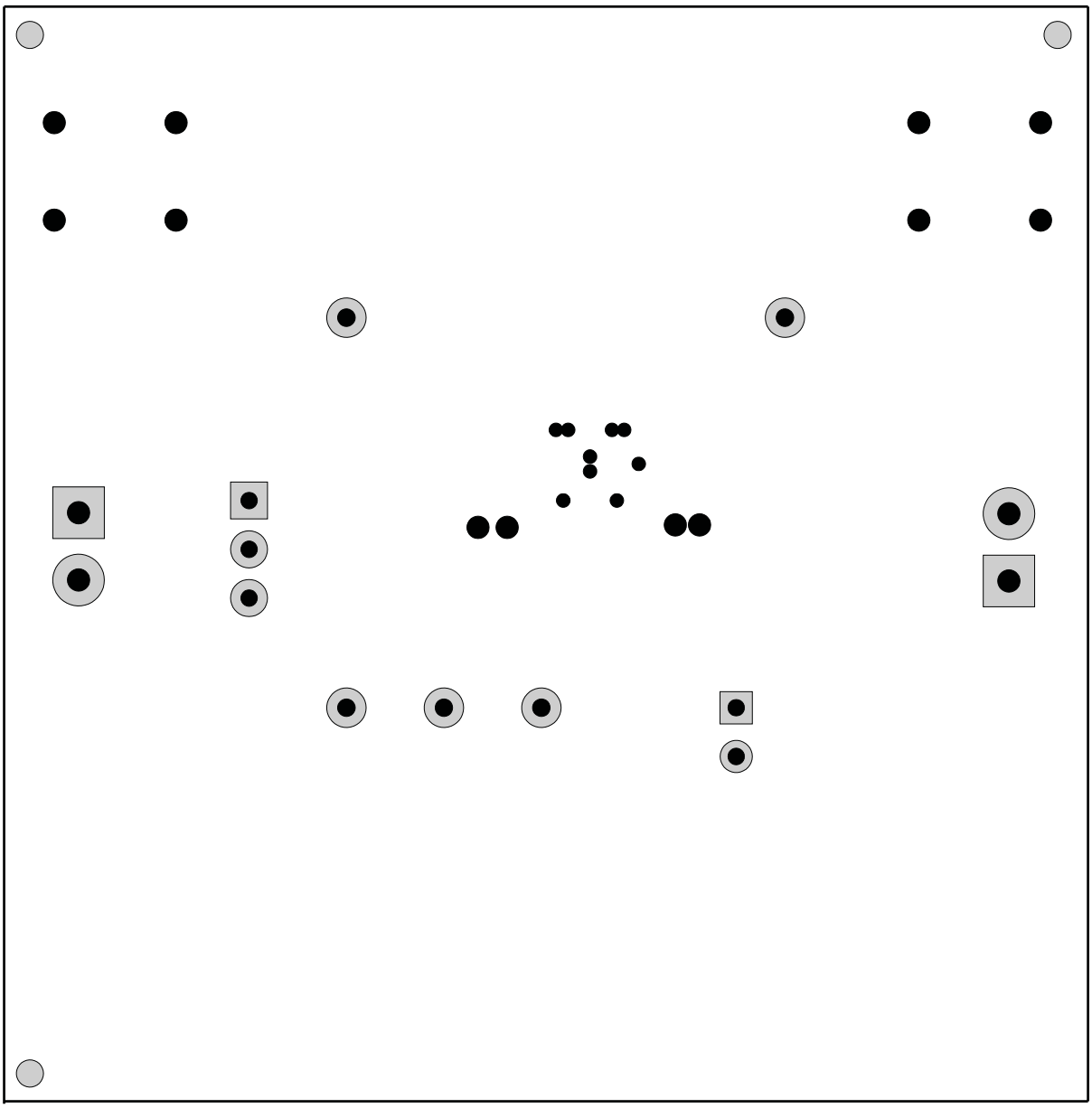

**Figure 10. EVM Bottom-Side Layout**

# 6 List of Materials

**Table 3. EVM List of Materials** (1)(2)(3)(4)(5)(6)

| QTY | | | | | | | | REF DES | DESCRIPTION | PART NUMBER | MFR |
|---|---|---|---|---|---|---|---|---|---|---|---|
| -001 | -002 | -003 | -004 | -005 | -006 | -007 | -008 | | | | |
| 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | C1 | Capacitor, ceramic, 10 µF, X7R, 10 V, 10%, 1206 | STD | STD |
| 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | C2, C3 | Capacitor, ceramic, 16 V, X7R, 10%, 0.1 µF, 0805 | STD | STD |
| 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | C4 | Capacitor, tantalum, 150 µF, 10 V, 100 mΩ, 10%, 7343 (D) | B45197A2157K409 | KEMET |
| 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | J2 | Header, male 2 pin, 100-mil spacing, (36-pin strip), 0.100 inch x 2 | PTC36SAAN | Sullins |
| 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | U1 | Power-distribution switch, current limited, SOT-23-6 | TPS2552DBV | TI |
| 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | U1 | Power-distribution switch, current limited, SOT-23-6 | TPS2553DBV | |
| 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | U1 | Power-distribution switch, current limited, SOT-23-6 | TPS2552DBV-1 | |
| 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | U1 | Power-distribution switch, current limited, SOT-23-6 | TPS2553DBV-1 | |
| 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | U2 | Power-distribution switch, current limited, SON | TPS2552DRV | TI |
| 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | U2 | Power-distribution switch, current limited, SON | TPS2553DRV | |
| 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | U2 | Power-distribution switch, current limited, SON | TPS2552DRV-1 | |
| 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | U2 | Power-distribution switch, current-limited, SON | TPS2553DRV-1 | |
| 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | -- | PCB, 2.25 In x 2.225 In x 0.062 In, SON | HPA364 | Any |
| 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | R1, R2 | Resistor, chip, 1/10 W, 1%, 10.0 kΩ, 0805 | CRCW0805-1002F | Vishay |
| 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | R3, R4 | Resistor, chip, 1/10 W, 1%, 20.0 kΩ, 0805 | CRCW0805-2002-F | Vishay |
| 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | S1 | Switch, SPDT, slide, PC mount, 500 mA, 0.400 x 0.100 in | 09-03201-02 | EAO |
| 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | J1, J3 | Terminal block, 2 pin, 6 A, 3.5 mm, 0.27 x 0.25 inch | ED555/2DS | OST |
| 5 | 5 | 5 | 5 | 5 | 5 | 5 | 5 | TP1, TP2, TP3, TP4, TP5 | Test point, white, thru-hole color keyed, 0.100 x 0.100 in | 5002 | Keystone |
| 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | TP6, TP7, TP8, TP9 | Test point, SM, 0.150 x 0.090, 0.185 x 0.135 in | 5016 | Keystone |

(1) These assemblies are ESD sensitive, ESD precautions shall be observed.
(2) These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.
(3) These assemblies must comply with workmanship standards IPC-A-610 Class 2.
(4) Ref designators marked with an asterisk ('**') cannot be substituted. All other components can be substituted with equivalent MFG's components.
(5) Attach a rubber bumper to each corner of the PCB.
(6) Insert shorting jumper on header J2.

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## EVM WARNINGS AND RESTRICTIONS

It is important to operate this EVM within the input voltage range of 2.5 V to 5.5 V and the output voltage range of 2.5 V to 5.5 V.

Exceeding the specified input range may cause unexpected operation and/or irreversible damage to the EVM. If there are questions concerning the input range, please contact a TI field representative prior to connecting the input power.

Applying loads outside of the specified output range may result in unintended operation and/or possible permanent damage to the EVM. Please consult the EVM User's Guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative.

During normal operation, some circuit components may have case temperatures greater than 85°C. The EVM is designed to operate properly with certain components above 85°C as long as the input and output ranges are maintained. These components include but are not limited to linear regulators, switching transistors, pass transistors, and current sense resistors. These types of devices can be identified using the EVM schematic located in the EVM User's Guide. When placing measurement probes near these devices during operation, please be aware that these devices may be very warm to the touch.

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