

## 1 Introduction

## 2 Features

- ### 3 Evaluation Board Schematic



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## 4 Powering and Loading Considerations

Read this entire section prior to attempting to power the evaluation board.

### 4.1 Quick Start Procedure

**Step 1:** Set the bench power supply current limit to 3A. Set the power supply voltage to 3.5V. Turn off the power supply output. Connect the power supply to the LMR61428 demo board. Positive connection to  $V_{IN}$  and negative connection to GND.

**Step 2:** Connect a load, as high as 0.5A, to the  $V_{OUT}$  terminal. Positive connection to  $V_{OUT}$  and negative connection to GND.

**Step 3:** Connect the shunt so as to short the pins 1 and 2 of the jumper J1. This sets the bootstrap to  $V_{OUT}$

**Step 4:** The EN pin should be left open for normal operation.

**Step 5:** Turn on the bench power supply with no load applied to the LMR61428. If the shunt for the jumper J1 was in place, the  $V_{OUT}$  would be in regulation at a nominal 5V.

**Step 6:** Gradually increase the load and  $V_{OUT}$  should remain in regulation as the load is increased up to 0.5 Amps.

### 4.2 Shutdown Operation

The EVM includes a pull-up resistor  $R_{EN}$  to enable the device. Use the EN post to disable the device by pulling this node to GND.

### 4.3 Bootstrap Operation

The EVM has a jumper installed to select the bootstrap option. The default condition is that the jumper be set such that the bootstrap voltage is obtained from the output. For more information, see *LMR61428 SIMPLE SWITCHER 14Vout, 2.85A Step-Up Voltage Regulator in VSSOP* ([SNVS815](#)).

### 4.4 Setting the Output Voltage

The output voltage of the step-up regulator can be set between 1.24V and 14V. But because of the gated oscillator scheme, the maximum possible input to output boost ratio is fixed. For a boost regulator,

$$V_{OUT} / V_{IN} = 1 / [1 - D] \quad (1)$$

The LMR61428 has a fixed duty cycle,  $D$ , of 70% typical. Therefore,

$$V_{OUT} / V_{IN} = 1 / 0.3 \quad (2)$$

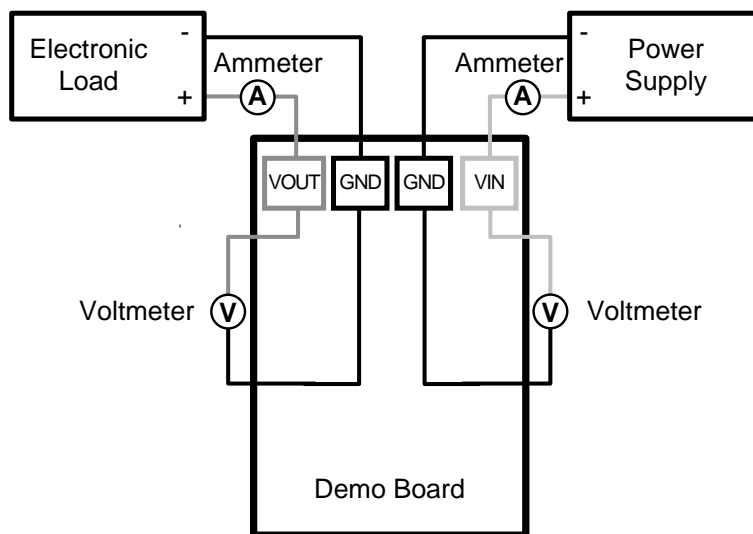
This sets the maximum possible boost ratio of  $V_{IN}$  to  $V_{OUT}$  to about 3 times. The user can now estimate what the minimum design inputs should be in order to achieve a desired output, or what the output would be when a certain minimum input is applied. For example, if the desired  $V_{OUT}$  was 14V, then the least  $V_{IN}$  should be higher than  $V_{OUT} / 3$ . If the input voltage fell below this threshold, the output voltage would not be regulated because of the fixed duty cycle. If the minimum  $V_{IN}$  was guaranteed at 2V, the max possible  $V_{OUT}$  would be  $V_{IN} \times 3$ .

The  $V_{OUT}$  is set by connecting a feedback resistive divider made of  $R_{fbt}$  and  $R_{fbb}$ . The feedback resistor values are selected as follows:

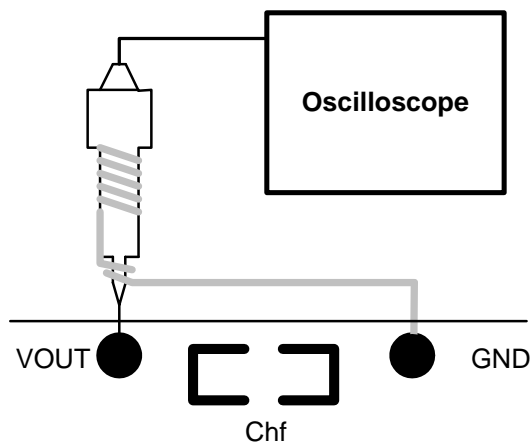
$$R_{fbb} = R_{fbt} / [(V_{OUT} / 1.24) - 1] \quad (3)$$

A value of 150k $\Omega$  is suggested for  $R_{fbt}$ . Then,  $R_{fbb}$  can be selected using [Equation 3](#). A 39pF capacitor ( $C_{ff}$ ) connected across  $R_{fbt}$  helps in feeding back most of the AC ripple at  $V_{OUT}$  to the FB pin. This helps reduce the peak-to-peak output voltage ripple as well as improve the efficiency of the step-up regulator, because a set hysteresis of 30mV at the FB pin is used for the gated oscillator control scheme.

## 4.5 Typical Test Setup



**Figure 2. Efficiency Measurements**



**Figure 3. Voltage Ripple Measurements**

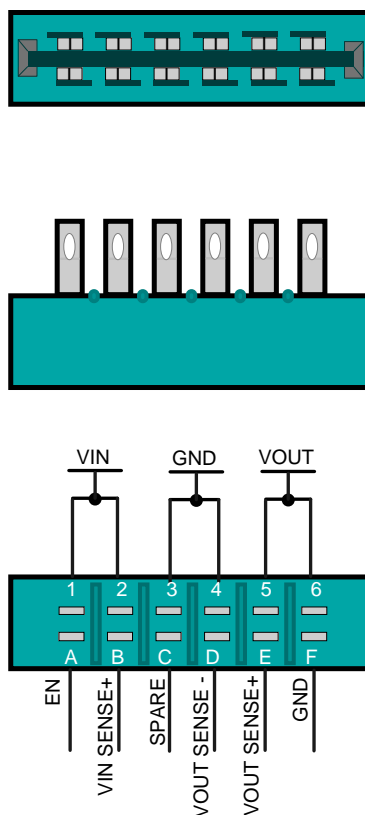
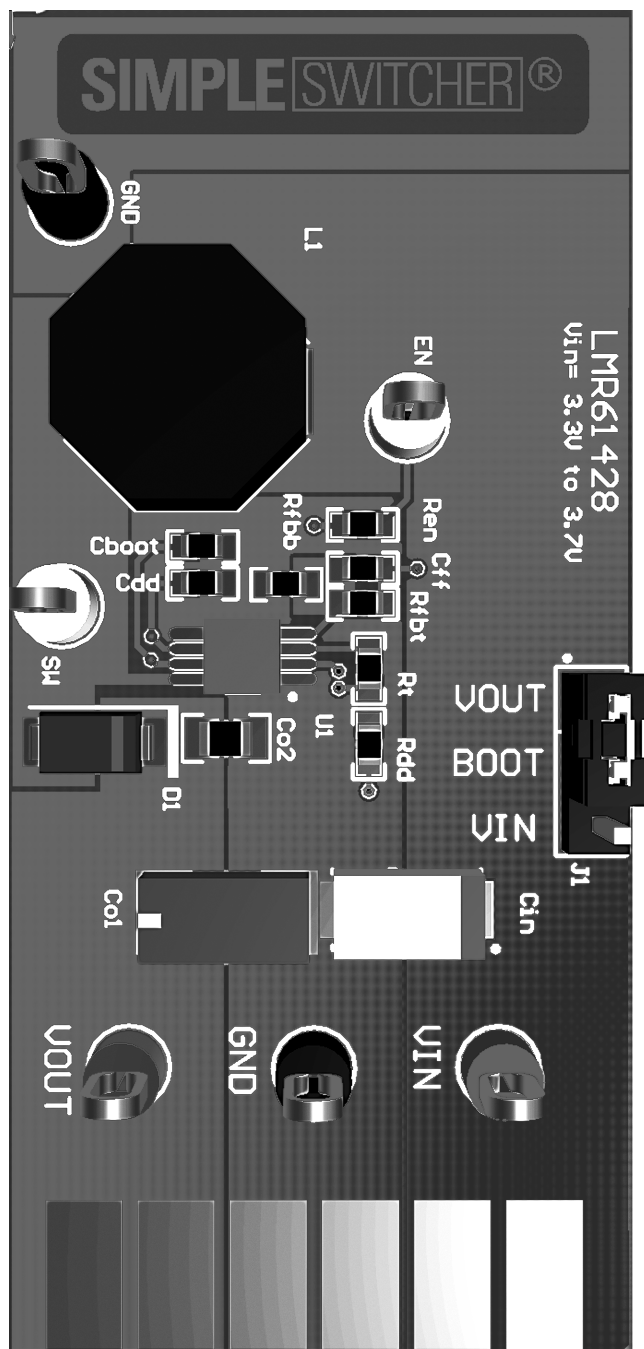


Figure 4. Edge Connector Schematic

#### 4.6 Board Images



### Figure 5. Top Side

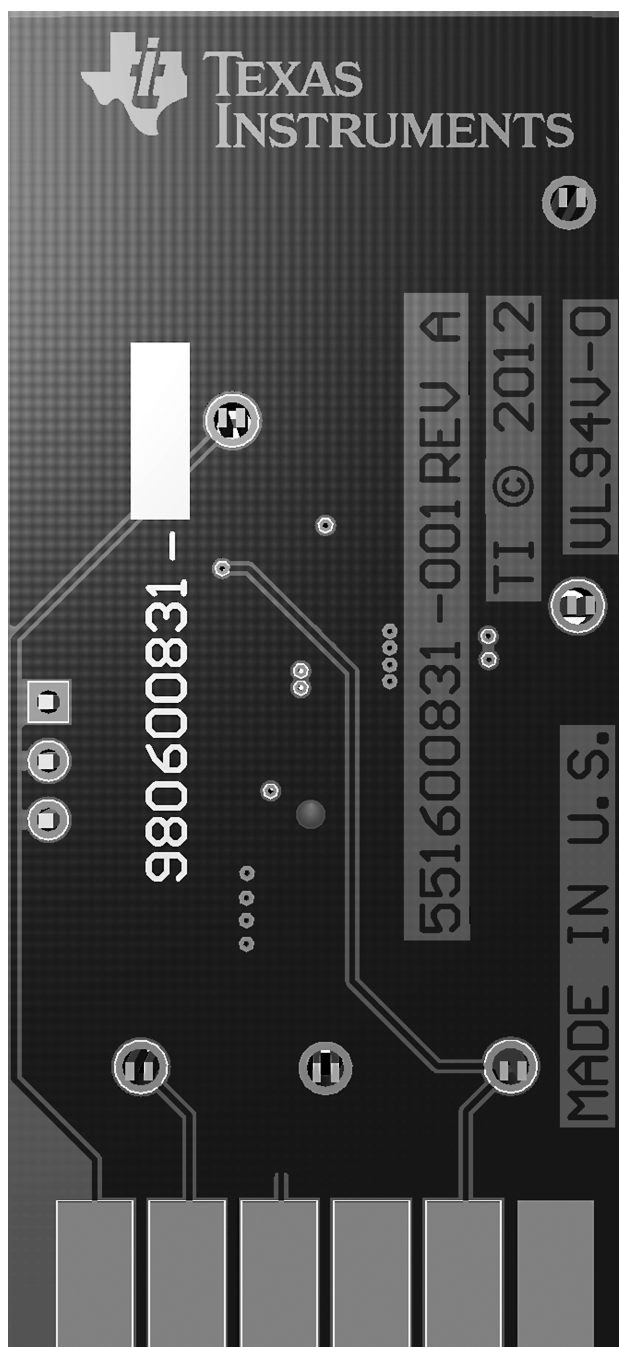
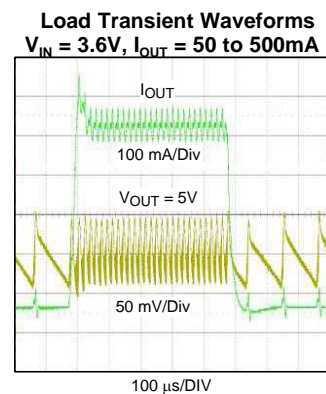
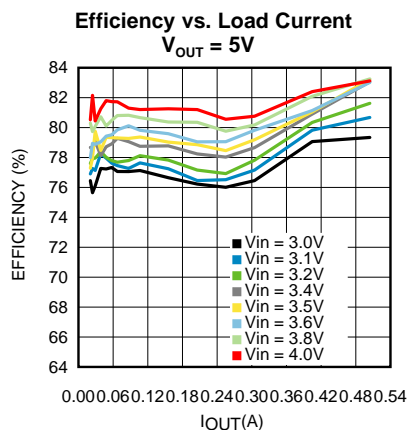
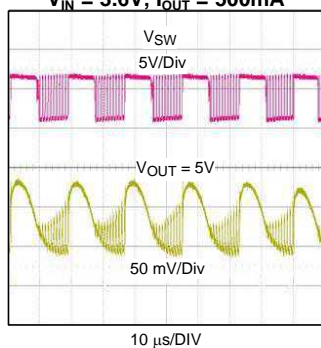


Figure 6. Bottom Side

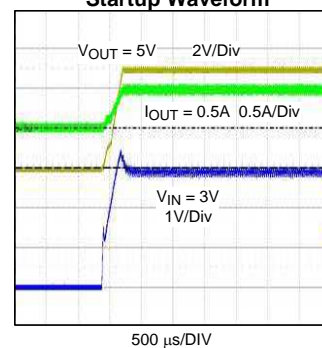
## 5 Typical Performance Characteristics



**Switching Node and Output Voltage Waveforms**  
 $V_{IN} = 3.6V, I_{OUT} = 500mA$



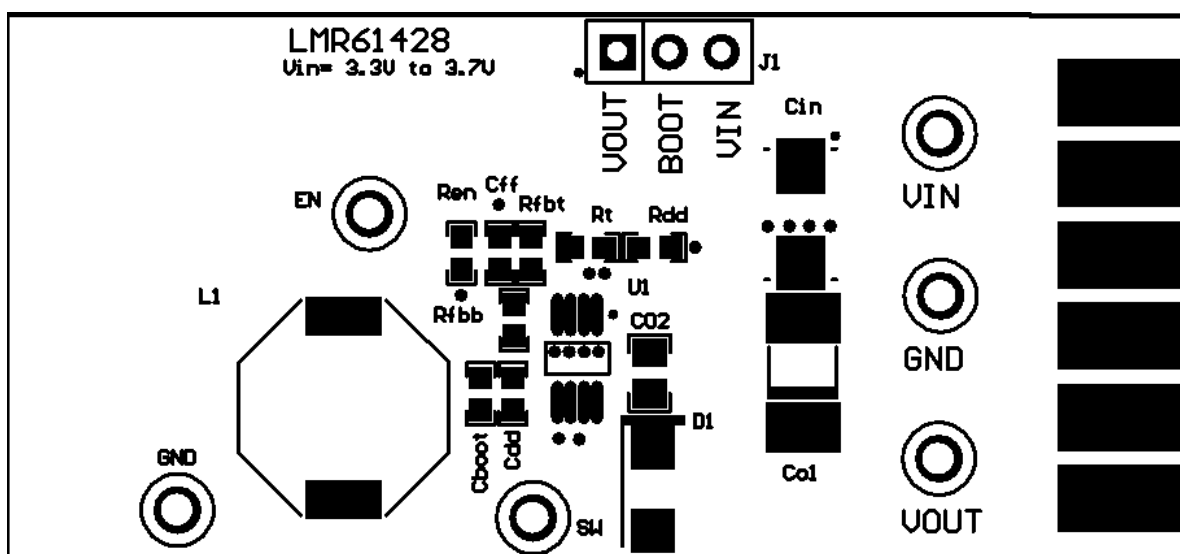
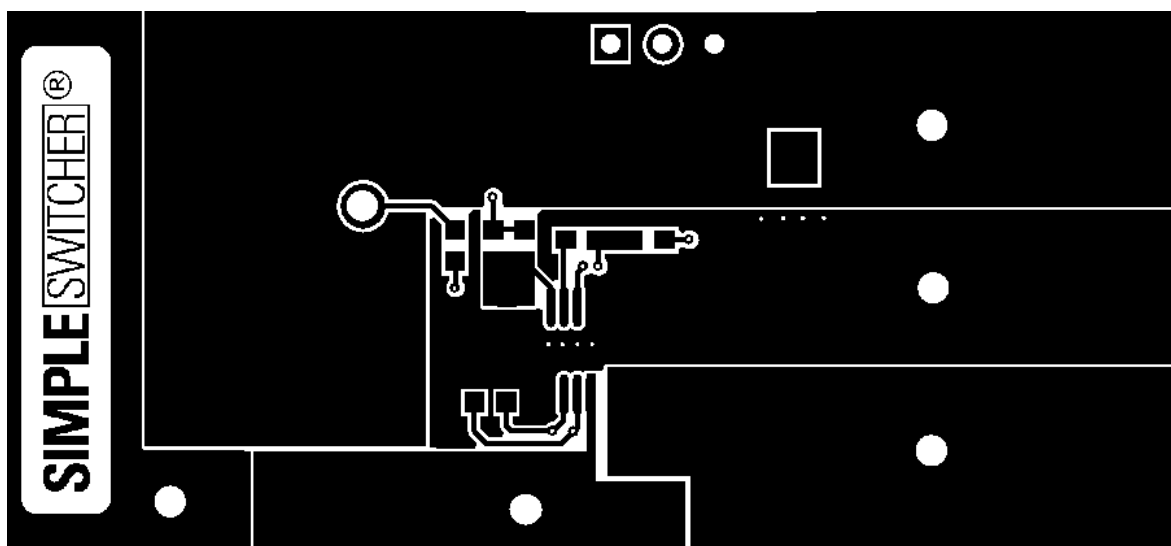
**Startup Waveform**



## 6 Bill of Materials

ID	Part Number	Type	Size	Parameters	Qty	Vendor
U1	LMR61428	Boost Regulator	SOT-23		1	Texas Instruments
L1	SRU1048-8R2Y	Inductor	SMD	8.2uH, 4.6A, 0.015 ohm,	1	Bourns
D1	B220A-13-F	Diode	SMA	Schottky, 20V, 2A	1	Toshiba
Cin	293D226X9010C2TE3	Capacitor	SMD	Tantalum, 22uF, 10V	1	Vishay-Sprague
Co1	594D686X0010C2T	Capacitor	SMD	Tantalum, 68uF, 10V	1	Vishay-Sprague
Co2	08053D105KAT2A	Capacitor	0805	Ceramic, 1uF, 25V, X5R	1	AVX
Cdd	C0603C105K4PACTU	Capacitor	0603	Ceramic, 1uF, 16V, X5R	1	Kemet
Cff	GRM1885C2A390JA01D	Capacitor	0603	Ceramic, 39pF, 100V, C0G/NP0	1	MuRata
Rfbt	RG1608P-154-B-T5	Resistor	0603	150 kΩ	1	Susumu Co Ltd
Rfbb	RG1608P-4992-B-T5	Resistor	0603	49.9 kΩ	1	Susumu Co Ltd
Rt	CRCW0603118KFKEA	Resistor	0603	118 kΩ	1	Vishay-Dale
Rdd	CRCW060349R9FKEA	Resistor	0603	49.9 Ω	1	Vishay-Dale
Ren	CRCW060310K0FKEA	Resistor	0603	10.0 kΩ	1	Vishay-Dale
EN	5014	Test Point Loop		Yellow	1	Keystone
VIN	5010	Test Point Loop		Red	1	Keystone
VOUT	5013	Test Point Loop		Orange	1	Keystone
GND	5011	Test Point Loop		Black	2	Keystone
SW	5012	Test Point Loop		White	1	Keystone
J1	PBC03SAAN	Header		100mil, 1x3	1	Sullins Connector Solutions
SH-J1	969102-0000-DA	Shunt		100mil, Black	1	3M





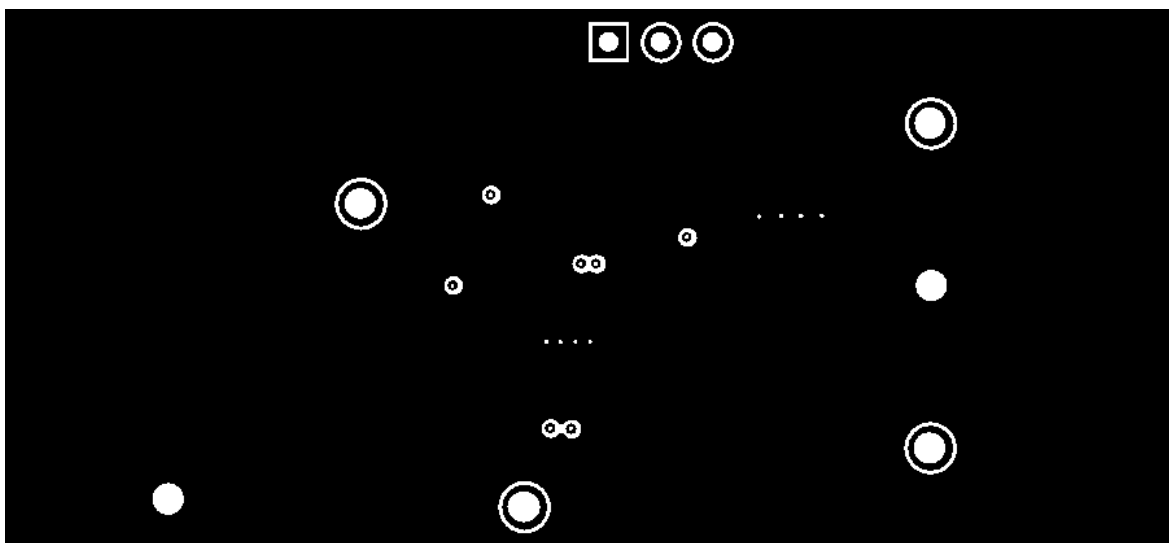


Figure 9. Internal Layer 1

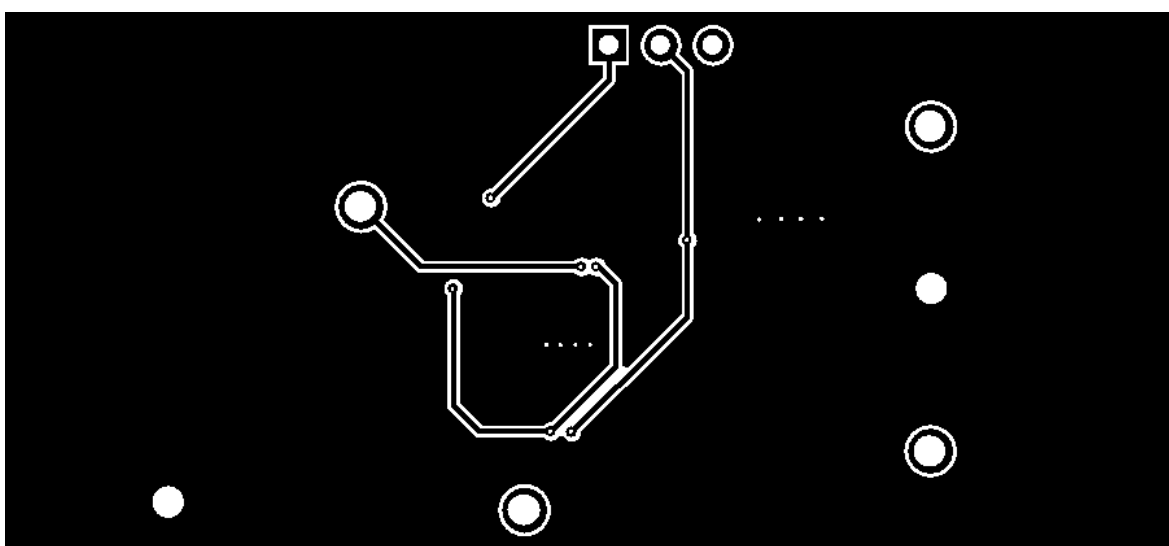


Figure 10. Internal Layer 2

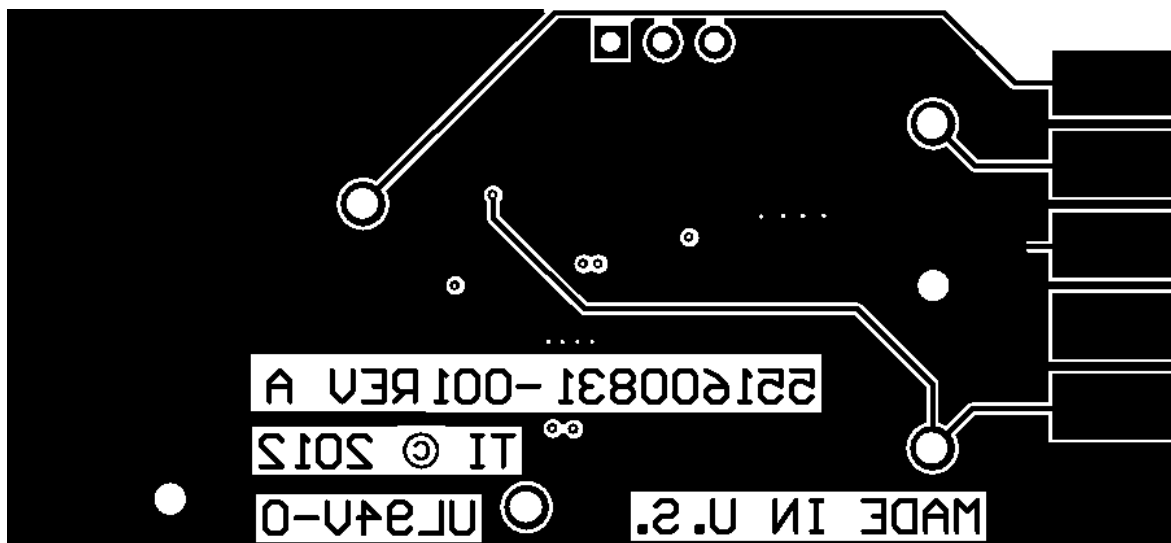


Figure 11. Bottom Copper

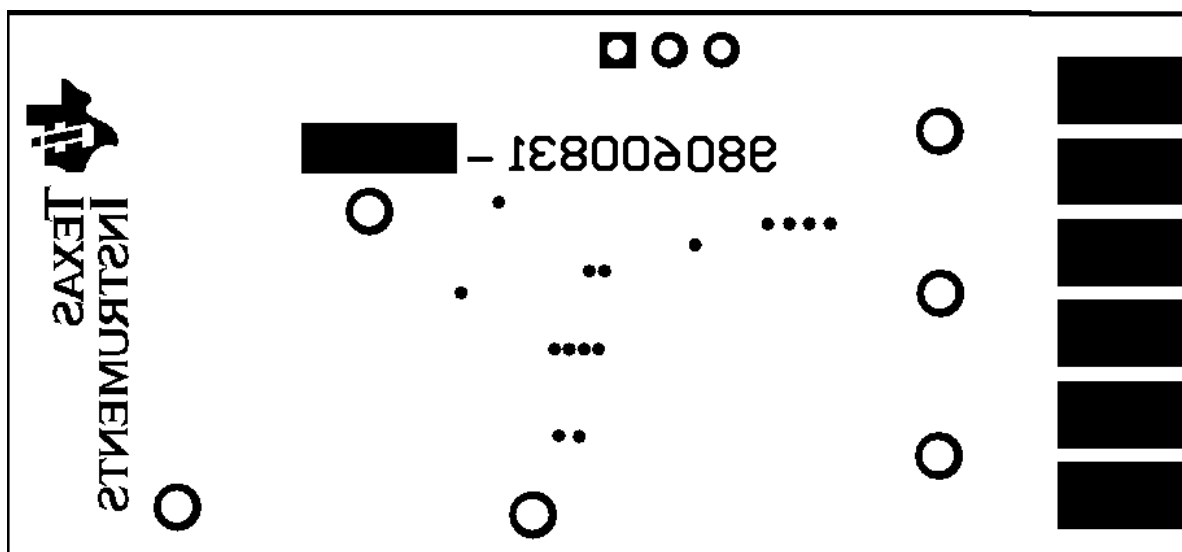


Figure 12. Bottom Overlay

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### 3 Regulatory Notices:

#### 3.1 United States

##### 3.1.1 Notice applicable to EVMs not FCC-Approved:

**FCC NOTICE:** This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.

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Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

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#### **FCC Interference Statement for Class B EVM devices**

*NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:*

- *Reorient or relocate the receiving antenna.*
- *Increase the separation between the equipment and receiver.*
- *Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.*
- *Consult the dealer or an experienced radio/TV technician for help.*

#### 3.2 Canada

##### 3.2.1 For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210 or RSS-247

#### **Concerning EVMs Including Radio Transmitters:**

This device complies with Industry Canada license-exempt RSSs. Operation is subject to the following two conditions:

(1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

#### **Concernant les EVMs avec appareils radio:**

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

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Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

### Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur.

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2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
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Last updated 10/2025