**File Description**

‘TS3A226E\_fet\_ron\_TB\_windows.sp’ -which is the test bench- invokes other 2 files:

‘process\_models\_windows.lib’ –nominal process library, and

‘TS3A226E\_subckt\_window.inc’ –subcircuits’ definition

**Instructions**

1) Open your simulator HSPICE\_A-2008.03-SP1 on Windows

2) On the file menu, open and run file ‘TS3A226E\_fet\_ron\_TB\_windows.sp’.

Plots of the test-setup schematic and simulation results are shown below:



