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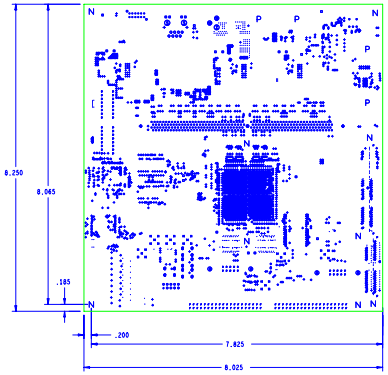
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

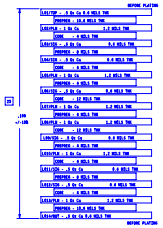
NOTES UNLESS OTHERWISE SPECIFIED:

1. PCB VENDOR MUST NOTIFY RESPONSIBLE PCB CONTACT OF ANY DISCREPANCIES FOUND BETWEEN FABRICATION DATA AND FABRICATION DRAWING NOTES.
2. THIS DRAWING IS VIEWED FROM THE PLOTMAP OF TOP SIDE OF THE PCB.
3. FABRICATION OF THIS PCB SHALL BE IN COMPLIANCE WITH THE FOLLOWING SPECIFICATIONS: IPC-4101 CLASS 2 (REVISED).
4. FABRICATION OF THIS PCB TO BE ACCEPTABLE TO IPC-A-600 CLASS 2 (LATEST REVISION).
5. ALL DIMENSIONAL LIMITS APPLY AFTER PLATING OR PROCESSING.
6. TOLERANCES OF DATUM HOLE TO, DOWEL DOWN LOCATIONS +/- 0.001, DRILLED HOLE LOCATIONS +/- 0.002.
7. [1] BASE MATERIAL: FR4-A EPOXY.
8. FLAME CLASS: UL 94V-0 @ MEET REQUIREMENTS OF UL746.
9. MANUFACTURER MUST BE UL RECOGNIZED TO PRODUCE THIS PRODUCT SUCH THAT IT MEETS 170 DEGREES CELSIUS MAXIMUM OPERATING TEMPERATURE (TOT).
10. [2] THE FOLLOWING MUST BE MARKED ON SECONDARY SIDE OF PCB IN AREA AS SHOWN:
    a) DATE CODE.
    b) UL RECOGNIZED VENDOR ID, UL TYPE DESIGNATION 94V-0 MARKING WHICH REFLECT THE APPLICATION FLAME CLASS AND MAXIMUM OPERATING TEMPERATURE RATINGS.
11. [3] PCB VENDOR TO ENSURE ALL UNCONNECTED (NON-FUNCTIONAL) INTERNAL COPPER, SUCH AS THOSE ON PLANE ARE REMOVED.
12. [4] GOLD FLASH / PLATING - 0.001 MIN. OVERLAY / 0.0002 ABSOLUTE MIN. PLATING. SELECTIVE DEPOSITION NOT SPECIFIED AFTER PLATING (SEE HOLE DIAGRAM).
13. [5] VIAS TO BE FILLED WITH NON-CONDUCTIVE EPOXY.
14. AFTER REVISIONING FABRICATION DATA, PCB VENDOR MUST DISCUSS MINIMUM COPPER THICKNESS IS NECESSARY WITH THE RESPONSIBLE PCB CONTACT. ONCE DETERMINED NECESSARY, A MINIMUM OF SIX HOLE FOUR-WAY STAKES COPPER PLATING ON THE BOARD MUST BE INCORPORATED.
15. BREAK-REMOVAL SHALL NOT EXCEED GREATER THAN 0.001.
16. FINISHED CONDUCTOR WIDTH MUST TO BE REDUCED MORE THAN 50% OF MINIMUM WIDTH OR SPACING. FINISHED CONDUCTOR SPACING MUST NOT BE REDUCED TO BE REDUCED MORE THAN 20% OF MINIMUM SPACING FROM ARTWORK ARTWORK.
17. INTERNAL ANNULAR RING 0.001 MINIMUM. EXTERNAL ANNULAR RING 0.002 MINIMUM. BOTH ARE MEASURED AT LAND TO PAD EDGE. TEAR DROPPING OF TRACE TO PAD JUNCTION IS PERMITTED PROVIDED MINIMUM METAL-TO-METAL NOTCHING SPACING ARE NOT COMPROMISED. VIAS TO BE SOLID TO IPC-4552 - CLASS 2.
18. SOLDER MASK IS 1 LAYER PHOTO IMAGEABLE AND IN ACCORDANCE WITH IPC-SM-840C CLASS H. MINIMUM SOLDER MASK DAM IS 0.004 WIDE. SOLDER MASK TO BE APPLIED +/- 0.004 OF ITS NOMINATIVE WHEN SCREENED LAYERS. SOLDER MASK ALIGNMENT TOLERANCE BETWEEN SOLDERMASK PADS ARE THE SAME SIZE (1:1) TO PROVIDE UP TO .002 DIAMETER CLEARANCE FROM PADS TO VIA PROVIDED NO ADJACENT COPPER IS EXPOSED AND NO CONFLICT IS PRODUCED WITH ANY STATED "VIA TENTING/PLUGGING" REQUIREMENTS.
19. PCB VENDOR TO ENSURE ALL VIAS ARE COMPLETELY FREE OF SOLDERMASK ON TOP SIDE OF PCB.
20. [6] PLATING WITH 125UIN WITH 110-250 MICRO INCHES MINIMUM ELECTROLESS NICKEL (EN) FOLLOWED BY 2-400 MICRO INCHES IMMERSION GOLD (AU).
21. REMOVE ALL SHARP EDGES AND BURRS 0.004" MAXIMUM.
22. [7] CLEARANCES VIOLATIONS, NON-CONNECTIVE, ARE NOT DUE TO ERRORS OR EXPOSED COPPER ON THE PCB. ALL CLEARANCES ARE IN ACCORDANCE WITH 0.005 MIN. THE OF SOLDER MASK COVERED PADS IS PERMITTABLE. EXCEPTION OF SOLDERMASK 0.003 MIN. FROM PADS IS PERMITTED.
23. SHOP & TRUST SHALL BE DETERMINED BY PHYSICAL MEASUREMENT AND PROCEDURAL CALCULATION IN ACCORDANCE WITH IPC-TM-650, METHOD 2.4.22. SHOP AND TRUST MAY NOT EXCEED 0.75.
24. LOOP CONTINUITY AND ISOLATION ELECTRICAL TESTING PER CURRENT IPC TEST SPECIFICATION AND LATEST PCB TOOL PCB TEST DATA MUST BE CROSS-REFERENCED TO IPC-D-356 FILE.
25. [8] CONTROLLED IMPEDANCE TRACES ARE AS FOLLOWS:
    a) TOLERANCE (ALL CLASS): +/- 10%.
    b) SINGLE ENDED TRACES TO BE 10 OHM. (2 MILS TRACES TOP AND BOTTOM LAYERS).
    c) SINGLE ENDED TRACES TO BE 150 OHMS. (3 MILS TRACES TOP AND BOTTOM LAYERS).
    d) DIFFERENTIAL TRACES TO BE 100 OHMS. (3 MIL TRACES 4 MIL SPACE, LAYERS 1,4,8,11).
26. PCB VENDOR TO PROVIDE ONE TEST COUPON AND ONE CROSS SECTION PER LOT WITH SHIPMENT.
27. PCB VENDOR TO PROVIDE 2 SOLDER SAMPLES WITH FIRST SHIPMENT.
28. PCB MUST BE RoHS COMPLIANT AND SUPPORT LEAD FREE ASSEMBLY. NON REFLOW OF ENG DOCUMENT # (TO FOLLOW).



PRIMARY SIDE VIEW

DRILL CHART: TOP to BOTTOM ALL UNITS ARE IN MILS			
FIGURE	SIZE	PLATED	QTY
-	10.0	PLATED	389
+	10.0	PLATED	3112
+	14.0	PLATED	60
+	28.0	PLATED	240
+	32.0	PLATED	12
+	35.0	PLATED	21
+	35.0	PLATED	9
+	35.0	PLATED	8
+	38.0	PLATED	35
+	38.0	PLATED	80
+	39.0	PLATED	16
+	40.0	PLATED	21
+	43.0	PLATED	5
+	50.0	PLATED	2
+	62.0	PLATED	2
+	67.0	PLATED	35
◆	96.0	PLATED	2
●	120.0	PLATED	2
N	125.0	PLATED	9
ⓞ	140.0	PLATED	1
P	170.0	PLATED	4
+	40.0	NON-PLATED	2
+	41.0	NON-PLATED	2
●	96.0	NON-PLATED	1
ⓞ	110.0	NON-PLATED	4
ⓞ	126.0	NON-PLATED	2



DETAIL A
SCALE: NONE
(SHOWN BOTTOM)

			DATE	CHK	TEXAS INSTRUMENTS
			SPEC	ENG CHK	
-	SS-003-3006			RELEASE	PCB FABRICATION TSM3100
DASH	NEXT ASSY	USED ON	FROM	RELEASED BY:	
APPLICATION					
THIRD ANGLE PROJECTION	UNLESS OTHERWISE SPECIFIED: DIMENSIONS ARE IN INCHES IN ACCORDANCE WITH ANSI Y14.5. DRILL TOLERANCES ARE PER CHART		TOLERANCES: FRC .X ± FRC .XX ±1° FRC .XXX ± FRACTIONS ANGLES ±	CONTRACT NO.	SIZE D / CAGE CODE N/A / DWG NO. / REV E2 SCALE: 1/1 SHEET 1 OF 1