

Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ3


Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

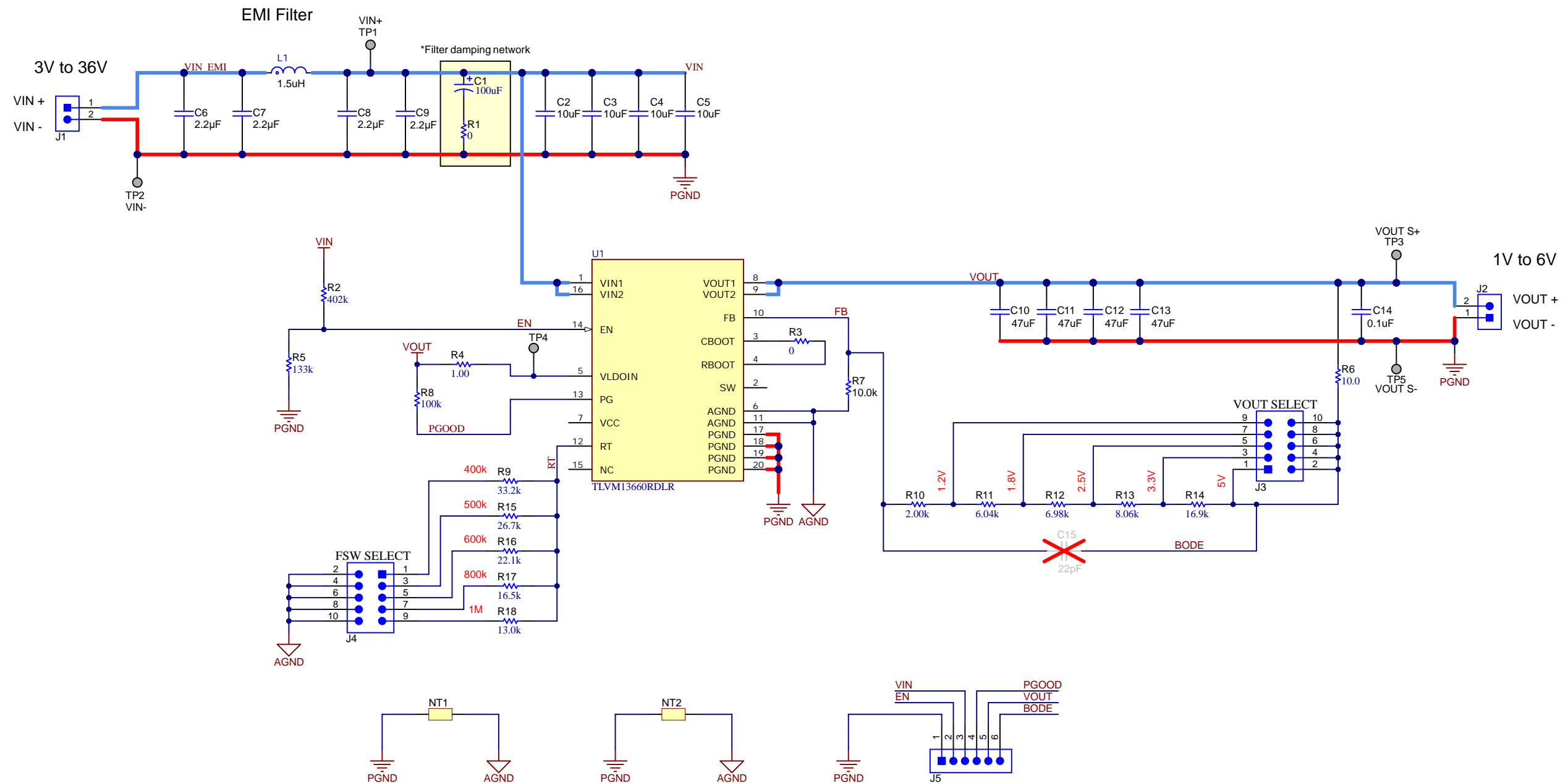
ZZ4
Assembly Note
Place SH-J1 on J3 pins 1 and 2

ZZ5
Assembly Note
Place SH-J2 on J4 pins 9 and 10

ZZ6
Assembly Note
Place SH-J3 on J5 pins 5 and 6

Orderable: TLMV13660EVM	Designed for: Public Release	Mod. Date: 2/2/2022	 TEXAS INSTRUMENTS http://www.ti.com © Texas Instruments 2020
TID #: N/A	Project Title: CruzlineValueModuleEVM		
Number: BSR210	Rev: A	Sheet Title: TLMV13660EVM schematic	
SVN Rev: Not in version control		Assembly Variant: 001	
Drawn By: T. Jacobs		File: BSR210AHardware.SchDoc	
Engineer: T. Hegarty		Contact: http://www.ti.com/support	Size: B

Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.



Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable: TLVM13660EVM	Designed for: Public Release	Mod. Date: 2/2/2022
TID #: N/A	Project Title: CruzlineValueModuleEVM	
Number: BSR210	Rev: A	Sheet Title: TLVM13660EVM schematic
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 2 of 2
Drawn By: T. Jacobs	File: BSR210A.SchDoc	Size: B
Engineer: T. Hegarty	Contact: http://www.ti.com/support	