

Revision History				
Rev	ECN #	Approved Date	Approved by	Notes
N/A	N/A	N/A	N/A	N/A

Hardware
PRU_IO-Link_EVM_Hardware.SchDoc

Repeat(IO,1,8)
PRU_IO-Link_IOS.SchDoc

Repeat(EN)

Repeat(RX)

Repeat(TX)

Repeat(DI)

Repeat(FAULT)

Repeat(L+)

Repeat(LED_G)

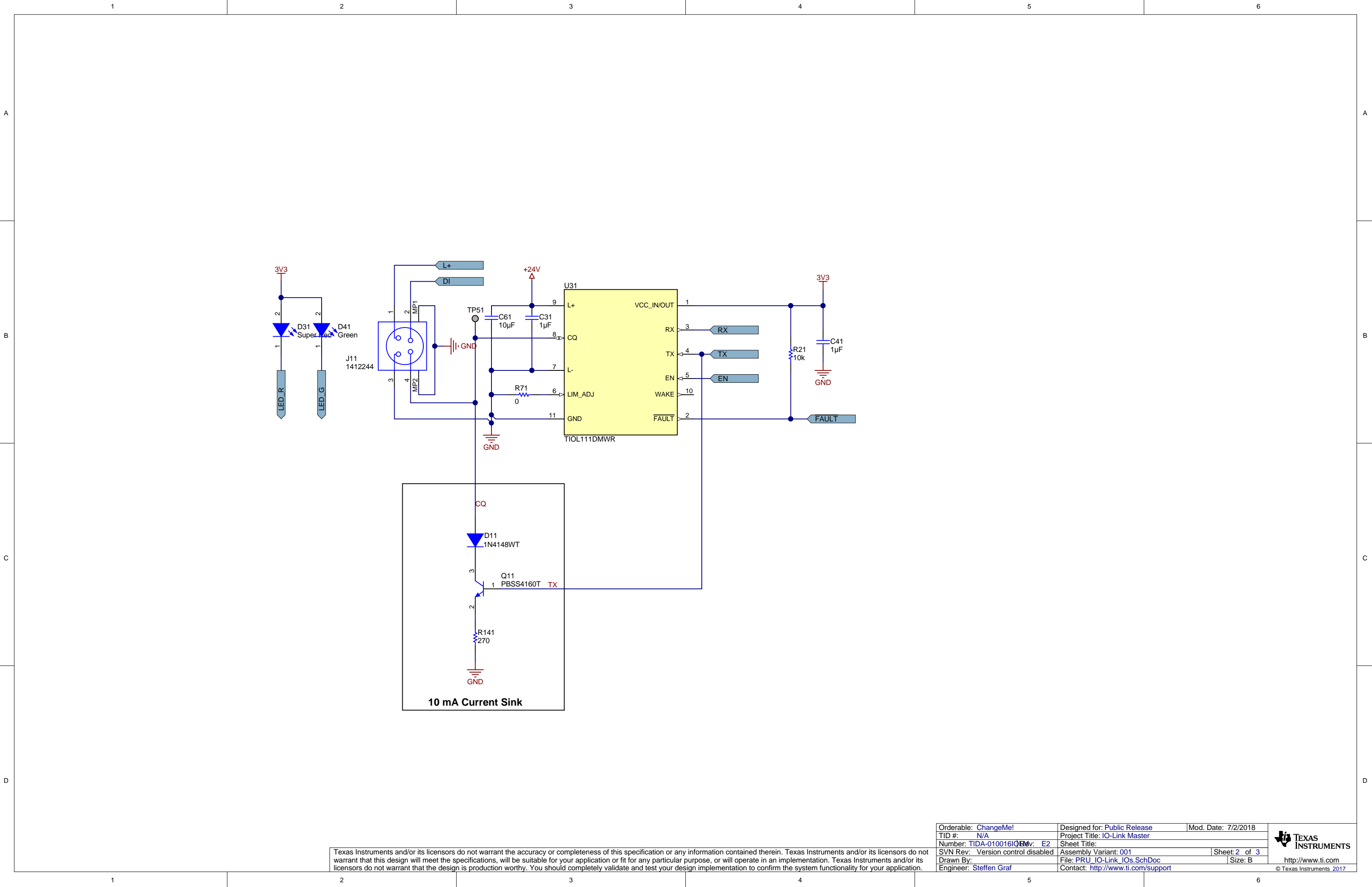
Repeat(LED_R)

LED_G LED_G(1..8)

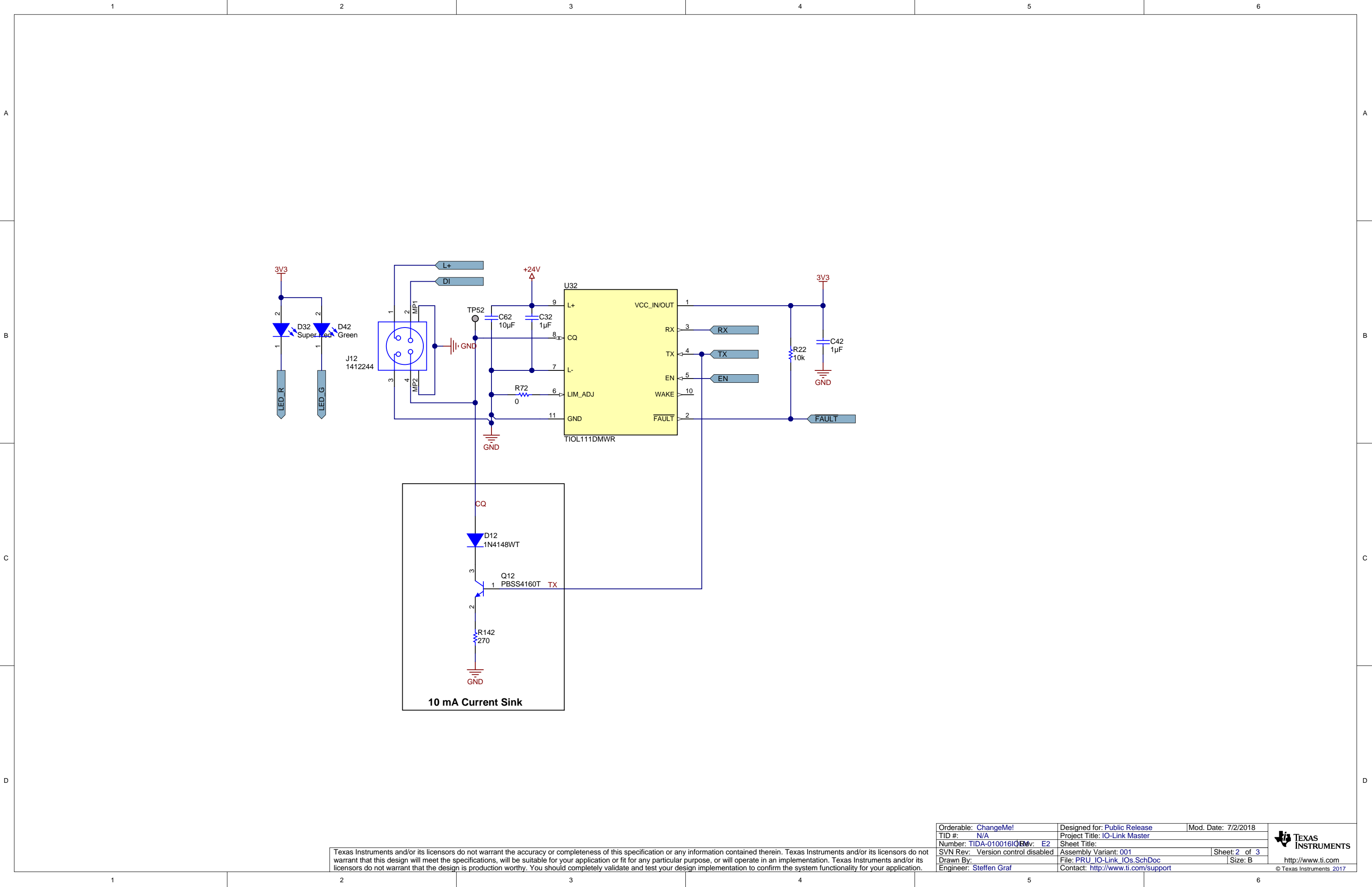
LED_R LED_R(1..8)

Orderable: ChangeMe!	Designed for: Public Release	Mod. Date: 6/8/2018
TID #: N/A	Project Title: IO-Link Master	
Number: TIDA-010016	Rev: E2	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 1 of 3
Drawn By:	File: PRU_IO-Link_CoverSheet.SchDoc	Size: B
Engineer: Steffen Graf	Contact: http://www.ti.com/support	

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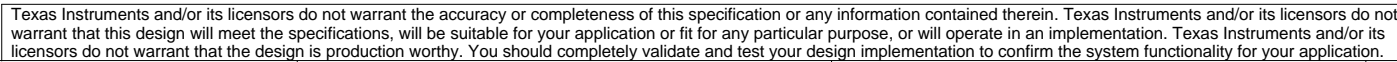


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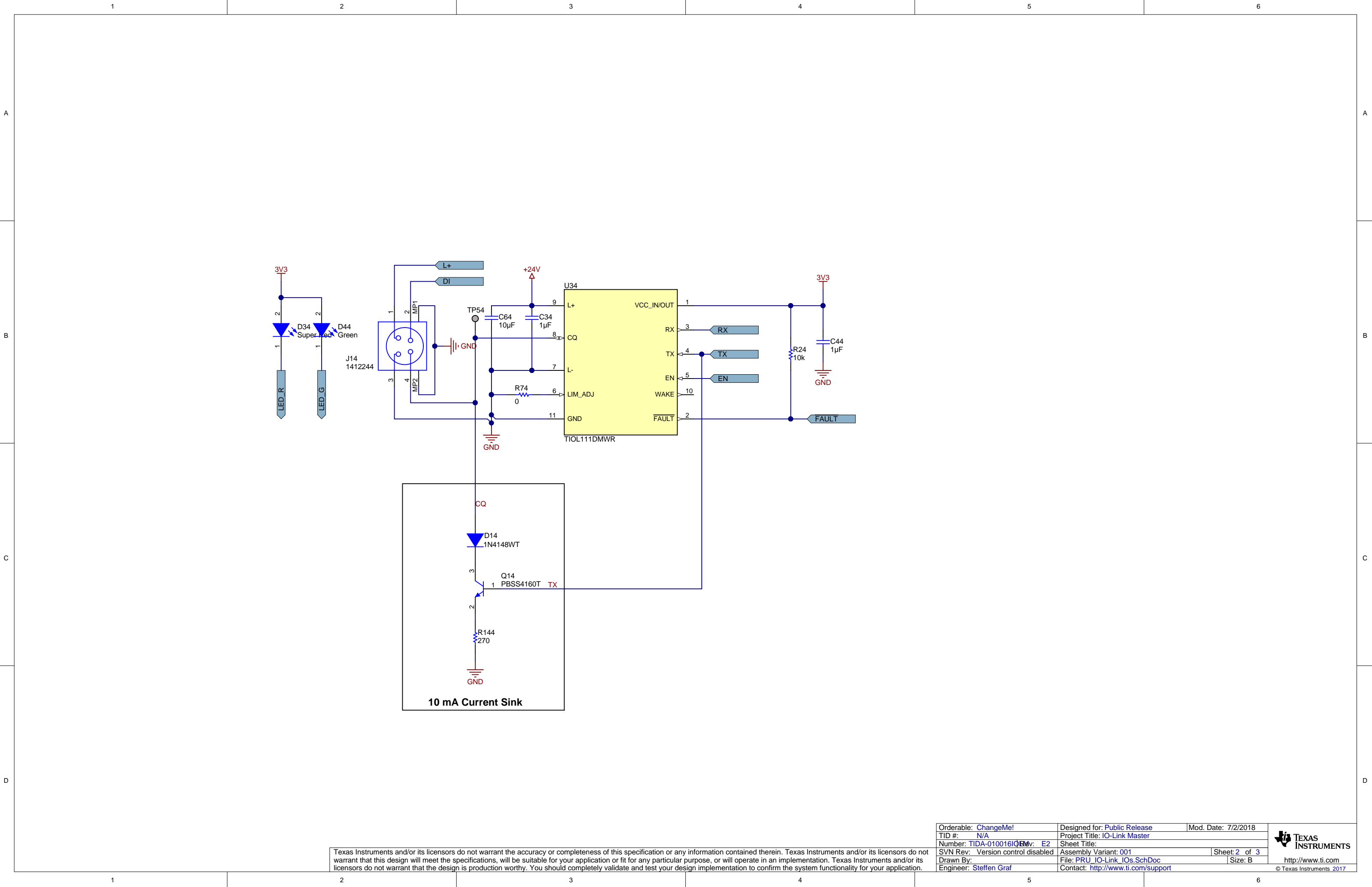


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Orderable: ChangeMe!	Designed for: Public Release	Mod. Date: 7/2/2018
TID #: N/A	Project Title: IO-Link Master	
Number: TIDA-0100161	Rev: E2	Sheet: 2 of 3
SVN Rev: Version control disabled	Assembly Variant: 001	Size: B
Drawn By: Steffen Graf	File: PRU_IO-Link_IOS.SchDoc	
Engineer: Steffen Graf	Contact: http://www.ti.com/support	

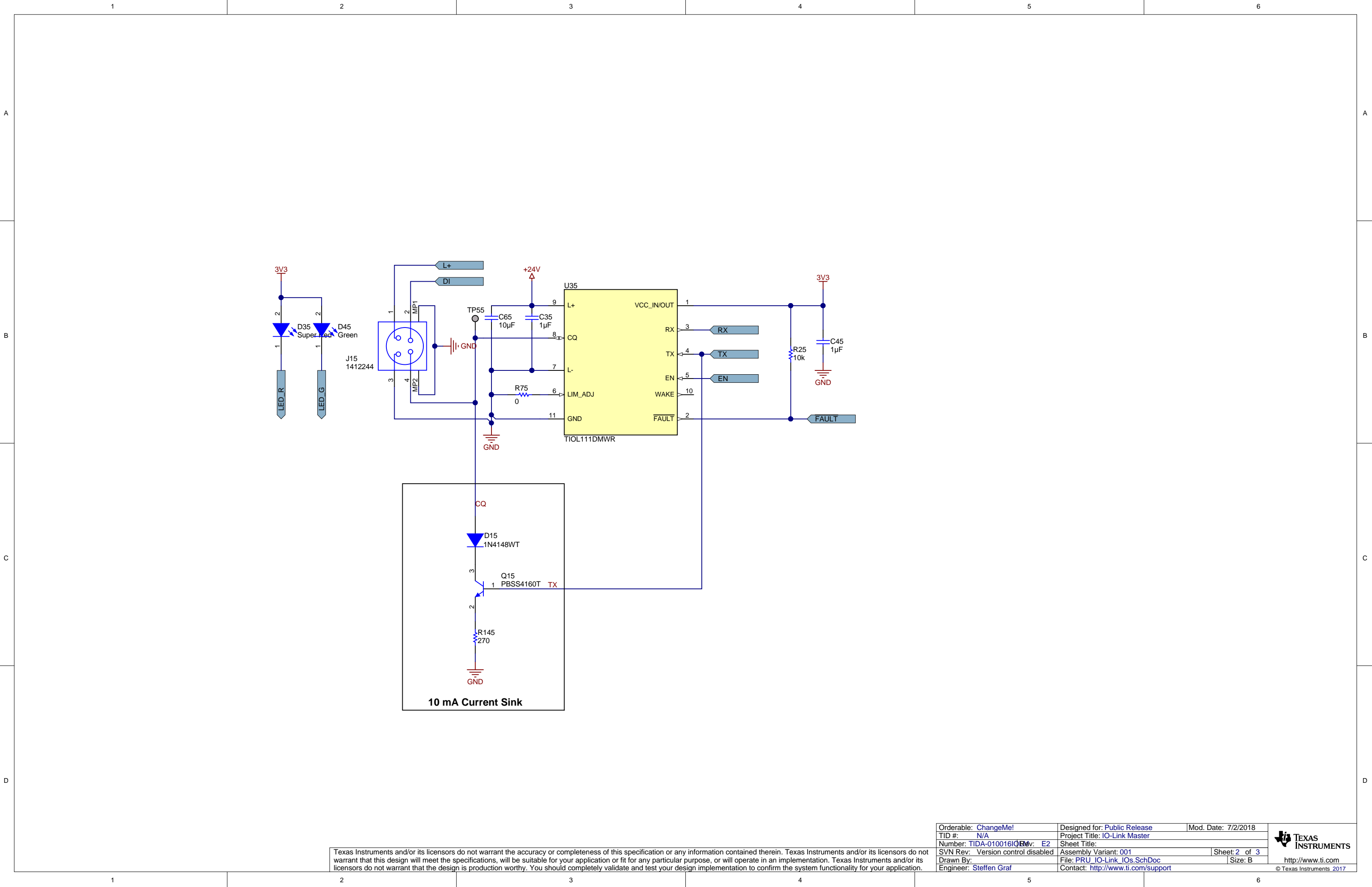


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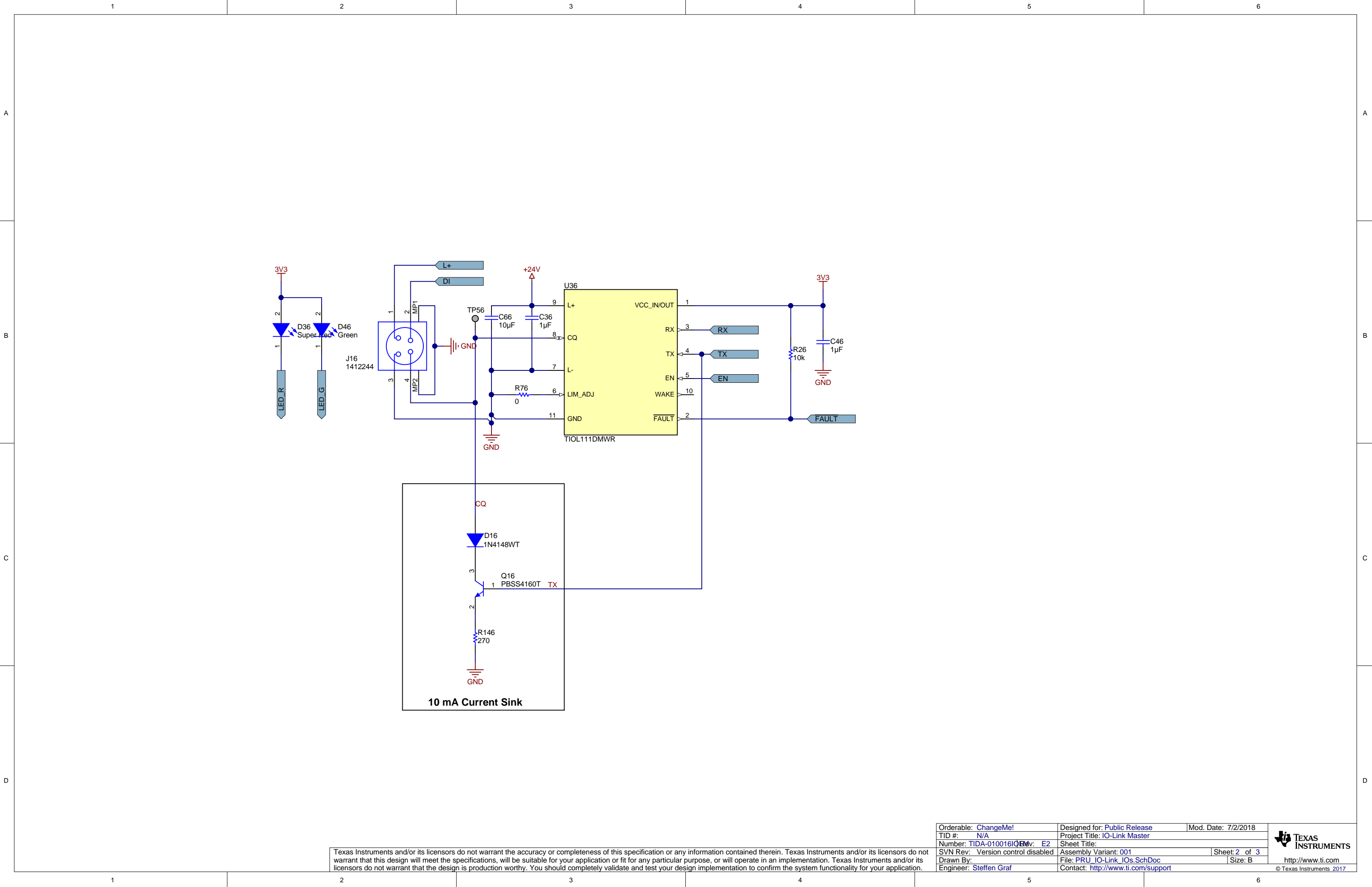
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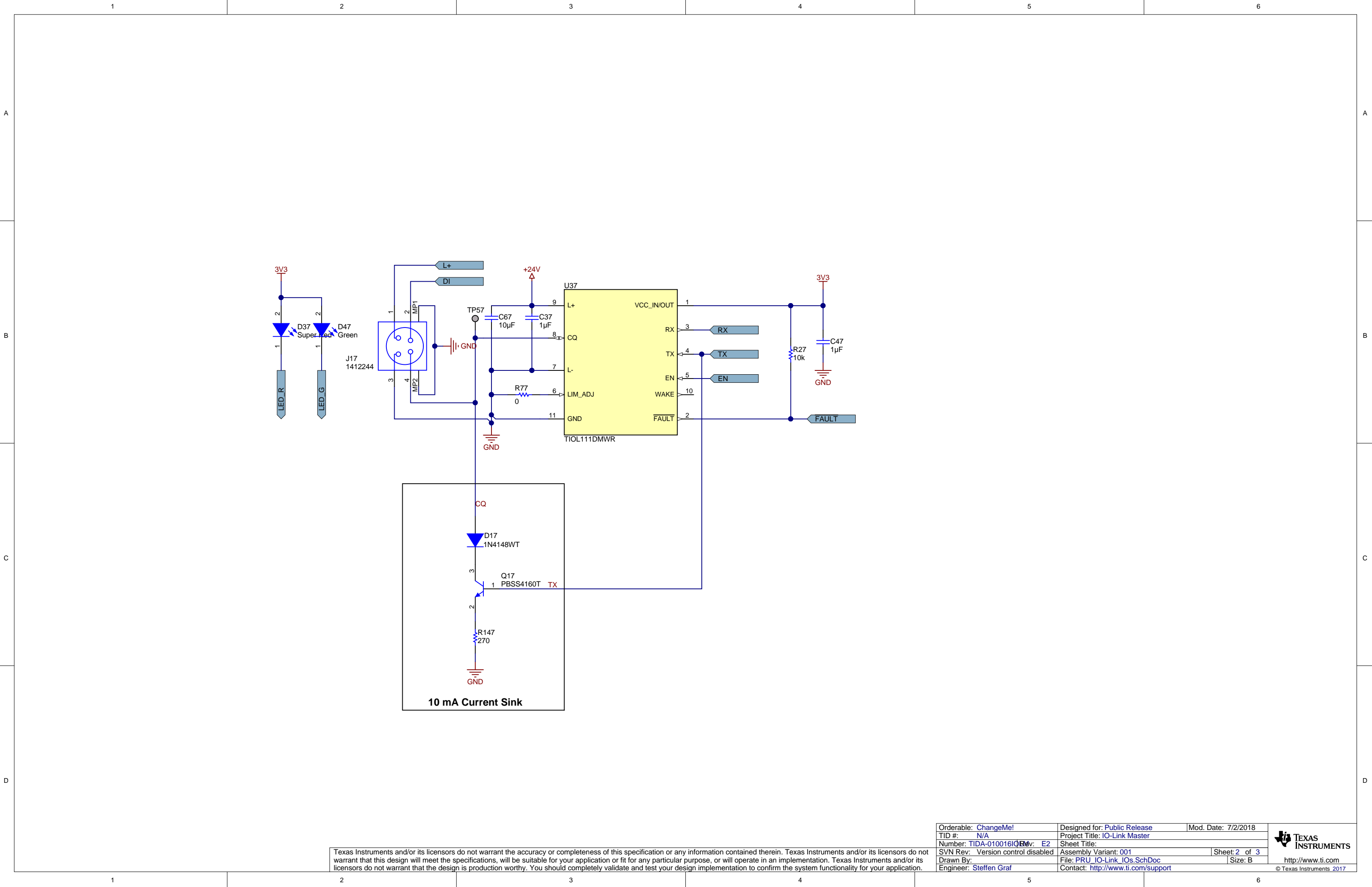
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PCB
LOGO
FCC disclaimer

PCB
LOGO
WEEE logo

PCB Number: TIDA-010016IOLM
PCB Rev: E2

LBL1
PCB Label
THT-14-423-10
Size: 0.65" x 0.20 "

ZZ1
Label Assembly Note
This Assembly Note is for PCB labels only

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Variant/Label Table	
Variant	Label Text
001	Default Variant

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