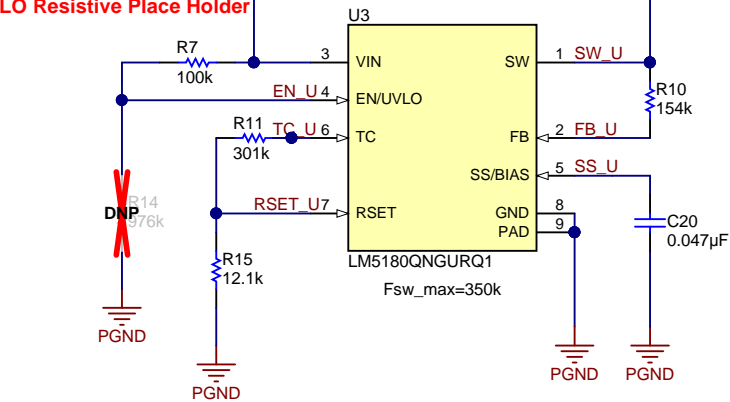
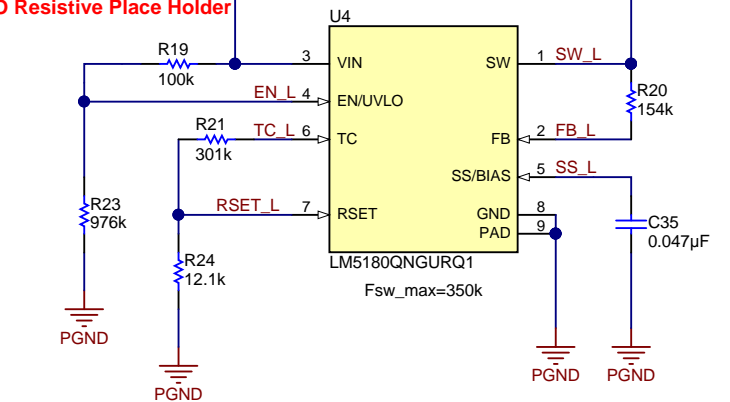


UVLO Resistive Place Holder



UVLO Resistive Place Holder



Orderable: N/A	Designed for: Public Release	Mod. Date: 4/1/2019
TID #:	Project Title: LM5180_based_Flyback	
Number: TIDA-020014	Rev: E1	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 2 of 3
Drawn By: X.Gong	File: TIDA_020014_LM5180_based_Flyback.SchDoc	Size: B
Engineer: X.Gong	Contact: http://www.ti.com/support	



FID1



FID2



FID3

PCB Number: TIDA-020014  
PCB Rev: E1



Variant/Label Table	
Variant	Label Text
001	ChangeMe!
002	ChangeMe!

ZZ1

### Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ2


### Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ3

### Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Orderable: <a href="#">N/A</a>	Designed for: <a href="#">Public Release</a>	Mod. Date: 4/1/2019	 <a href="http://www.ti.com">http://www.ti.com</a> © Texas Instruments 2018
TID #:	Project Title: <a href="#">LM5180_based_Flyback</a>		
Number: <a href="#">TIDA-020014</a>   Rev: <a href="#">E1</a>	Sheet Title:		
SVN Rev: Version control disabled	Assembly Variant: <a href="#">001</a>	Sheet: <a href="#">3</a> of <a href="#">3</a>	
Drawn By:	File: <a href="#">TIDA_020014_LM5180_based_Flyback_Hardware_Rev2_001</a>		
Engineer: <a href="#">X.Gong</a>	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>		

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