

PR448
Virtex™-4 Design 4
TPS51020 Dual DC/DC Controller-based Power Management Solution Providing up to
6A from $V_{IN} = 5\text{ V}$

FEATURES:

- Powers one or more FPGAs
- Can be modified to provide currents up to 25 A per rail
- High efficiency minimizes heat
- Flexible dual controller (TPS51020) design allows optimization for input voltage (4.5V to 28V) size, power dissipation and/or cost
- The controller can be configured to provide DDR termination

IMPORTANT WEB LINKS:

- Link to the TI home page for Xilinx FPGA power management solutions at <http://www.ti.com/xilinuxfpga> for more information and other reference designs.
- Link to the datasheets at <http://focus.ti.com/lit/ds/symlink/TPS51020.pdf>
- Link to 40K design software tool at <http://focus.ti.com/docs/toolsw/folders/print/tps40k-sw.html> to assist further optimization/customization of design.

IMPLEMENTATION NOTES:

- **Sequencing:** Per Xilinx DS302 v1.3, Virtex-4 power rails can be turned on in any sequence, though V_{CCAUX} must power on before or with V_{CCO} for the minimum power-on current specifications shown in the Xilinx datasheet to apply. For this reason and to reduce the risk that large currents for charging bulk capacitance forces the point of load converter into current limit and/or pulls down the input supply at power up, the following recommended design techniques were used:
 - o Integrated soft-start configured with a capacitor to provide up to 5 ms monotonic rise time for V_{CCINT} , V_{CCAUX} and V_{CCO}
 - o Sequencing V_{CCINT} , V_{CCAUX} then V_{CCO} using the TPS3808 SVS and the PWRGD and ENABLE of the TPS51020
- **Additional Capacitance:** The TPS51020 controllers used for the V_{CCINT} , V_{CCAUX} and V_{CCO} rails have been compensated and soft-started to allow for a certain amount of additional bulk capacitance on the output rail, as specified below.

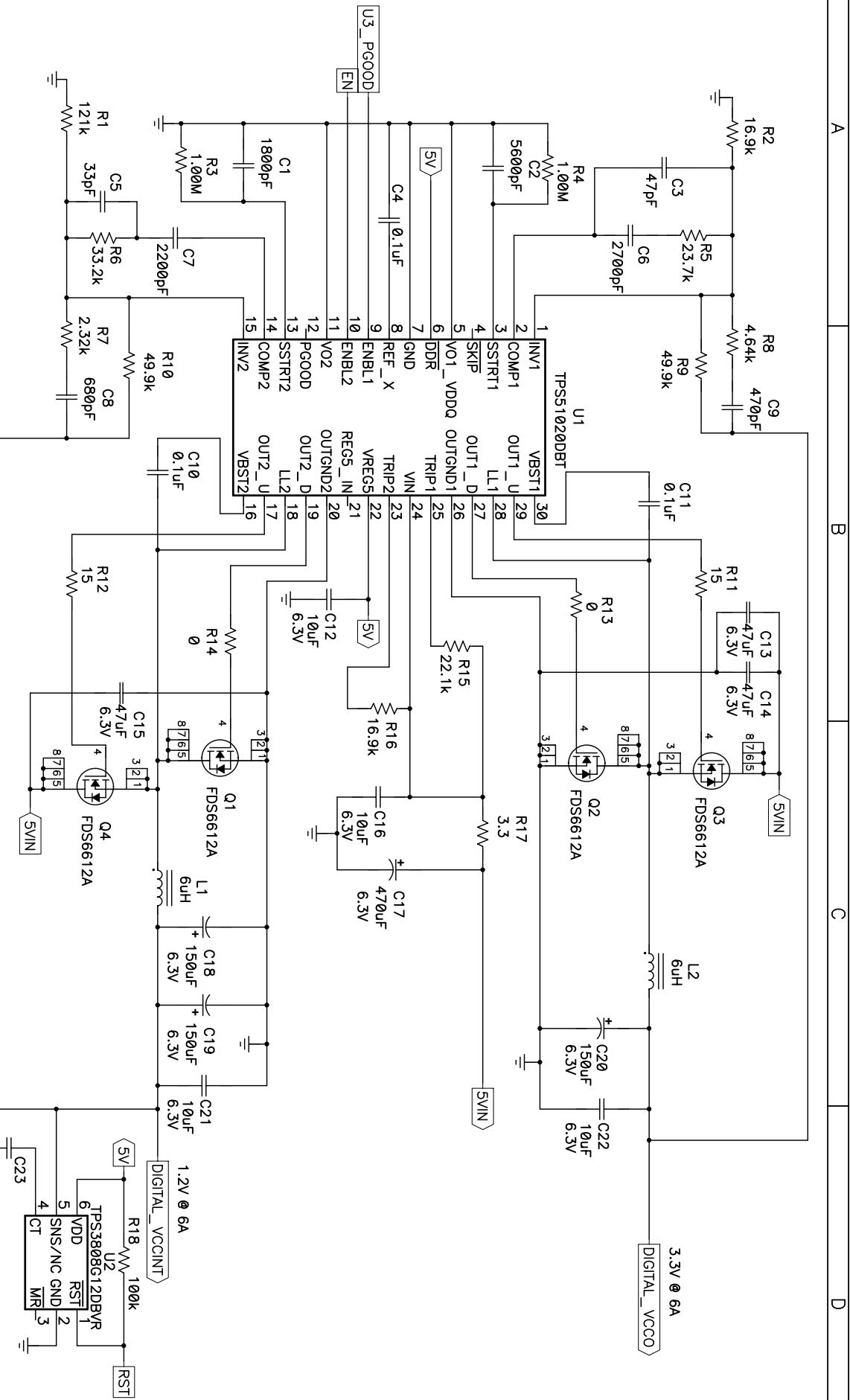
Capacitor (uF)	V_{CCO} QTY	V_{CCAUX} QTY	V_{CCINT} QTY
0.047	51	14	38
0.22	39	7	19
0.68	15	4	10
2.2	15	3	7
330 (0.15 Ω ESR)	15	1	3

If the amount of bulk capacitance increases, the TPS51020 control loop may need to be re-compensated and/or the soft-start timing may need to change. The 40K design software can help with these design changes.

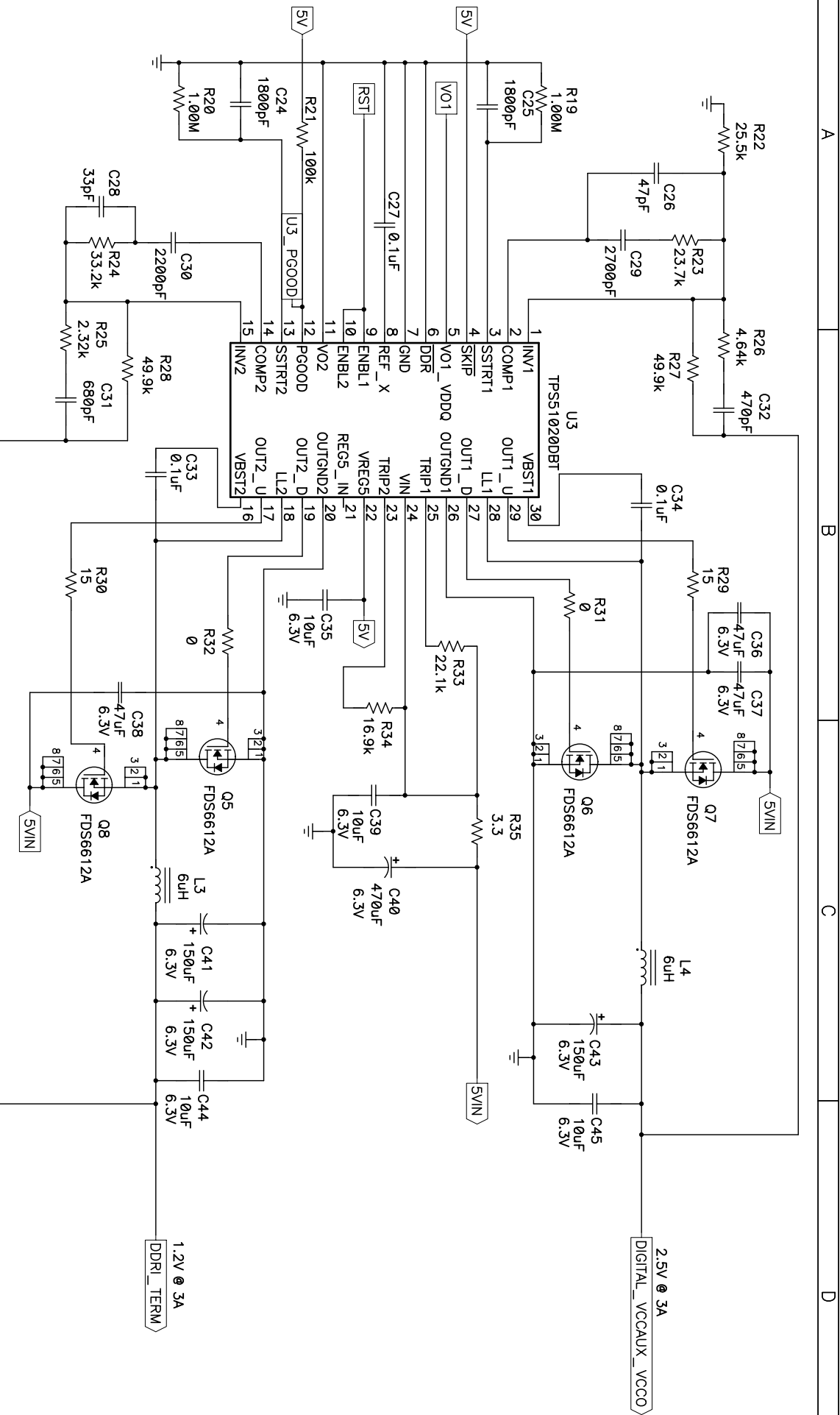
- **V_{CCAUX}** : V_{CCAUX} powers time-critical resources in the FPGA, including the Digital Clock Managers (DCMs). Therefore, this supply voltage is especially susceptible to power supply noise. V_{CCAUX} can share a power plane with V_{CCO}, but only if V_{CCO} does not have excessive noise. Changes in V_{CCAUX} voltage should take place no faster than 10 mV/ms.
- **Modifications:** For input voltages other than 5V or high output currents, use the 40K design software to make design modifications. For 12V inputs, see Virtex-4 Design 5 (PR449).

QUESTIONS?

- Send an email to <mailto:fpgasupport@list.ti.com>



Title		Virtex-4 510220 based for Vin=5V	
Size	Number	Rev	
B	PR448		
Date	02/21/05	Drawn by	
Filename	pr448.sch	Sheet 1	of 3



Title	VrteX-4 51020 based for Vin=5V		
Size	Number	Rev	
B	PR448		
Date	02/21/05	Drawn by	
Filename	pr448.sch	Sheet 2	of 3

A

B

C

D

1

1

Additional Caps

VALUE	DIGITAL_VCCO QTY	DIGITAL_VCCAUX QTY	DIGITAL_VCCINT QTY
0.047 uF	51	14	38
0.22 uF	39	7	19
0.68 uF	15	4	10
2.2 uF	15	3	7
330 uF	15	1	3

3

3

4

4

A

B

C

D

Title		Vrtex-4 51020 based for Vin=5V	
Size	Number	Rev	
B	PR448		
Date	02/21/05	Drawn by	
Filename	pr448.sch	Sheet 3	of 3

2

Filename: PR448_bom.xls					
Date: 02/22/2005					
PR448 BOM					
COUNT	RefDes	Description	Size	Part Number	MFR
3	C1, C24, C25	Capacitor, Ceramic, 1800pF, 25V, X5R	0805	Std	Std
8	C12, C16, C21, C22, C35, C39, C44, C45	Capacitor, Ceramic, 10uF, 6.3V, X5R, 10%	0805	C2012X5R0J106K	TDK
6	C13, C14, C15, C36, C37, C38	Capacitor, Ceramic, 47uF, 6.3V, X5R, 10%	1210	C3225X5R0J476K	TDK
2	C17, C40	Capacitor, Aluminum, SM, 470uF, 6.3V	8x10.2mm	EEVFK0J471P	Panasonic
6	C18, C19, C20, C41, C42, C43	Capacitor, Aluminum, 150uF, 6.3V, 12 milliohms, 20%	7343	EEF-UE0J151R	Panasonic
1	C2	Capacitor, Ceramic, 1800pF, 25V, X5R	0805	Std	Std
1	C23	Capacitor, Ceramic, 100pF, 25V, X5R	0805	Std	Std
2	C3, C26	Capacitor, Ceramic, 47pF, 50V, C0G	0805	Std	Std
6	C4, C10, C11, C27, C33, C34	Capacitor, Ceramic, 0.1uF, 16V, X5R	0805	Std	Std
2	C5, C28	Capacitor, Ceramic, 33pF, 50V, C0G	0805	Std	Std
2	C6, C29	Capacitor, Ceramic, 2700pF, 25V, X5R	0805	Std	Std
2	C7, C30	Capacitor, Ceramic, 2200pF, 25V, X5R	0805	Std	Std
2	C8, C31	Capacitor, Ceramic, 680pF, 25V, X5R	0805	Std	Std
2	C9, C32	Capacitor, Ceramic, 470pF, 25V, C0G	0805	Std	Std
4	L1, L2, L3, L4	Inductor, SMT, 6uH, 5.8A, 8milliohms	12.9x12.9mm	CEP125-6R0	Sumida
8	Q1 - Q8	Transistor, MOSFET, N-Chan, 30V, 8.4A, Rds 22 milliOhm	SO8	FDS6612A	Fairchild
1	R1	Resistor, Chip, 121k Ohms, 1/10W, 1%	0805	Std	Std
4	R11, R12, R29, R30	Resistor, Chip, 15 Ohms, 1/10W, 1%	0805	Std	Std
4	R13, R14, R31, R32	Resistor, Chip, 0 Ohms, 1/10W, 1%	0805	Std	Std
2	R15, R33	Resistor, Chip, 22.1k Ohms, 1/10W, 1%	0805	Std	Std
2	R17, R35	Resistor, Chip, 3.3 Ohms, 1/10W, 1%	0805	Std	Std
2	R18, R21	Resistor, Chip, 100k Ohms, 1/10W, 1%	0805	Std	Std
3	R2, R16, R34	Resistor, Chip, 16.9k Ohms, 1/10W, 1%	0805	Std	Std
1	R22	Resistor, Chip, 25.5k Ohms, 1/10W, 1%	0805	Std	Std
4	R3, R4, R19, R20	Resistor, Chip, 1.00M Ohms, 1/10W, 1%	0805	Std	Std
2	R5, R23	Resistor, Chip, 23.7k Ohms, 1/10W, 1%	0805	Std	Std
2	R6, R24	Resistor, Chip, 33.2k Ohms, 1/10W, 1%	0805	Std	Std
2	R7, R25	Resistor, Chip, 2.32k Ohms, 1/10W, 1%	0805	Std	Std
2	R8, R26	Resistor, Chip, 4.64k Ohms, 1/10W, 1%	0805	Std	Std
4	R9, R10, R27, R28	Resistor, Chip, 49.9k Ohms, 1/10W, 1%	0805	Std	Std
2	U1, U3	IC, Dual Sync Step-Down Controller	TSSOP30	TPS51020DBT	TI
1	U2	IC, Low Quiescent Current Programmable, 1.2-V, Delay Time 1ms to10s	SOT23-6	TPS3808G12DBVR	TI

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