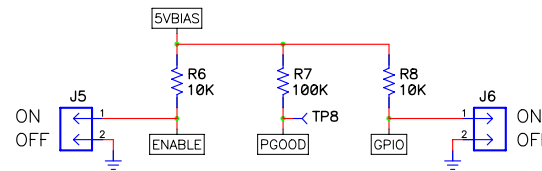


SN0803030RHL Notes:

- NVDD = $(1 + R_{top}/R_{bot}) * 0.75$
- VID Pin Logic:
High = 3.3V or 5V
Low = GND
- Enable Pin Logic:
On = 3.3V or 5V
Off = GND

Component Notes:

- L1
Coilcraft
SER2009-901MLD
Inductor, 0.9uH, 0.74mohm, 33A
- C8
Sanyo
2SEPC820MY
Capacitor, 820uF, 2.5V, 0.005 Ohms
Or can use:
x2 Fujitsu
FP-4R0RE122M-R7R
Capacitor, 1200uF, 4V, 7mohm



NVDD	R _{top}	R _{bot}	GPIO	Actual Voltage
0.90V	20k	100k	High	0.90V
1.05V	20k	100k 100K	Low	1.05V

Title SN0803030 Single Phase Design		
Size C	Number Texas Instruments	Rev A
Date 8/8/08	Drawn by R Taylor	
Filename PMP3994_REVA.sch	Sheet	of

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