

Errata

DATE: September 2000

Document Being Updated: MSP50C30 User's Guide

Literature Number Being Updated: SPSU012

This Errata (SPSZ001) ships with the *MSP50C30 Speech Synthesis Processor* User's Guide

Section: Change or Add:

2.1.9 This note was added to the end of section 2.1.9:
NOTE: It is important that the LBR immediately follow the TAM which loads the new value to the PSA.

Section: Change or Add:

3.1.6.3 This note was added to the end of section 3.1.6.3:
NOTE: It is important that the PSA register be changed IMMEDIATELY before the LBR instruction with no intervening instructions. The effect of the change in the PSA register is delayed exactly two instruction cycles to permit time to execute the LBR branch.

Section: Change or Add:

5.1.1 This note was added to the end of section 5.1.1:
NOTE: All interrupts should be disabled while executing the LBR instruction which branches across segment boundaries. If this is not done, it is possible for an interrupt to happen immediately following the change in the PSA register (but before executing the LBR instruction) resulting in the page change happening without the proper LBR instruction. The interrupts can be re-enabled on the other side of the LBR instruction.

Section: Change or Add:

Appendix I-4 Change *recommended operating conditions (MSP50C32, MSP50C33, MSP50C34)* header to *recommended operating conditions (MSP50C30)*.

Section: Change or Add:

Appendix I-4 Change the column headings in the *recommended operating conditions (MSP50C30)* from *max* and *max* to *min* and *max*.