



LMV821-N, LMV822-N, LMV822-N-Q1, LMV824-N, LMV824-N-Q1

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# LMV82x シングルデュアルクワッド、低電圧、低消費電力、RRO、5MHzオペアンプ

## 1 特長

- 車載アプリケーション用に認定済み
- 下記内容でAEC-Q100認定済み
  - LMV822-Q1およびLMV824-Q1は車載用AEC-Q100グレード1バージョンで利用可能
- 特に記述のない限り、 $V_s = 5V$  (標準値)
- LMV824は125°Cまでの拡張温度範囲で利用可能
- 2.0×1.25×0.95mmの小型SC70-5パッケージ
- 2.5V、2.7V、5Vでの性能を規定
- $V_{OS}$ : 3.5mV (最大)
- $TCV_{OS}$ : 1 $\mu V/^{\circ}C$
- ゲイン帯域幅積(2.7V時): 5MHz
- 2.7V電源での $I_{Supply}$ : アンプごとに220 $\mu A$
- スルー・レート: 1.4V/ $\mu s$  (最小値)
- CMRR: 90dB
- PSRR: 85dB
- 5V電源での $V_{CM}$ : -0.3V~4.3V
- レール・ツー・レール出力(RRO)
  - 600 $\Omega$ 負荷でレールから160mV
  - 10k $\Omega$ 負荷でレールから55mV
- 容量性負荷で安定した性能

## 2 アプリケーション

- コードレス電話
- 携帯電話
- ノート PC
- 携帯情報端末
- PCMCIA

## 3 概要

LMV821/LMV822/LMV824オペアンプは、低電圧、低消費電力システムで高い性能と経済性を実現します。2.7V電源でのユニティ・ゲイン周波数が5MHz、スルー・レートが1.4V/ $\mu s$ 、静止電流がアンプごとにわずか220 $\mu A$ です。600 $\Omega$ 負荷へのレール・ツー・レール出力(RRO)を行えます。入力同相電圧範囲にはグラウンドが含まれ、最大入力オフセット電圧は3.5mVです。「アプリケーション」セクションに記されているように、大きな容量性負荷も簡単に駆動可能です。

LMV821シングル・オペアンプは小さなSC70-5パッケージで供給され、これまで最小であったSOT23-5の約半分のサイズです。LMV824NDGVは拡張工業用温度範囲で動作が規定され、TVSOPパッケージで供給されます。

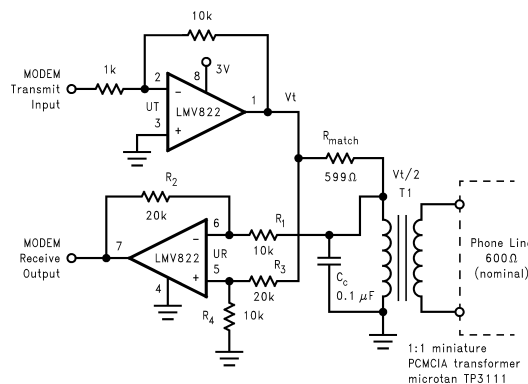
全体として、LMV821/LMV822/LMV824デバイスはコスト・パフォーマンスが高く、広範なアプリケーション用に設計された、低電圧、低消費電力、高性能のオペアンプです。

### 製品情報<sup>(1)</sup>

| 型番          | パッケージ      | 本体サイズ         |
|-------------|------------|---------------|
| LMV821-N    | SOT23 (5)  | 2.92mm×1.60mm |
|             | SC70 (5)   | 2.00mm×1.25mm |
| LMV822-N    | SOIC (8)   | 4.90mm×3.91mm |
|             | VSSOP (8)  | 3.00mm×3.00mm |
| LMV822-N-Q1 | VSSOP (8)  | 3.00mm×3.00mm |
| LMV824-N    | SOIC (14)  | 8.65mm×3.91mm |
|             | TSSOP (14) | 5.00mm×4.40mm |
| LMV824-N-Q1 | TSSOP (14) | 5.00mm×4.40mm |
| LMV824I     | TVSOP (14) | 4.40mm×3.60mm |

(1) 利用可能なすべてのパッケージについては、このデータシートの末尾にある注文情報を参照してください。

### PCMCIAモデム・カード用の電話線トランシーバ



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## 4 改訂履歴

資料番号末尾の英字は改訂を表しています。その改訂履歴は英語版に準じています。

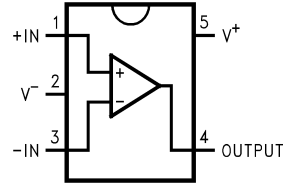
| Revision H (April 2014) から Revision I に変更                                              | Page |
|----------------------------------------------------------------------------------------|------|
| 「特長」セクション 変更                                                                           | 1    |
| Moved Storage Temperature from Handling Ratings Table which has been renamed ESD Table | 4    |
| Changed Handling Ratings Table to ESD Ratings Table Format - no data changed           | 4    |
| Added Thermal Information                                                              | 4    |
| Changed and updated Electrical Tables                                                  | 8    |

| Revision G (November 2013) から Revision H に変更                                                                               | Page |
|----------------------------------------------------------------------------------------------------------------------------|------|
| 新しいTI標準に合わせてデータシートのフローとレイアウトを変更。「アプリケーションと実装」、「電源に関する推奨事項」、「レイアウト」、「デバイスおよびドキュメントのサポート」、「メカニカル、パッケージ、および注文情報」の各セクションを追加 変更 | 1    |
| データシート全体を通して新しいLMV824Iを追加                                                                                                  | 1    |
| Deleted "Refer to application note AN-397 for detailed explanation." - no such appnote                                     | 22   |
| Added Added Section                                                                                                        | 27   |
| Added Added Section                                                                                                        | 27   |

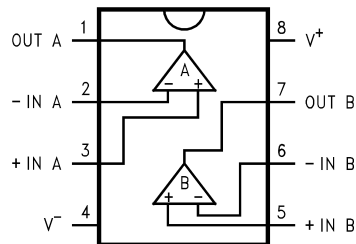
| Revision D (February 2013) から Revision G に変更 | Page |
|----------------------------------------------|------|
| 新規部品 追加                                      | 1    |
| 新規デバイス 追加                                    | 1    |
| Added new device                             | 4    |
| Added new device                             | 5    |
| Added new device                             | 8    |
| Added new device                             | 8    |

## 5 Pin Configuration and Functions

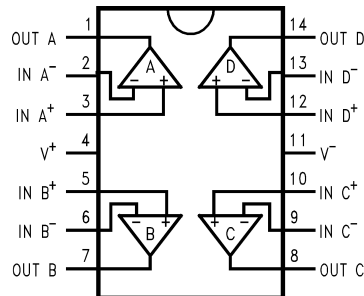
**5-Pin SC70-5/SOT23-5**  
**DCK0005A, DBV0005A Packages**  
**Top View**



**8-Pin SOIC/VSSOP**  
**D0008A, DGK0008A Packages**  
**Top View**



**14-Pin SOIC/TSSOP/TVSOP**  
**D0014A, PW0014A, DGV0014A Packages**  
**Top View**



### Pin Functions

| PIN NAME | I/O | DESCRIPTION         |
|----------|-----|---------------------|
| +IN      | I   | Non-Inverting Input |
| -IN      | I   | Inverting Input     |
| OUT      | O   | Output              |
| V-       | P   | Negative Supply     |
| V+       | P   | Positive Supply     |

## 6 Specifications

### 6.1 Absolute Maximum Ratings<sup>(1)(2)</sup>

|                                                       | MIN            | MAX                | UNIT |
|-------------------------------------------------------|----------------|--------------------|------|
| Differential Input Voltage                            | V <sup>-</sup> | V <sup>+</sup>     | V    |
| Supply Voltage (V <sup>+</sup> – V <sup>-</sup> )     | –0.3           | 5.5                | V    |
| Output Short Circuit to V <sup>+</sup> <sup>(3)</sup> |                | See <sup>(3)</sup> |      |
| Output Short Circuit to V <sup>-</sup> <sup>(3)</sup> |                | See <sup>(3)</sup> |      |
| Soldering Information                                 |                |                    |      |
| Infrared or Convection (20 sec)                       |                | 235                | °C   |
| Junction Temperature <sup>(4)</sup>                   |                | 150                | °C   |
| Storage Temperature T <sub>stg</sub>                  | –65            | 150                | °C   |

- (1) Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but specific performance is not ensured. For ensured specifications and the test conditions, see the Electrical Characteristics.
- (2) If Military/Aerospace specified devices are required, please contact the TI Sales Office/Distributors for availability and specifications.
- (3) Applies to both single-supply and split-supply operation. Continuous short circuit operation at elevated ambient temperature can result in exceeding the maximum allowed junction temperature of 150°C. Output currents in excess of 45 mA over long term may adversely affect reliability.
- (4) The maximum power dissipation is a function of T<sub>J(max)</sub>, θ<sub>JA</sub>, and T<sub>A</sub>. The maximum allowable power dissipation at any ambient temperature is P<sub>D</sub> = (T<sub>J(max)</sub> – T<sub>A</sub>) / θ<sub>JA</sub>. All numbers apply for packages soldered directly into a PC board.

### 6.2 ESD Ratings

|                    |                         |                                                                                       | VALUE | UNIT |
|--------------------|-------------------------|---------------------------------------------------------------------------------------|-------|------|
| V <sub>(ESD)</sub> | Electrostatic discharge | Human body model (HBM), per ANSI/ESDA/JEDEC JS-001, all pins <sup>(1)(2)(3)</sup>     | ±2000 | V    |
|                    |                         | Human body model (HBM), per ANSI/ESDA/JEDEC JS-001, all pins <sup>(1)</sup><br>LMV821 | ±1500 |      |
|                    |                         | Machine Model (MM) <sup>(4)</sup>                                                     | ±200  |      |

- (1) Level listed above is the passing level per ANSI, ESDA, and JEDEC JS-001. JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.
- (2) Human body model, 1.5 kΩ in series with 100 pF.
- (3) AEC Q100-002 indicates HBM stressing is done in accordance with the ANSI/ESDA/JEDEC JS-001 specification for Q grade devices.
- (4) Machine model, 200Ω in series with 100 pF.

### 6.3 Recommended Operating Conditions

|                   |                                  | MIN | NOM | MAX | UNIT |
|-------------------|----------------------------------|-----|-----|-----|------|
| Supply Voltage    |                                  | 2.5 |     | 5.5 | V    |
| Temperature Range | LMV821, LMV822, LMV824           | –40 |     | 85  | °C   |
|                   | LMV822-Q1, LMV824I and LMV824-Q1 | –40 |     | 125 |      |

### 6.4 Thermal Information, 5 Pins<sup>(1)</sup>

| THERMAL METRIC <sup>(1)</sup> |                                              | DCK<br>SC70-5<br>PACKAGE | DBV<br>SOT23-5<br>PACKAGE | UNIT |
|-------------------------------|----------------------------------------------|--------------------------|---------------------------|------|
|                               |                                              | 5 PIN                    | 5 PIN                     |      |
| R <sub>θJA</sub>              | Junction-to-ambient thermal resistance       | 263.4                    | 217.8                     | °C/W |
| R <sub>θJC(top)</sub>         | Junction-to-case (top) thermal resistance    | 102.8                    | 142.4                     | °C/W |
| R <sub>θJB</sub>              | Junction-to-board thermal resistance         | 50.9                     | 49.4                      | °C/W |
| ψ <sub>JT</sub>               | Junction-to-top characterization parameter   | 3.7                      | 29.1                      | °C/W |
| ψ <sub>JB</sub>               | Junction-to-board characterization parameter | 50.2                     | 48.5                      | °C/W |
| R <sub>θJC(bot)</sub>         | Junction-to-case (bottom) thermal resistance | N/A                      | N/A                       | °C/W |

- (1) For more information about traditional and new thermal metrics, see the [Semiconductor and IC Package Thermal Metrics](#) application report.

## 6.5 Thermal Information, 8 Pins<sup>(1)</sup>

| THERMAL METRIC <sup>(1)</sup> |                                              | D<br>SOIC<br>PACKAGE | DGK<br>VSSOP<br>PACKAGE | UNIT |
|-------------------------------|----------------------------------------------|----------------------|-------------------------|------|
|                               |                                              | 8 PIN                | 8 PIN                   |      |
| $R_{\theta JA}$               | Junction-to-ambient thermal resistance       | 132.6                | 193.9                   | °C/W |
| $R_{\theta JC(top)}$          | Junction-to-case (top) thermal resistance    | 76.9                 | 84.4                    | °C/W |
| $R_{\theta JB}$               | Junction-to-board thermal resistance         | 73.2                 | 114.5                   | °C/W |
| $\Psi_{JT}$                   | Junction-to-top characterization parameter   | 25.0                 | 21.6                    | °C/W |
| $\Psi_{JB}$                   | Junction-to-board characterization parameter | 72.6                 | 113.0                   | °C/W |
| $R_{\theta JC(bot)}$          | Junction-to-case (bottom) thermal resistance | N/A                  | N/A                     | °C/W |

(1) For more information about traditional and new thermal metrics, see the [Semiconductor and IC Package Thermal Metrics](#) application report.

## 6.6 Thermal Information, 14 Pins<sup>(1)</sup>

| THERMAL METRIC <sup>(1)</sup> |                                              | D<br>SOIC PACKAGE | PW<br>TSSOP<br>PACKAGE | DGV<br>TVSOP<br>PACKAGE | UNIT |
|-------------------------------|----------------------------------------------|-------------------|------------------------|-------------------------|------|
|                               |                                              | 14 PIN            | 14 PIN                 | 14 PIN                  |      |
| $R_{\theta JA}$               | Junction-to-ambient thermal resistance       | 109.7             | 135.6                  | 148.2                   | °C/W |
| $R_{\theta JC(top)}$          | Junction-to-case (top) thermal resistance    | 65.9              | 63.8                   | 67.3                    | °C/W |
| $R_{\theta JB}$               | Junction-to-board thermal resistance         | 64.1              | 77.4                   | 77.5                    | °C/W |
| $\Psi_{JT}$                   | Junction-to-top characterization parameter   | 24.5              | 13.0                   | 12.9                    | °C/W |
| $\Psi_{JB}$                   | Junction-to-board characterization parameter | 63.9              | 76.8                   | 76.9                    | °C/W |
| $R_{\theta JC(bot)}$          | Junction-to-case (bottom) thermal resistance | N/A               | N/A                    | N/A                     | °C/W |

(1) For more information about traditional and new thermal metrics, see the [Semiconductor and IC Package Thermal Metrics](#) application report.

## 6.7 DC Electrical Characteristics 2.7V

Unless otherwise specified, all limits ensured for  $T_J = 25^\circ\text{C}$ .  $V^+ = 2.7\text{V}$ ,  $V^- = 0\text{V}$ ,  $V_{CM} = 1.0\text{V}$ ,  $V_O = 1.35\text{V}$  and  $R_L > 1\text{M}\Omega$ . Temperature extremes are  $-40^\circ\text{C} \leq T_J \leq 85^\circ\text{C}$  for LMV821/822/824, and  $-40^\circ\text{C} \leq T_J \leq 125^\circ\text{C}$  for LMV822-Q1/LMV824-Q1/LMV824I.

| PARAMETER                                     | TEST CONDITIONS                                                                                                                                         | MIN <sup>(1)</sup> | TYP <sup>(2)</sup> | MAX <sup>(1)</sup> | UNIT                         |
|-----------------------------------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------|--------------------|--------------------|------------------------------|
| $V_{OS}$ Input Offset Voltage                 | LMV821/822/822-Q1/824                                                                                                                                   |                    | 1                  | 3.5                | mV                           |
|                                               | LMV821/822/822-Q1/824, Over Temperature                                                                                                                 |                    |                    | 4                  |                              |
|                                               | LMV824-Q1/LMV824I                                                                                                                                       |                    | 1                  |                    |                              |
|                                               | LMV824-Q1/LMV824I, Over Temperature                                                                                                                     |                    |                    | 5.5                |                              |
| $TCV_{OS}$ Input Offset Voltage Average Drift |                                                                                                                                                         |                    | 1                  |                    | $\mu\text{V}/^\circ\text{C}$ |
| $I_B$ Input Bias Current                      |                                                                                                                                                         |                    | 30                 | 90                 | nA                           |
|                                               | Over Temperature                                                                                                                                        |                    |                    | 140                |                              |
| $I_{OS}$ Input Offset Current                 |                                                                                                                                                         |                    | 0.5                | 30                 | nA                           |
|                                               | Over Temperature                                                                                                                                        |                    |                    | 50                 |                              |
| CMRR Common Mode Rejection Ratio              | $0\text{V} \leq V_{CM} \leq 1.7\text{V}$                                                                                                                | 70                 | 85                 |                    | dB                           |
|                                               | $0\text{V} \leq V_{CM} \leq 1.7\text{V}$ , Over Temperature                                                                                             | 68                 |                    |                    |                              |
| +PSRR Positive Power Supply Rejection Ratio   | $1.7\text{V} \leq V^+ \leq 4\text{V}$ , $V^- = 1\text{V}$ , $V_O = 0\text{V}$ , $V_{CM} = 0\text{V}$<br>LMV821/822/824/824-Q1/LMV824I                   | 75                 | 85                 |                    | dB                           |
|                                               | $1.7\text{V} \leq V^+ \leq 4\text{V}$ , $V^- = 1\text{V}$ , $V_O = 0\text{V}$ , $V_{CM} = 0\text{V}$<br>LMV821/822/824/824-Q1/LMV824I, Over Temperature | 70                 |                    |                    |                              |
|                                               | LMV822-Q1                                                                                                                                               | 75                 | 85                 |                    |                              |

(1) All limits are ensured by testing or statistical analysis.

(2) Typical Values represent the most likely parametric norm.

## DC Electrical Characteristics 2.7V (continued)

Unless otherwise specified, all limits ensured for  $T_J = 25^\circ\text{C}$ .  $V^+ = 2.7\text{V}$ ,  $V^- = 0\text{V}$ ,  $V_{\text{CM}} = 1.0\text{V}$ ,  $V_O = 1.35\text{V}$  and  $R_L > 1\text{M}\Omega$ . Temperature extremes are  $-40^\circ\text{C} \leq T_J \leq 85^\circ\text{C}$  for LMV821/822/824, and  $-40^\circ\text{C} \leq T_J \leq 125^\circ\text{C}$  for LMV822-Q1/LMV824-Q1/LMV824I.

| PARAMETER       |                                       | TEST CONDITIONS                                                                                                                                                      | MIN <sup>(1)</sup> | TYP <sup>(2)</sup> | MAX <sup>(1)</sup> | UNIT |
|-----------------|---------------------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------|--------------------|--------------------|------|
| -PSRR           | Negative Power Supply Rejection Ratio | $-1.0\text{V} \leq V^- \leq -3.3\text{V}$ , $V^+ = 1.7\text{V}$ , $V_O = 0\text{V}$ , $V_{\text{CM}} = 0\text{V}$<br>LMV821/822/824/824-Q1/LMV824I                   | 73                 | 85                 |                    | dB   |
|                 |                                       | $-1.0\text{V} \leq V^- \leq -3.3\text{V}$ , $V^+ = 1.7\text{V}$ , $V_O = 0\text{V}$ , $V_{\text{CM}} = 0\text{V}$<br>LMV821/822/824/824-Q1/LMV824I, Over Temperature | 70                 |                    |                    |      |
|                 |                                       | LMV822-Q1                                                                                                                                                            | 73                 | 85                 |                    |      |
| $V_{\text{CM}}$ | Input Common-Mode Voltage Range       | For CMRR $\geq 50\text{dB}$                                                                                                                                          |                    | -0.3               | -0.2               | V    |
|                 |                                       |                                                                                                                                                                      | 1.9                | 2.0                |                    |      |
| $A_v$           | Large Signal Voltage Gain             | Sourcing, $R_L = 600\Omega$ to $1.35\text{V}$ ,<br>$V_O = 1.35\text{V}$ to $2.2\text{V}$ ;<br>LMV821/822/824                                                         | 90                 | 100                |                    | dB   |
|                 |                                       | Sourcing, $R_L = 600\Omega$ to $1.35\text{V}$ ,<br>$V_O = 1.35\text{V}$ to $2.2\text{V}$ ;<br>LMV821/822/824, Over Temperature                                       | 85                 |                    |                    |      |
|                 |                                       | LMV822-Q1/LMV824-Q1/LMV824I                                                                                                                                          | 90                 | 100                |                    |      |
|                 |                                       | Sinking, $R_L = 600\Omega$ to $1.35\text{V}$ ,<br>$V_O = 1.35\text{V}$ to $0.5\text{V}$<br>LMV821/822/824                                                            | 85                 | 90                 |                    | dB   |
|                 |                                       | Sinking, $R_L = 600\Omega$ to $1.35\text{V}$ ,<br>$V_O = 1.35\text{V}$ to $0.5\text{V}$<br>LMV821/822/824, Over Temperature                                          | 80                 |                    |                    |      |
|                 |                                       | LMV824I                                                                                                                                                              | 85                 | 90                 |                    |      |
|                 |                                       | LMV824I, Over Temperature                                                                                                                                            | 78                 |                    |                    |      |
|                 |                                       | LMV822-Q1/LMV824-Q1                                                                                                                                                  | 85                 | 90                 |                    |      |
|                 |                                       | Sourcing, $R_L = 2\text{k}\Omega$ to $1.35\text{V}$ ,<br>$V_O = 1.35\text{V}$ to $2.2\text{V}$ ;<br>LMV821/822/824                                                   | 95                 | 100                |                    | dB   |
|                 |                                       | Sourcing, $R_L = 2\text{k}\Omega$ to $1.35\text{V}$ ,<br>$V_O = 1.35\text{V}$ to $2.2\text{V}$ ;<br>LMV821/822/824, Over Temperature                                 | 90                 |                    |                    |      |
|                 |                                       | LMV822-Q1/LMV824-Q1/LMV824I                                                                                                                                          | 95                 | 100                |                    |      |
|                 |                                       | Sinking, $R_L = 2\text{k}\Omega$ to $1.35\text{V}$ ,<br>$V_O = 1.35\text{V}$ to $0.5\text{V}$<br>LMV821/822/824                                                      | 90                 | 95                 |                    | dB   |
|                 |                                       | Sinking, $R_L = 2\text{k}\Omega$ to $1.35\text{V}$ ,<br>$V_O = 1.35\text{V}$ to $0.5\text{V}$<br>LMV821/822/824, Over Temperature                                    | 85                 |                    |                    |      |
|                 |                                       | LMV822-Q1/LMV824-Q1/LMV824I                                                                                                                                          | 90                 | 95                 |                    |      |
| $V_O$           | Output Swing                          | $V^+ = 2.7\text{V}$ , $R_L = 600\Omega$ to $1.35\text{V}$                                                                                                            | 2.50               | 2.58               |                    | V    |
|                 |                                       |                                                                                                                                                                      |                    | 0.13               | 0.20               |      |
|                 |                                       | $V^+ = 2.7\text{V}$ , $R_L = 600\Omega$ to $1.35\text{V}$ , Over Temp                                                                                                | 2.40               |                    | 0.30               |      |
|                 |                                       | $V^+ = 2.7\text{V}$ , $R_L = 2\text{k}\Omega$ to $1.35\text{V}$                                                                                                      | 2.60               | 2.66               |                    | V    |
|                 |                                       |                                                                                                                                                                      |                    | 0.08               | 0.120              |      |
|                 |                                       | $V^+ = 2.7\text{V}$ , $R_L = 2\text{k}\Omega$ to $1.35\text{V}$ , Over Temp                                                                                          | 2.50               |                    | 0.200              |      |
| $I_O$           | Output Current                        | Sourcing, $V_O = 0\text{V}$                                                                                                                                          | 12                 | 16                 |                    | mA   |
|                 |                                       | Sinking, $V_O = 2.7\text{V}$                                                                                                                                         | 12                 | 26                 |                    |      |

## DC Electrical Characteristics 2.7V (continued)

Unless otherwise specified, all limits ensured for  $T_J = 25^\circ\text{C}$ .  $V^+ = 2.7\text{V}$ ,  $V^- = 0\text{V}$ ,  $V_{CM} = 1.0\text{V}$ ,  $V_O = 1.35\text{V}$  and  $R_L > 1\text{ M}\Omega$ . Temperature extremes are  $-40^\circ\text{C} \leq T_J \leq 85^\circ\text{C}$  for LMV821/822/824, and  $-40^\circ\text{C} \leq T_J \leq 125^\circ\text{C}$  for LMV822-Q1/LMV824-Q1/LMV824I.

| PARAMETER            | TEST CONDITIONS          | MIN <sup>(1)</sup> | TYP <sup>(2)</sup> | MAX <sup>(1)</sup> | UNIT |
|----------------------|--------------------------|--------------------|--------------------|--------------------|------|
| $I_S$ Supply Current | LMV821 (Single)          |                    | 0.22               | 0.3                | mA   |
|                      | LMV821, Over Temperature |                    |                    | 0.5                |      |
|                      | LMV822 (Dual)            |                    | 0.45               | 0.6                | mA   |
|                      | LMV822, Over Temperature |                    |                    | 0.8                |      |
|                      | LMV824 (Quad)            |                    | 0.72               | 1.0                | mA   |
|                      | LMV824, Over Temperature |                    |                    | 1.2                |      |

## 6.8 DC Electrical Characteristics 2.5V

Unless otherwise specified, all limits ensured for  $T_J = 25^\circ\text{C}$ .  $V^+ = 2.5\text{V}$ ,  $V^- = 0\text{V}$ ,  $V_{\text{CM}} = 1.0\text{V}$ ,  $V_O = 1.25\text{V}$  and  $R_L > 1\text{ M}\Omega$ . Temperature extremes are  $-40^\circ\text{C} \leq T_J \leq 85^\circ\text{C}$  for LMV821/822/824, and  $-40^\circ\text{C} \leq T_J \leq 125^\circ\text{C}$  for LMV822-Q1/LMV824-Q1/LMV824I.

| PARAMETER                            | CONDITION                                                                          | MIN <sup>(1)</sup> | TYP <sup>(2)</sup> | MAX <sup>(1)</sup> | UNIT |
|--------------------------------------|------------------------------------------------------------------------------------|--------------------|--------------------|--------------------|------|
| $V_{\text{OS}}$ Input Offset Voltage | LMV821/822/822-Q1/824                                                              |                    | 1                  | 3.5                | mV   |
|                                      | LMV821/822/822-Q1/824, Over Temperature                                            |                    |                    | 4                  |      |
|                                      | LMV824-Q1/LMV824I                                                                  |                    | 1                  |                    |      |
|                                      | LMV824-Q1/LMV824I, Over Temperature                                                |                    |                    | 5.5                |      |
| $V_O$ Output Swing                   | $V^+ = 2.5\text{V}$ , $R_L = 600\Omega$ to $1.25\text{V}$                          | 2.30               | 2.37               |                    | V    |
|                                      |                                                                                    |                    | 0.13               | 0.20               |      |
|                                      | $V^+ = 2.5\text{V}$ , $R_L = 600\Omega$ to $1.25\text{V}$ , Over Temperature       | 2.20               |                    | 0.30               | V    |
|                                      | $V^+ = 2.5\text{V}$ , $R_L = 2\text{k}\Omega$ to $1.25\text{V}$                    | 2.40               | 2.46               |                    |      |
|                                      |                                                                                    |                    | 0.08               | 0.12               |      |
|                                      | $V^+ = 2.5\text{V}$ , $R_L = 2\text{k}\Omega$ to $1.25\text{V}$ , Over Temperature | 2.30               |                    | 0.20               |      |

(1) All limits are ensured by testing or statistical analysis.

(2) Typical Values represent the most likely parametric norm.

## 6.9 AC Electrical Characteristics 2.7V

Unless otherwise specified, all limits ensured for  $T_J = 25^\circ\text{C}$ .  $V^+ = 2.7\text{V}$ ,  $V^- = 0\text{V}$ ,  $V_{\text{CM}} = 1.0\text{V}$ ,  $V_O = 1.35\text{V}$  and  $R_L > 1\text{ M}\Omega$ . Temperature extremes are  $-40^\circ\text{C} \leq T_J \leq 85^\circ\text{C}$  for LMV821/822/824, and  $-40^\circ\text{C} \leq T_J \leq 125^\circ\text{C}$  for LMV822-Q1/LMV824-Q1/LMV824I.

| PARAMETER                          | TEST CONDITIONS                                                                                | MIN <sup>(1)</sup> | TYP <sup>(2)</sup> | MAX <sup>(1)</sup> | UNIT                   |
|------------------------------------|------------------------------------------------------------------------------------------------|--------------------|--------------------|--------------------|------------------------|
| SR Slew Rate                       | See <sup>(3)</sup>                                                                             |                    | 1.5                |                    | V/ $\mu\text{s}$       |
| GBW Gain-Bandwidth Product         |                                                                                                |                    | 5                  |                    | MHz                    |
| $\Phi_m$ Phase Margin              |                                                                                                |                    | 61                 |                    | Deg.                   |
| $G_m$ Gain Margin                  |                                                                                                |                    | 10                 |                    | dB                     |
| Amp-to-Amp Isolation               | See <sup>(4)</sup>                                                                             |                    | 135                |                    | dB                     |
| $e_n$ Input-Related Voltage Noise  | $f = 1\text{ kHz}$ , $V_{\text{CM}} = 1\text{V}$                                               |                    | 28                 |                    | nV/ $\sqrt{\text{Hz}}$ |
| $i_n$ Input-Referred Current Noise | $f = 1\text{ kHz}$                                                                             |                    | 0.1                |                    | pA/ $\sqrt{\text{Hz}}$ |
| THD Total Harmonic Distortion      | $f = 1\text{ kHz}$ , $A_V = -2$ , $R_L = 10\text{ k}\Omega$ , $V_O = 4.1\text{ V}_{\text{PP}}$ |                    | 0.01%              |                    |                        |

(1) All limits are ensured by testing or statistical analysis.

(2) Typical Values represent the most likely parametric norm.

(3)  $V^+ = 5\text{V}$ . Connected as voltage follower with 3V step input. Number specified is the slower of the positive and negative slew rates.

(4) Input referred,  $V^+ = 5\text{V}$  and  $R_L = 100\text{k}\Omega$  connected to  $2.5\text{V}$ . Each amp excited in turn with  $1\text{ kHz}$  to produce  $V_O = 3\text{ V}_{\text{PP}}$ .

## 6.10 DC Electrical Characteristics 5V

Unless otherwise specified, all limits ensured for  $T_J = 25^\circ\text{C}$ .  $V^+ = 5\text{V}$ ,  $V^- = 0\text{V}$ ,  $V_{\text{CM}} = 2.0\text{V}$ ,  $V_O = 2.5\text{V}$  and  $R_L > 1\text{ M}\Omega$ . Temperature extremes are  $-40^\circ\text{C} \leq T_J \leq 85^\circ\text{C}$  for LMV821/822/824, and  $-40^\circ\text{C} \leq T_J \leq 125^\circ\text{C}$  for LMV822-Q1/LMV824-Q1/LMV824I.

| PARAMETER                                                   | TEST CONDITIONS                         | MIN <sup>(1)</sup> | TYP <sup>(2)</sup> | MAX <sup>(1)</sup> | UNIT                         |
|-------------------------------------------------------------|-----------------------------------------|--------------------|--------------------|--------------------|------------------------------|
| $V_{\text{OS}}$ Input Offset Voltage                        | LMV821/822/822-Q1/824                   |                    | 1                  | 3.5                | mV                           |
|                                                             | LMV821/822/822-Q1/824, Over Temperature |                    |                    | 4.0                |                              |
|                                                             | LMV824-Q1/LMV824I                       |                    | 1                  |                    |                              |
|                                                             | LMV824-Q1/ LMV824I, Over Temperature    |                    |                    | 5.5                |                              |
| $\text{TCV}_{\text{OS}}$ Input Offset Voltage Average Drift |                                         |                    | 1                  |                    | $\mu\text{V}/^\circ\text{C}$ |

(1) All limits are ensured by testing or statistical analysis.

(2) Typical Values represent the most likely parametric norm.

## DC Electrical Characteristics 5V (continued)

Unless otherwise specified, all limits ensured for  $T_J = 25^\circ\text{C}$ .  $V^+ = 5\text{ V}$ ,  $V^- = 0\text{ V}$ ,  $V_{CM} = 2.0\text{ V}$ ,  $V_O = 2.5\text{ V}$  and  $R_L > 1\text{ M}\Omega$ . Temperature extremes are  $-40^\circ\text{C} \leq T_J \leq 85^\circ\text{C}$  for LMV821/822/824, and  $-40^\circ\text{C} \leq T_J \leq 125^\circ\text{C}$  for LMV822-Q1/LMV824-Q1/LMV824I.

| PARAMETER |                                       | TEST CONDITIONS                                                                                                                                           | MIN <sup>(1)</sup> | TYP <sup>(2)</sup> | MAX <sup>(1)</sup> | UNIT |
|-----------|---------------------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------|--------------------|--------------------|------|
| $I_B$     | Input Bias Current                    |                                                                                                                                                           |                    | 40                 | 100                | nA   |
|           |                                       | Over Temperature                                                                                                                                          |                    |                    | 150                |      |
| $I_{OS}$  | Input Offset Current                  |                                                                                                                                                           |                    | 0.5                | 30                 | nA   |
|           |                                       | Over Temperature                                                                                                                                          |                    |                    | 50                 |      |
| CMRR      | Common Mode Rejection Ratio           | $0\text{ V} \leq V_{CM} \leq 4.0\text{ V}$                                                                                                                | 72                 | 90                 |                    | dB   |
|           |                                       | $0\text{ V} \leq V_{CM} \leq 4.0\text{ V}$ , Over Temperature                                                                                             | 70                 |                    |                    |      |
| +PSRR     | Positive Power Supply Rejection Ratio | $1.7\text{ V} \leq V^+ \leq 4\text{ V}$ , $V^- = 1\text{ V}$ , $V_O = 0\text{ V}$ , $V_{CM} = 0\text{ V}$<br>LMV821/822/824/824-Q1/824I                   |                    | 85                 | 75                 | dB   |
|           |                                       | $1.7\text{ V} \leq V^+ \leq 4\text{ V}$ , $V^- = 1\text{ V}$ , $V_O = 0\text{ V}$ , $V_{CM} = 0\text{ V}$<br>LMV821/822/824/824-Q1/824I, Over Temperature |                    |                    | 70                 |      |
|           |                                       | LMV822-Q1                                                                                                                                                 | 75                 | 85                 |                    |      |
| -PSRR     | Negative Power Supply Rejection Ratio | $-1.0\text{ V} \leq V^- \leq -3.3\text{ V}$ , $V^+ = 1.7\text{ V}$ , $V_O = 0\text{ V}$ , $V_{CM} = 0\text{ V}$<br>LMV821/822/824/824-Q1/824I             | 73                 | 85                 |                    | dB   |
|           |                                       | $-1.0\text{ V} \leq V^- \leq -3.3\text{ V}$ , $V^+ = 1.7\text{ V}$ , $V_O = 0\text{ V}$ , $V_{CM} = 0\text{ V}$<br>LMV821/822/824/824-Q1/824I             | 70                 |                    |                    |      |
|           |                                       | LMV822-Q1                                                                                                                                                 | 73                 | 85                 |                    |      |
| $V_{CM}$  | Input Common-Mode Voltage Range       | For CMRR $\geq 50\text{ dB}$                                                                                                                              |                    | -0.3               | -0.2               | V    |
|           |                                       |                                                                                                                                                           | 4.2                | 4.3                |                    | V    |
| $A_V$     | Large Signal Voltage Gain             | Sourcing, $R_L = 600\Omega$ to $2.5\text{ V}$ ,<br>$V_O = 2.5\text{ V}$ to $4.5\text{ V}$ ;<br>LMV821/822/824                                             | 95                 | 105                |                    | dB   |
|           |                                       | Sourcing, $R_L = 600\Omega$ to $2.5\text{ V}$ ,<br>$V_O = 2.5\text{ V}$ to $4.5\text{ V}$ ;<br>LMV821/822/824, Over Temperature                           | 90                 |                    |                    |      |
|           |                                       | LMV822-Q1/LMV824-Q1/LMV824I                                                                                                                               | 95                 | 105                |                    |      |
|           |                                       | Sinking, $R_L = 600\Omega$ to $2.5\text{ V}$ ,<br>$V_O = 2.5\text{ V}$ to $0.5\text{ V}$<br>LMV821/822/824                                                | 95                 | 105                |                    | dB   |
|           |                                       | Sinking, $R_L = 600\Omega$ to $2.5\text{ V}$ ,<br>$V_O = 2.5\text{ V}$ to $0.5\text{ V}$<br>LMV821/822/824, Over Temperature                              | 90                 |                    |                    |      |
|           |                                       | LMV824I                                                                                                                                                   | 95                 | 105                |                    |      |
|           |                                       | LMV824I, Over Temperature                                                                                                                                 | 82                 |                    |                    |      |
|           |                                       | LMV822-Q1/LMV824-Q1                                                                                                                                       | 95                 | 105                |                    |      |
|           |                                       | Sourcing, $R_L = 2\text{ k}\Omega$ to $2.5\text{ V}$ ,<br>$V_O = 2.5\text{ V}$ to $4.5\text{ V}$ ;<br>LMV821/822/824                                      | 95                 | 105                |                    | dB   |
|           |                                       | Sourcing, $R_L = 2\text{ k}\Omega$ to $2.5\text{ V}$ ,<br>$V_O = 2.5\text{ V}$ to $4.5\text{ V}$ ;<br>LMV821/822/824, Over Temperature                    | 90                 |                    |                    |      |
|           |                                       | LMV822-Q1/LMV824-Q1/LMV824I                                                                                                                               | 95                 | 105                |                    |      |
|           |                                       | Sinking, $R_L = 2\text{ k}\Omega$ to $2.5\text{ V}$ ,<br>$V_O = 2.5\text{ V}$ to $0.5\text{ V}$<br>LMV821/822/824                                         | 95                 | 105                |                    | dB   |
|           |                                       | Sinking, $R_L = 2\text{ k}\Omega$ to $2.5\text{ V}$ ,<br>$V_O = 2.5\text{ V}$ to $0.5\text{ V}$<br>LMV821/822/824, Over Temperature                       | 90                 |                    |                    |      |
|           |                                       | LMV822-Q1/LMV824-Q1/LMV824I                                                                                                                               | 95                 | 105                |                    |      |

## DC Electrical Characteristics 5V (continued)

Unless otherwise specified, all limits ensured for  $T_J = 25^\circ\text{C}$ .  $V^+ = 5\text{ V}$ ,  $V^- = 0\text{ V}$ ,  $V_{\text{CM}} = 2.0\text{ V}$ ,  $V_O = 2.5\text{ V}$  and  $R_L > 1\text{ M}\Omega$ . Temperature extremes are  $-40^\circ\text{C} \leq T_J \leq 85^\circ\text{C}$  for LMV821/822/824, and  $-40^\circ\text{C} \leq T_J \leq 125^\circ\text{C}$  for LMV822-Q1/LMV824-Q1/LMV824I.

| PARAMETER            | TEST CONDITIONS                                                                              | MIN <sup>(1)</sup> | TYP <sup>(2)</sup> | MAX <sup>(1)</sup> | UNIT |
|----------------------|----------------------------------------------------------------------------------------------|--------------------|--------------------|--------------------|------|
| $V_O$ Output Swing   | $V^+ = 5\text{ V}, R_L = 600\Omega$ to $2.5\text{ V}$                                        | 4.75               | 4.84               |                    | V    |
|                      | $V^+ = 5\text{ V}, R_L = 600\Omega$ to $2.5\text{ V}$ , Over Temperature                     | 4.70               |                    |                    |      |
|                      | $V^+ = 5\text{ V}, R_L = 600\Omega$ to $2.5\text{ V}$ (LMV824-Q1, LMV824I)                   |                    | 4.84               |                    |      |
|                      | $V^+ = 5\text{ V}, R_L = 600\Omega$ to $2.5\text{ V}$ (LMV824-Q1, LMV824I), Over Temperature | 4.60               |                    |                    |      |
|                      | $V^+ = 5\text{ V}, R_L = 600\Omega$ to $2.5\text{ V}$                                        |                    | 0.17               | 0.250              | V    |
|                      | $V^+ = 5\text{ V}, R_L = 600\Omega$ to $2.5\text{ V}$ , Over Temperature                     |                    |                    | 0.30               |      |
|                      | $V^+ = 5\text{ V}, R_L = 600\Omega$ to $2.5\text{ V}$ (LMV824-Q1, LMV824I)                   |                    | 0.17               |                    |      |
|                      | $V^+ = 5\text{ V}, R_L = 600\Omega$ to $2.5\text{ V}$ (LMV824-Q1, LMV824I), Over Temperature |                    |                    | 0.40               |      |
|                      | $V^+ = 5\text{ V}, R_L = 2\text{ k}\Omega$ to $2.5\text{ V}$                                 | 4.85               | 4.90               |                    | V    |
|                      | $V^+ = 5\text{ V}, R_L = 2\text{ k}\Omega$ to $2.5\text{ V}$ , Over Temperature              |                    | 0.10               | 0.15               |      |
| $I_O$ Output Current | Sourcing, $V_O = 0\text{ V}$                                                                 | 20                 | 45                 |                    | mA   |
|                      | Sourcing, $V_O = 0\text{ V}$ , Over Temperature                                              | 15                 |                    |                    |      |
|                      | Sourcing, $V_O = 0\text{ V}$<br>LMV824I                                                      | 20                 | 45                 |                    | mA   |
|                      | Sourcing, $V_O = 0\text{ V}$<br>LMV824I, Over Temperature                                    | 10                 |                    |                    |      |
|                      | Sinking, $V_O = 5\text{ V}$                                                                  | 20                 | 40                 |                    | mA   |
|                      | Sinking, $V_O = 5\text{ V}$ , Over Temperature                                               | 15                 |                    |                    |      |
|                      | Sinking, $V_O = 5\text{ V}$<br>LMV824I                                                       | 20                 | 40                 |                    | mA   |
|                      | Sinking, $V_O = 5\text{ V}$<br>LMV824I, Over Temperature                                     | 10                 |                    |                    |      |
| $I_S$ Supply Current | LMV821 (Single)                                                                              |                    | 0.30               | 0.4                | mA   |
|                      | LMV821, Over Temperature                                                                     |                    |                    | 0.6                |      |
|                      | LMV822 (Dual)                                                                                |                    | 0.5                | 0.7                | mA   |
|                      | LMV822, Over Temperature                                                                     |                    |                    | 0.9                |      |
|                      | LMV824 (Quad)                                                                                |                    | 1.0                | 1.3                | mA   |
|                      | LMV824, Over Temperature                                                                     |                    |                    | 1.5                |      |
|                      | LMV824I (Quad)                                                                               |                    | 1.0                | 1.3                | mA   |
|                      | LMV824I, Over Temperature                                                                    |                    |                    | 1.6                |      |

## 6.11 AC Electrical Characteristics 5V

Unless otherwise specified, all limits ensured for  $T_J = 25^\circ\text{C}$ .  $V^+ = 5\text{ V}$ ,  $V^- = 0\text{ V}$ ,  $V_{CM} = 2.0\text{ V}$ ,  $V_O = 2.5\text{ V}$  and  $R_L > 1\text{ M}\Omega$ . Temperature extremes are  $-40^\circ\text{C} \leq T_J \leq 85^\circ\text{C}$  for LMV821/822/824, and  $-40^\circ\text{C} \leq T_J \leq 125^\circ\text{C}$  for LMV822-Q1/LMV824-Q1/LMV824I.

| PARAMETER |                              | TEST CONDITIONS                                                                            | MIN <sup>(1)</sup> | TYP <sup>(2)</sup> | MAX <sup>(1)</sup> | UNIT                   |
|-----------|------------------------------|--------------------------------------------------------------------------------------------|--------------------|--------------------|--------------------|------------------------|
| SR        | Slew Rate                    | See <sup>(3)</sup>                                                                         | 1.4                | 2.0                |                    | V/ $\mu\text{s}$ min   |
| GBW       | Gain-Bandwidth Product       |                                                                                            |                    | 5.6                |                    | MHz                    |
| $\Phi_m$  | Phase Margin                 |                                                                                            |                    | 67                 |                    | Deg.                   |
| $G_m$     | Gain Margin                  |                                                                                            |                    | 15                 |                    | dB                     |
|           | Amp-to-Amp Isolation         | See <sup>(4)</sup>                                                                         |                    | 135                |                    | dB                     |
| $e_n$     | Input-Related Voltage Noise  | $f = 1\text{ kHz}$ , $V_{CM} = 1\text{ V}$                                                 |                    | 24                 |                    | nV/ $\sqrt{\text{Hz}}$ |
| $i_n$     | Input-Referred Current Noise | $f = 1\text{ kHz}$                                                                         |                    | 0.25               |                    | pA/ $\sqrt{\text{Hz}}$ |
| THD       | Total Harmonic Distortion    | $f = 1\text{ kHz}$ , $A_V = -2$ ,<br>$R_L = 10\text{ k}\Omega$ , $V_O = 4.1\text{ V}_{PP}$ |                    | 0.01%              |                    |                        |

(1) All limits are ensured by testing or statistical analysis.

(2) Typical Values represent the most likely parametric norm.

(3)  $V^+ = 5\text{ V}$ . Connected as voltage follower with 3V step input. Number specified is the slower of the positive and negative slew rates.

(4) Input referred,  $V^+ = 5\text{ V}$  and  $R_L = 100\text{ k}\Omega$  connected to 2.5V. Each amp excited in turn with 1 kHz to produce  $V_O = 3\text{ V}_{PP}$ .

## 6.12 Typical Characteristics

Unless otherwise specified,  $V_S = +5V$ , single supply,  $T_A = 25^\circ C$ .

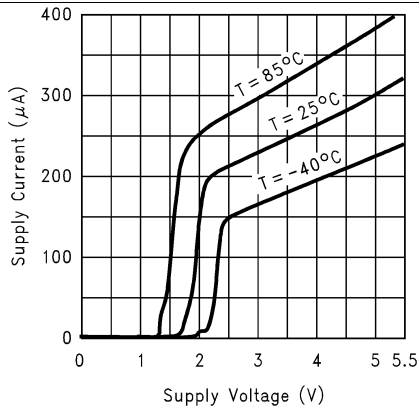


Figure 1. Supply Current vs. Supply Voltage (LMV821)

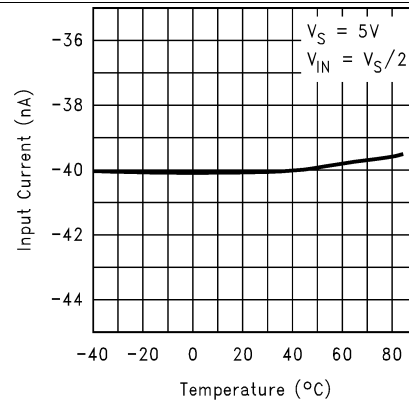


Figure 2. Input Current vs. Temperature

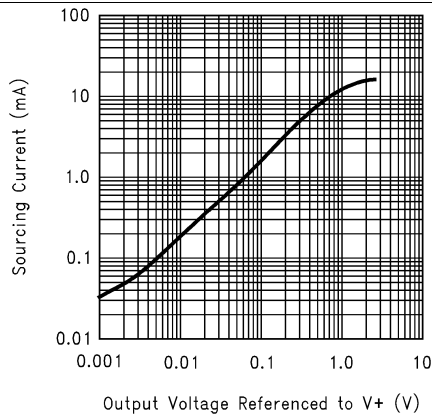


Figure 3. Sourcing Current vs. Output Voltage ( $V_S = 2.7V$ )

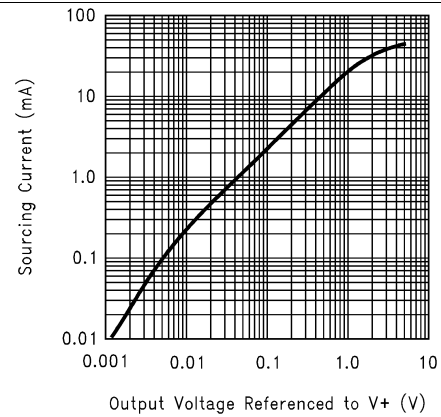


Figure 4. Sourcing Current vs. Output Voltage ( $V_S = 5V$ )

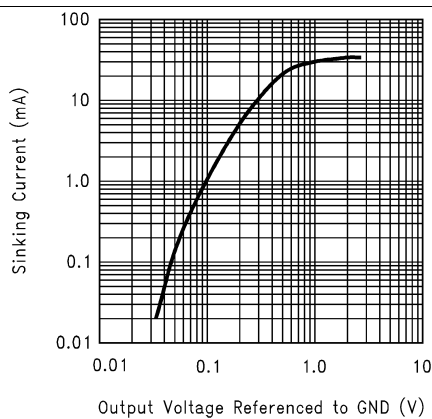


Figure 5. Sinking Current vs. Output Voltage ( $V_S = 2.7V$ )

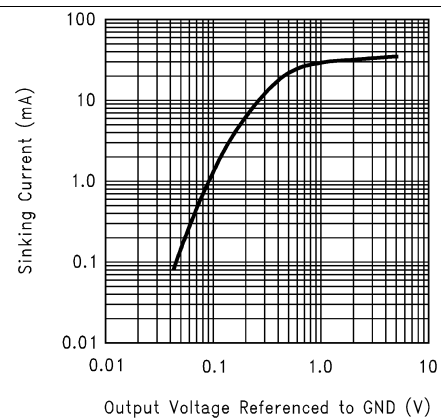


Figure 6. Sinking Current vs. Output Voltage ( $V_S = 5V$ )

## Typical Characteristics (continued)

Unless otherwise specified,  $V_S = +5V$ , single supply,  $T_A = 25^\circ C$ .

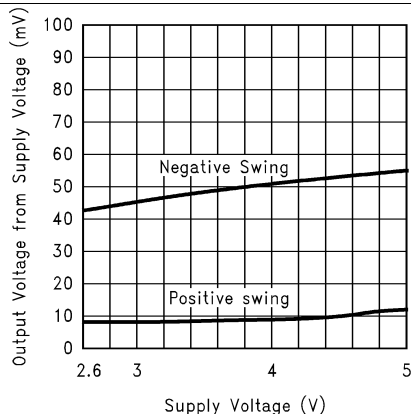


**Figure 7. Output Voltage Swing vs. Supply Voltage ( $R_L = 10k\Omega$ )**

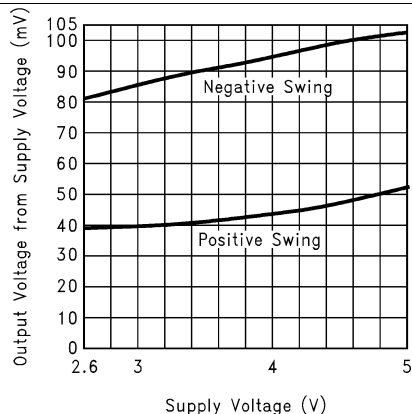


**Figure 8. Output Voltage Swing vs. Supply Voltage ( $R_L = 2k\Omega$ )**

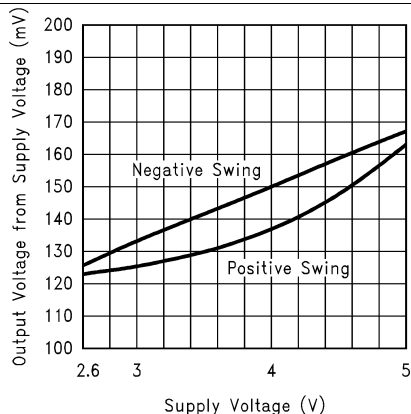


**Figure 9. Output Voltage Swing vs. Supply Voltage ( $R_L = 600\Omega$ )**

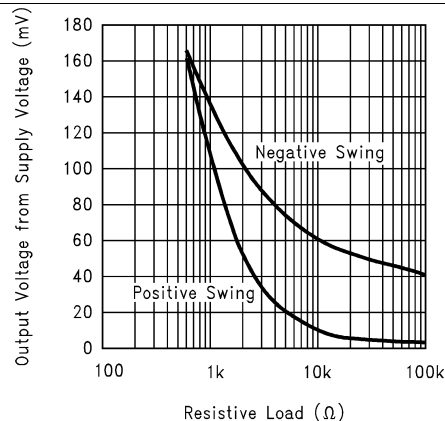


**Figure 10. Output Voltage Swing vs. Load Resistance**

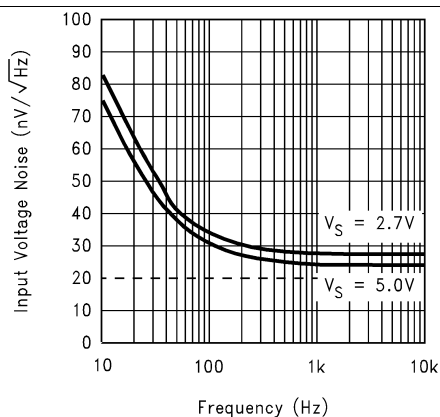


**Figure 11. Input Voltage Noise vs. Frequency**

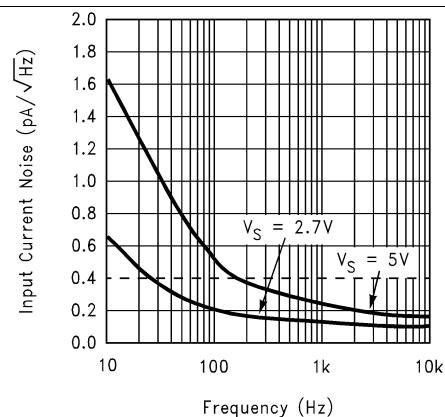


**Figure 12. Input Current Noise vs. Frequency**

## Typical Characteristics (continued)

Unless otherwise specified,  $V_S = +5V$ , single supply,  $T_A = 25^\circ C$ .

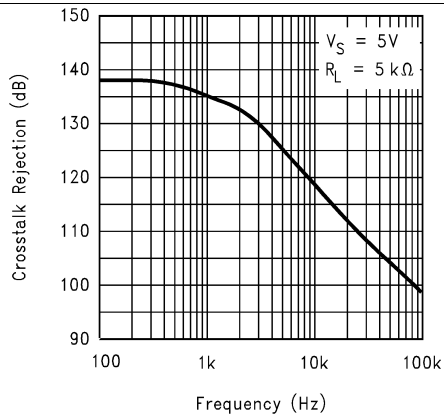


**Figure 13. Crosstalk Rejection vs. Frequency**

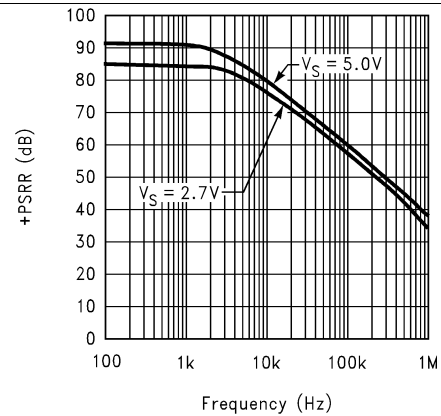


**Figure 14. +PSRR vs. Frequency**

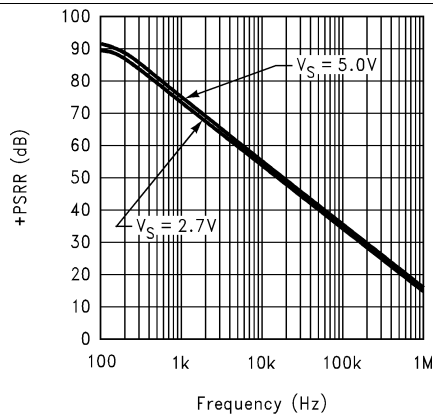


**Figure 15. -PSRR vs. Frequency**

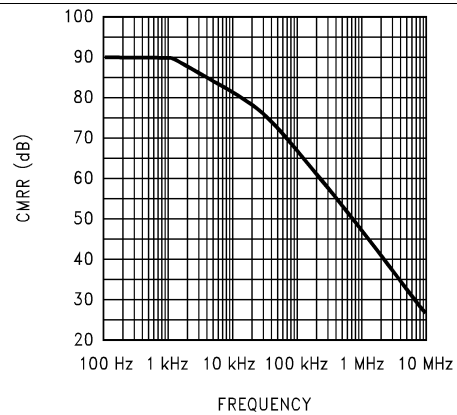


**Figure 16. CMRR vs. Frequency**

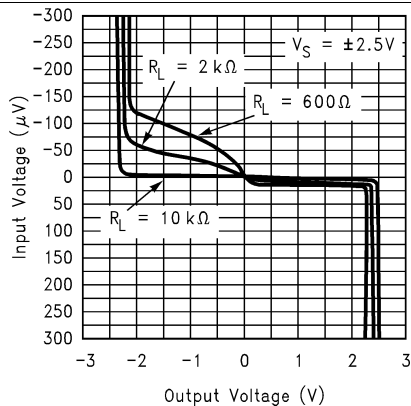


**Figure 17. Input Voltage vs. Output Voltage**

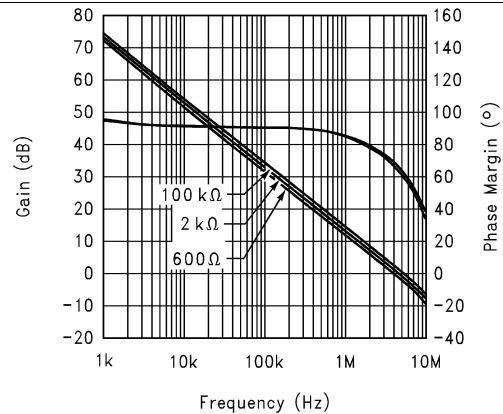


**Figure 18. Gain and Phase Margin vs. Frequency  
( $R_L = 100k\Omega, 2k\Omega, 600\Omega$ ) at 2.7V**

## Typical Characteristics (continued)

Unless otherwise specified,  $V_S = +5V$ , single supply,  $T_A = 25^\circ C$ .

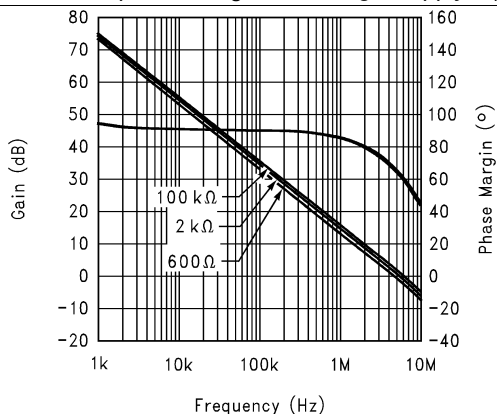


**Figure 19. Gain and Phase Margin vs. Frequency**  
( $R_L = 100k\Omega, 2k\Omega, 600\Omega$ ) at 5V

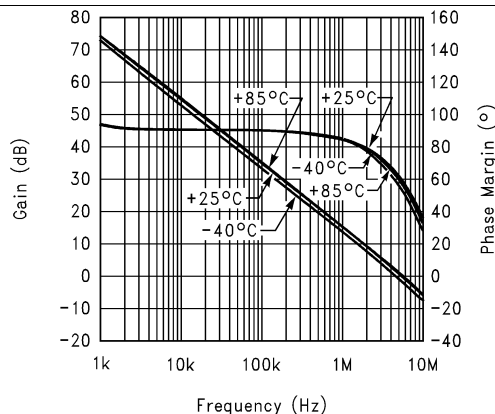


**Figure 20. Gain and Phase Margin vs. Frequency**  
(Temp. = 25, -40, 85°C,  $R_L = 10k\Omega$ ) at 2.7V

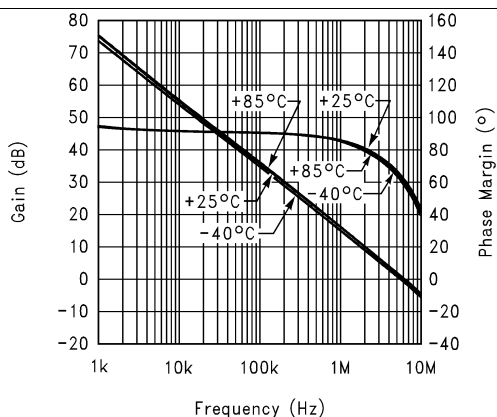


**Figure 21. Gain and Phase Margin vs. Frequency**  
(Temp. = 25, -40, 85°C,  $R_L = 10k\Omega$ ) at 5V

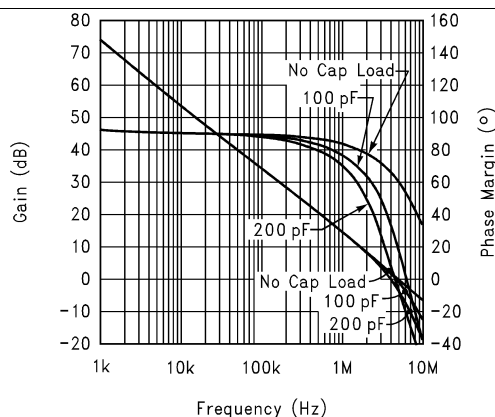


**Figure 22. Gain and Phase Margin vs. Frequency**  
( $C_L = 100pF, 200pF, 0pF$ ,  $R_L = 10k\Omega$ ) at 2.7V

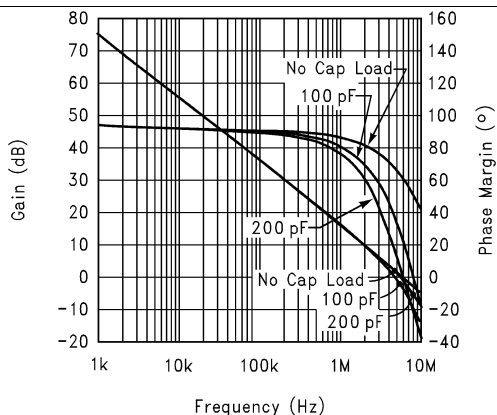


**Figure 23. Gain and Phase Margin vs. Frequency**  
( $C_L = 100pF, 200pF, 0pF$ ,  $R_L = 10k\Omega$ ) at 5V

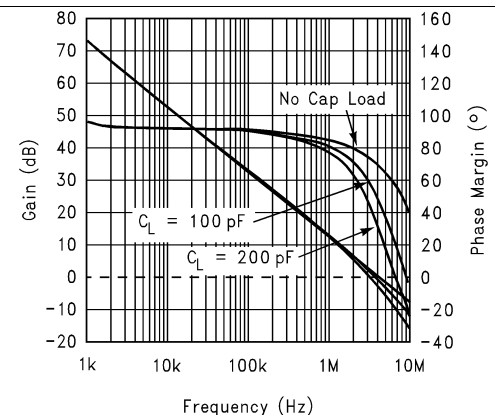


**Figure 24. Gain and Phase Margin vs. Frequency**  
( $C_L = 100pF, 200pF, 0pF$ ,  $R_L = 600\Omega$ ) at 2.7V

## Typical Characteristics (continued)

Unless otherwise specified,  $V_S = +5V$ , single supply,  $T_A = 25^\circ C$ .

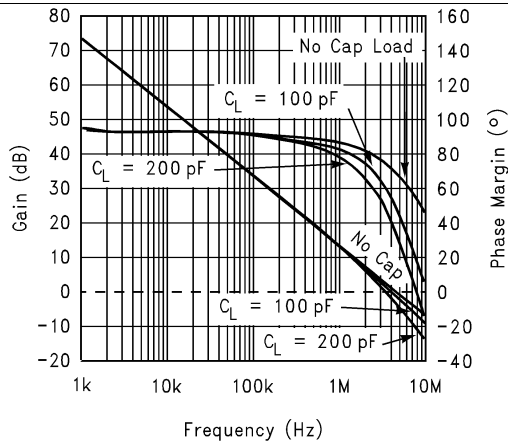


**Figure 25. Gain and Phase Margin vs. Frequency**  
( $C_L = 100pF, 200pF, 0pF$   $R_L = 600\Omega$ ) at 5V

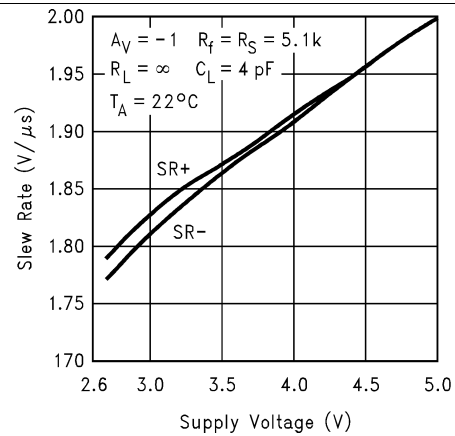


**Figure 26. Slew Rate vs. Supply Voltage**

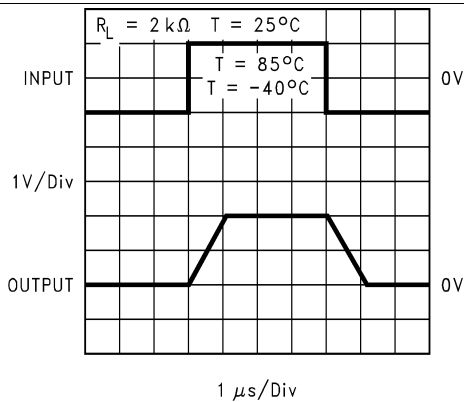


**Figure 27. Non-Inverting Large Signal Pulse Response**

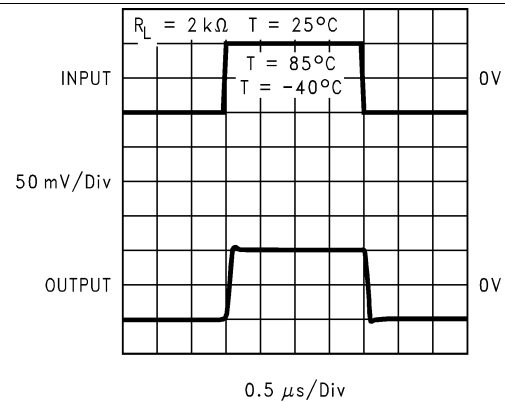


**Figure 28. Non-Inverting Small Signal Pulse Response**

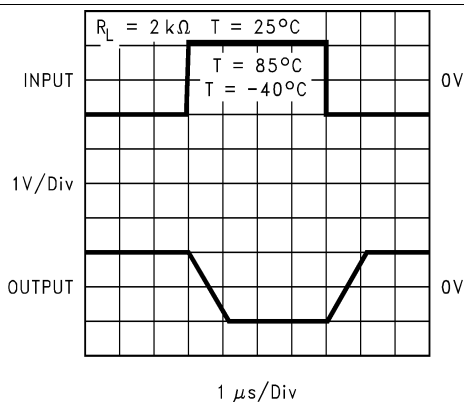


**Figure 29. Inverting Large Signal Pulse Response**

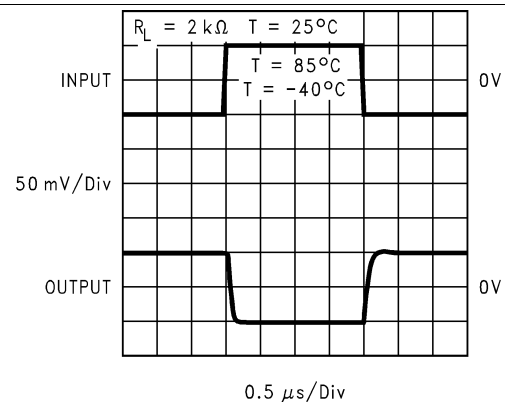


**Figure 30. Inverting Small Signal Pulse Response**

## Typical Characteristics (continued)

Unless otherwise specified,  $V_S = +5V$ , single supply,  $T_A = 25^\circ C$ .

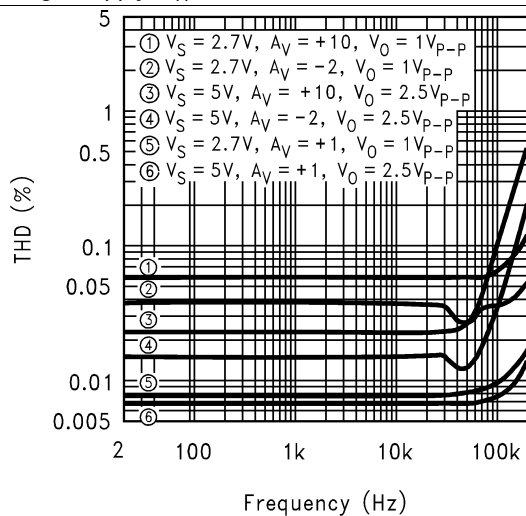


**Figure 31. THD vs. Frequency**

## 7 Detailed Description

### 7.1 Overview

The LMV821/LMV822/LMV824 bring performance and economy to low voltage / low power systems. With a 5 MHz unity-gain frequency and a specified 1.4 V/ $\mu$ s slew rate, the quiescent current is only 220  $\mu$ A/amplifier (2.7 V). They provide rail-to-rail (R-to-R) output swing into heavy loads (600  $\Omega$  specified). The input common-mode voltage range includes ground, and the maximum input offset voltage is 3.5 mV.

### 7.2 Functional Block Diagram

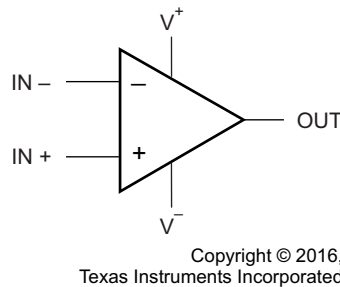


**Figure 32. (Each Amplifier)**

### 7.3 Feature Description

The amplifier's differential inputs consist of a non-inverting input (+IN) and an inverting input (–IN). The amplifier amplifies only the difference in voltage between the two inputs, which is called the differential input voltage. The output voltage of the op-amp  $V_{OUT}$  is given by [Equation 1](#):

$$V_{OUT} = A_{OL} (IN^+ - IN^-) \quad (1)$$

where  $A_{OL}$  is the open-loop gain of the amplifier, typically around 100dB (100,000x, or 10uV per Volt).

### 7.4 Device Functional Modes

This section covers the following design considerations:

1. Frequency and Phase Response Considerations
2. Unity-Gain Pulse Response Considerations
3. Input Bias Current Considerations

#### 7.4.1 Frequency and Phase Response Considerations

The relationship between open-loop frequency response and open-loop phase response determines the closed-loop stability performance (negative feedback). The open-loop phase response causes the feedback signal to shift towards becoming positive feedback, thus becoming unstable. The further the output phase angle is from the input phase angle, the more stable the negative feedback will operate. Phase Margin ( $\phi_m$ ) specifies this output-to-input phase relationship at the unity-gain crossover point. Zero degrees of phase-margin means that the input and output are completely in phase with each other and will sustain oscillation at the unity-gain frequency.

The AC tables show  $\phi_m$  for a no load condition. But  $\phi_m$  changes with load. The Gain and Phase margin vs Frequency plots in the curve section can be used to graphically determine the  $\phi_m$  for various loaded conditions. To do this, examine the phase angle portion of the plot, find the phase margin point at the unity-gain frequency, and determine how far this point is from zero degree of phase-margin. The larger the phase-margin, the more stable the circuit operation.

## Device Functional Modes (continued)

The bandwidth is also affected by load. The graphs of [Figure 33](#) and [Figure 34](#) provide a quick look at how various loads affect the  $\phi_m$  and the bandwidth of the LMV821/822/824 family. These graphs show capacitive loads reducing both  $\phi_m$  and bandwidth, while resistive loads reduce the bandwidth but increase the  $\phi_m$ . Notice how a 600 $\Omega$  resistor can be added in parallel with 220 pF capacitance, to increase the  $\phi_m$  20°(approx.), but at the price of about a 100 kHz of bandwidth.

Overall, the LMV821/822/824 family provides good stability for loaded condition.

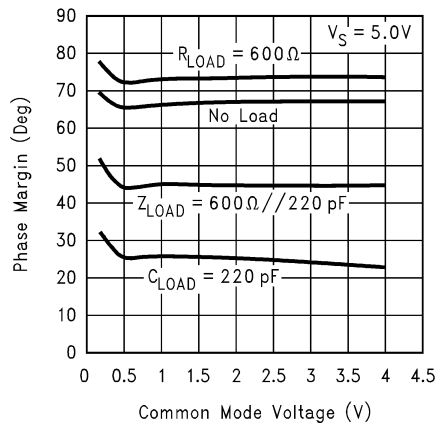


**Figure 33. Phase Margin vs Common Mode Voltage for Various Loads**

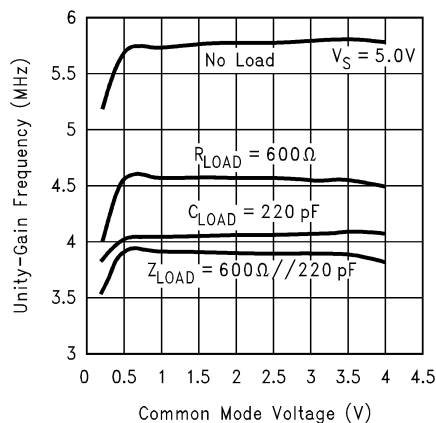


**Figure 34. Unity-Gain Frequency vs Common Mode Voltage for Various Loads**

### 7.4.2 Unity Gain Pulse Response Consideration

A pull-up resistor is well suited for increasing unity-gain, pulse response stability. For example, a 600  $\Omega$  pull-up resistor reduces the overshoot voltage by about 50%, when driving a 220 pF load. [Figure 35](#) shows how to implement the pull-up resistor for more pulse response stability.

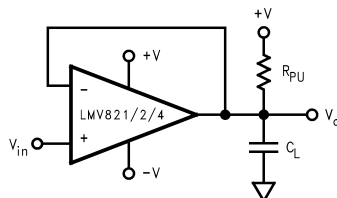


**Figure 35. Using a Pull-up Resistor at the Output for Stabilizing Capacitive Loads**

## Device Functional Modes (continued)

Higher capacitances can be driven by decreasing the value of the pull-up resistor, but its value shouldn't be reduced beyond the sinking capability of the part. An alternate approach is to use an isolation resistor as illustrated in [Figure 36](#).

[Figure 37](#) shows the resulting pulse response from a LMV824, while driving a 10,000 pF load through a 20Ω isolation resistor.

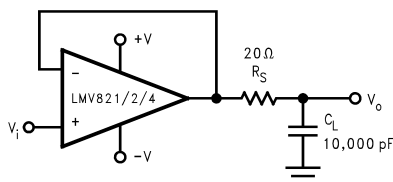


**Figure 36. Using an Isolation Resistor to Drive Heavy Capacitive Loads**

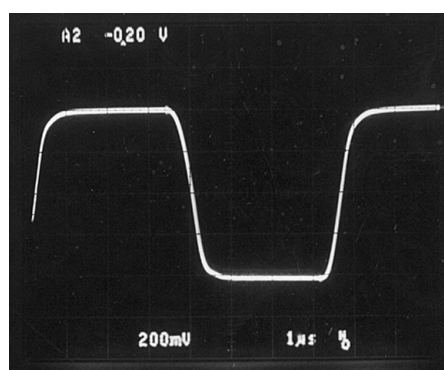


**Figure 37. Pulse Response per [Figure 36](#)**

### 7.4.3 Input Bias Current Consideration

Input bias current ( $I_B$ ) can develop a somewhat significant offset voltage. This offset is primarily due to  $I_B$  flowing through the negative feedback resistor,  $R_F$ . For example, if  $I_B$  is 90 nA (max @ room) and  $R_F$  is 100 kΩ, then an offset of 9 mV will be developed ( $V_{OS} = I_B \times R_F$ ). Using a compensation resistor ( $R_C$ ), as shown in [Figure 38](#), cancels out this affect. But the input offset current ( $I_{OS}$ ) will still contribute to an offset voltage in the same manner - typically 0.05 mV at room temp.

## Device Functional Modes (continued)

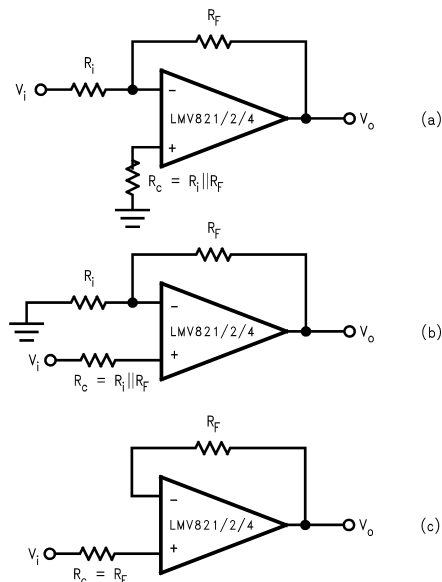


Figure 38. Canceling the Voltage Offset Effect of Input Bias Current

## 8 Application and Implementation

### 8.1 Application Information

The LMV82x bring performance and economy to low voltage/low power systems. They provide rail-to-rail output swing into heavy loads and are capable of driving large capacitive loads.

### 8.2 Typical Applications

#### 8.2.1 Telephone-Line Transceiver

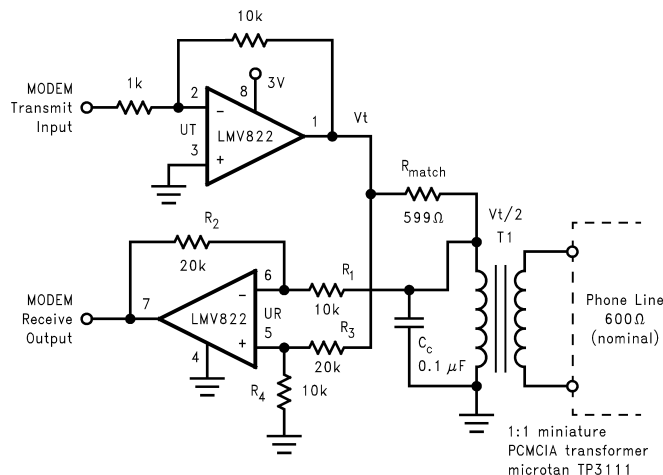


Figure 39. Telephone-Line Transceiver for a PCMCIA Modem Card

## Typical Applications (continued)

### 8.2.1.1 Design Requirements

The telephone-line transceiver of [Figure 39](#) provides a full-duplexed connection through a PCMCIA, miniature transformer. The differential configuration of receiver portion (UR), cancels reception from the transmitter portion (UT). Note that the input signals for the differential configuration of UR, are the transmit voltage ( $V_T$ ) and  $V_T/2$ . This is because  $R_{match}$  is chosen to match the coupled telephone-line impedance; therefore dividing  $V_T$  by two (assuming  $R1 \gg R_{match}$ ).

### 8.2.1.2 Detailed Design Procedure

The differential configuration of UR has its resistors chosen to cancel the  $V_T$  and  $V_T/2$  inputs according to the following equation:

$$V_O = V_T \left( \frac{R_4}{R_3 + R_4} \right) \left( 1 + \frac{R_2}{R_1} \right) - \frac{V_T}{2} \left( \frac{R_2}{R_1} \right) = V_T \frac{1}{3} (3) - \frac{V_T}{2} (2) = 0 \quad (2)$$

Note that  $C_c$  is included for canceling out the inadequacies of the lossy, miniature transformer.

### 8.2.2 “Simple” Mixer (Amplitude Modulator)

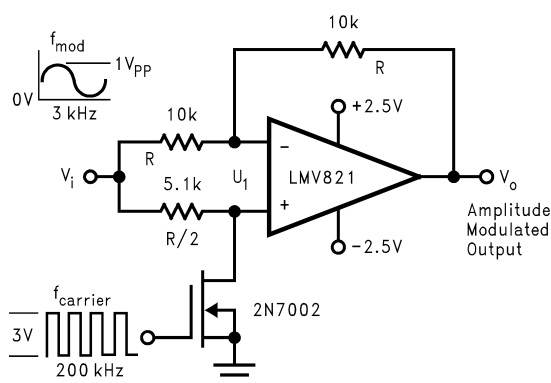


**Figure 40. Amplitude Modulator Circuit**

### 8.2.2.1 Design Requirements

The simple mixer can be applied to applications that utilize the Doppler Effect to measure the velocity of an object. The difference frequency is one of its output frequency components. This difference frequency magnitude ( $|F_M - F_C|$ ) is the key factor for determining an object's velocity per the Doppler Effect. If a signal is transmitted to a moving object, the reflected frequency will be a different frequency. This difference in transmit and receive frequency is directly proportional to an object's velocity.

### 8.2.2.2 Detailed Design Procedure

The mixer of [Figure 40](#) is simple and provides a unique form of amplitude modulation.  $V_i$  is the modulation frequency ( $F_M$ ), while a +3V square-wave at the gate of Q1, induces a carrier frequency ( $F_C$ ). Q1 switches (toggles)  $U1$  between inverting and non-inverting unity gain configurations. Offsetting a sine wave above ground at  $V_i$  results in the oscilloscope photo of [Figure 41](#).

## Typical Applications (continued)

### 8.2.2.3 Application Performance Plot

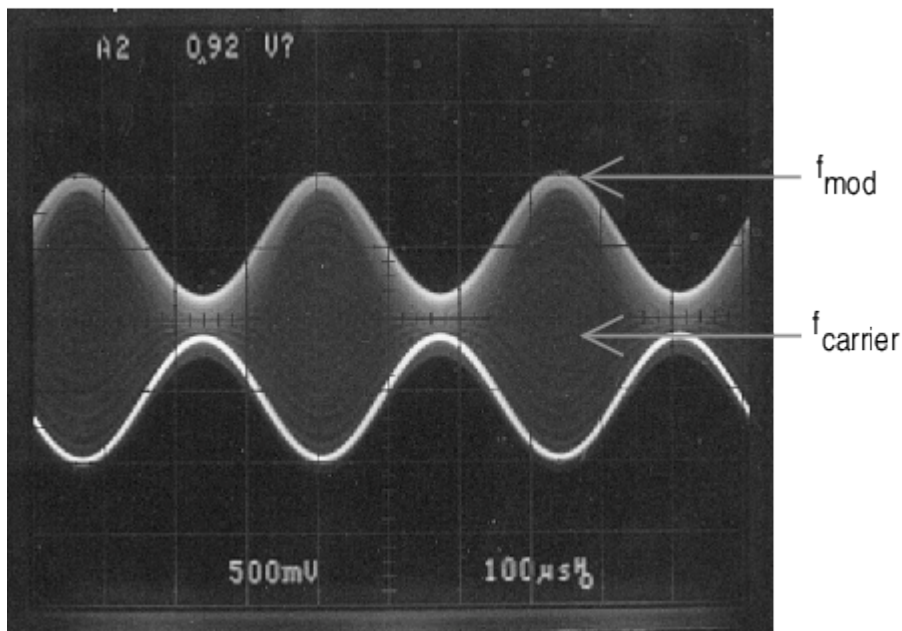


Figure 41. Output signal of Figure 40

### 8.2.3 Tri-Level Voltage Detector

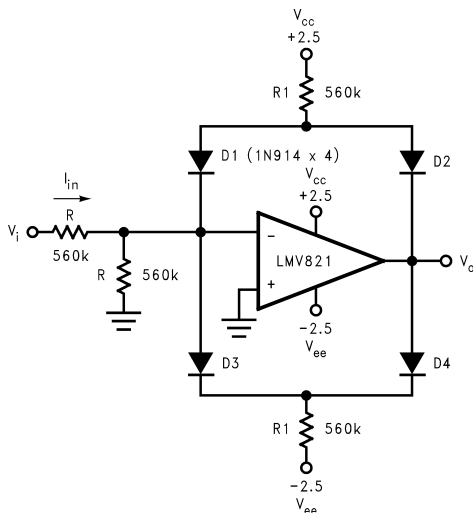


Figure 42. Tri-level Voltage Detector

#### 8.2.3.1 Design Requirements

The tri-level voltage detector of Figure 42 provides a type of window comparator function. It detects three different input voltage ranges: Min-range, Mid-range, and Max-range. The output voltage ( $V_O$ ) is at  $V_{CC}$  for the Min-range.  $V_O$  is clamped at GND for the Mid-range. For the Max-range,  $V_O$  is at  $V_{EE}$ . Figure 43 shows a  $V_O$  vs.  $V_I$  oscilloscope photo per the circuit of Figure 42.

## Typical Applications (continued)

Its operation is as follows:  $V_I$  deviating from GND, causes the diode bridge to absorb  $I_{IN}$  to maintain a clamped condition ( $V_O = 0V$ ). Eventually,  $I_{IN}$  reaches the bias limit of the diode bridge. When this limit is reached, the clamping effect stops and the op amp responds open loop. The design equation directly preceding Figure 43, shows how to determine the clamping range. The equation solves for the input voltage band on each side GND. The mid-range is twice this voltage band.

### 8.2.3.2 Detailed Design Procedure

$$\Delta V = \frac{R}{R_1} (V_{CC} - V_{Diode}) \quad (3)$$

### 8.2.3.3 Application Performance Plot

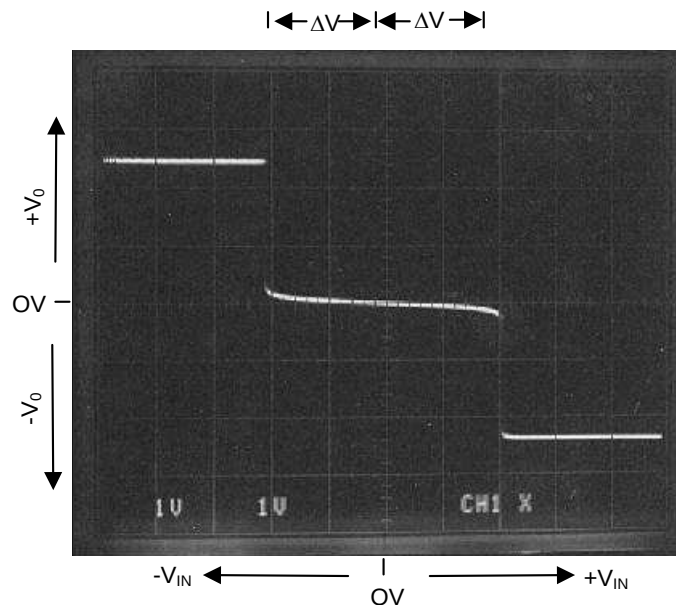
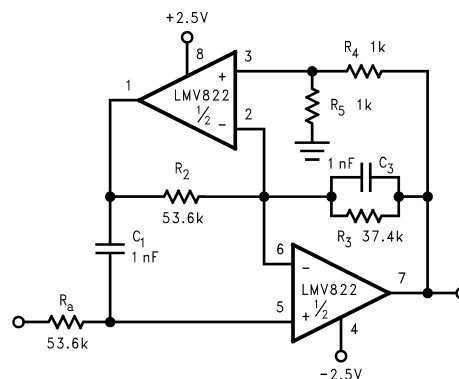


Figure 43. X, Y Oscilloscope Trace showing  $V_{OUT}$  vs  $V_{IN}$  per the Circuit of Tri-Level Voltage Detector

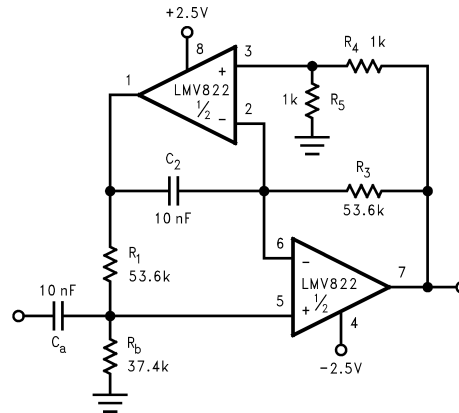
## 8.2.4 Dual Amplifier Active Filters (DAAFs)



3 kHz Low-Pass Active Filter with a Butterworth Response and a Pass Band Gain of Times Two

Figure 44. Dual Amplifier Active Low-Pass Filter

## Typical Applications (continued)



300 Hz High-Pass Active Filter with a Butterworth Response and a Pass Band Gain of Times Two

**Figure 45. Dual Active Amplifier High-Pass Filter**

### 8.2.4.1 Design Requirements

The LMV822/24 bring economy and performance to DAAFs. The low-pass and the high-pass filters of [Figure 44](#) and [Figure 45](#) (respectively), offer one key feature: excellent sensitivity performance. Good sensitivity is when deviations in component values cause relatively small deviations in a filter's parameter such as cutoff frequency (Fc). Single amplifier active filters like the Sallen-Key provide relatively poor sensitivity performance that sometimes cause problems for high production runs; their parameters are much more likely to deviate out of specification than a DAAF would. The DAAFs of [Figure 44](#) and [Figure 45](#) are well suited for high volume production.

### 8.2.4.2 Detailed Design Procedure

Active filters are also sensitive to an op amp's parameters -Gain and Bandwidth, in particular. The LMV822/24 provide a large gain and wide bandwidth. And DAAFs make excellent use of these feature specifications.

Single Amplifier versions require a large open-loop to closed-loop gain ratio - approximately 50 to 1, at the Fc of the filter response.

In addition to performance, DAAFs are relatively easy to design and implement. The design equations for the low-pass and high-pass DAAFs are shown below. The first two equations calculate the Fc and the circuit Quality Factor (Q) for the LPF ([Figure 44](#)). The second two equations calculate the Fc and Q for the HPF ([Figure 45](#)).

$$\begin{aligned}
 \text{(LPF)} \quad F_C &= \frac{\sqrt{R_5}}{2\pi \sqrt{R_a} \cdot \sqrt{R_2} \cdot \sqrt{R_4} \cdot \sqrt{C_1} \cdot \sqrt{C_3}} \\
 Q &= 2\pi F_C \sqrt{C_1} \cdot \sqrt{C_3} \\
 \text{(HPF)} \quad F_C &= \frac{\sqrt{R_4}}{2\pi \sqrt{R_1} \cdot \sqrt{R_3} \cdot \sqrt{R_5} \cdot \sqrt{C_a} \cdot \sqrt{C_2}} \\
 Q &= 2\pi F_C \sqrt{C_a} \cdot \sqrt{C_2}
 \end{aligned} \tag{4}$$

To simplify the design process, certain components are set equal to each other. Refer to [Figure 44](#) and [Figure 45](#). These equal component values help to simplify the design equations as follows:

$$\begin{aligned}
 \text{(LPF)} \quad R_a &= R_2 = \frac{1}{2\pi F_C \sqrt{C_1} \cdot \sqrt{C_3}} \\
 R_3 &= \frac{Q}{2\pi F_C \sqrt{C_1} \cdot \sqrt{C_3}} \\
 \text{(HPF)} \quad R_1 &= R_3 = \frac{1}{2\pi F_C \sqrt{C_a} \cdot \sqrt{C_2}} \\
 R_b &= \frac{Q}{2\pi F_C \sqrt{C_a} \cdot \sqrt{C_2}}
 \end{aligned} \tag{5}$$

## Typical Applications (continued)

To illustrate the design process/implementation, a 3 kHz, Butterworth response, low-pass filter DAAF (Figure 44) is designed as follows:

1. Choose  $C_1 = C_3 = C = 1 \text{ nF}$
2. Choose  $R_4 = R_5 = 1 \text{ k}\Omega$
3. Calculate  $R_a$  and  $R_2$  for the desired  $F_c$  as follows:

$$\begin{aligned} R_a = R_2 &= \frac{1}{2\pi(F_c)C} \\ &= \frac{1}{2\pi(3 \text{ kHz})1 \text{ nF}} \\ &= 53.1 \text{ k}\Omega \\ &\cong 53.6 \text{ k}\Omega \text{ (Practical Value)} \end{aligned} \quad (6)$$

4. Calculate  $R_3$  for the desired  $Q$ . The desired  $Q$  for a Butterworth (Maximally Flat) response is 0.707 (45 degrees into the s-plane).  $R_3$  calculates as follows:

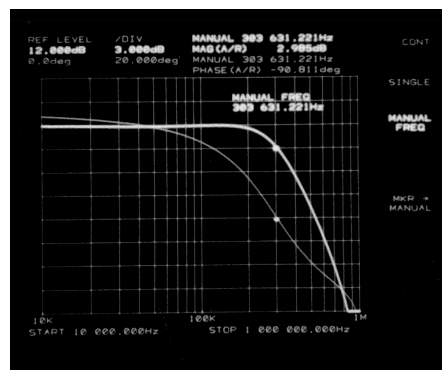
$$\begin{aligned} R_3 &= \frac{Q}{2\pi(F_c)C} \\ &= \frac{0.707}{2\pi(3 \text{ kHz})1 \text{ nF}} \\ &= 37.5 \text{ k}\Omega \\ &\cong 37.4 \text{ k}\Omega \text{ (Practical Value)} \end{aligned} \quad (7)$$

Notice that  $R_3$  could also be calculated as 0.707 of  $R_a$  or  $R_2$ .

The circuit was implemented and its cutoff frequency measured. The cutoff frequency measured at 2.92 kHz.

The circuit also showed good repeatability. Ten different LMV822 samples were placed in the circuit. The corresponding change in the cutoff frequency was less than a percent.

### 8.2.4.3 Application Performance Plots



Butterworth Response as Measured by the HP3577A Network Analyzer

**Figure 46. 300 kHz, DAAF Low-Pass Filter Measurement Results**

Figure 46 shows an impressive photograph of a network analyzer measurement (HP3577A). The measurement was taken from a 300 kHz version of Figure 44. At 300 kHz, the open-loop to closed-loop gain ratio @  $F_c$  is about 5 to 1. This is 10 times lower than the 50 to 1 “rule of thumb” for Single Amplifier Active Filters.

Table 1 provides sensitivity measurements for a 10 M $\Omega$  load condition. The left column shows the passive components for the 3 kHz low-pass DAAF. The third column shows the components for the 300 Hz high-pass DAAF. Their respective sensitivity measurements are shown to the right of each component column. Their values consists of the percent change in cutoff frequency ( $F_c$ ) divided by the percent change in component value. The lower the sensitivity value, the better the performance.

## Typical Applications (continued)

Each resistor value was changed by about 10 percent, and this measured change was divided into the measured change in  $F_c$ . A positive or negative sign in front of the measured value, represents the direction  $F_c$  changes relative to components' direction of change. For example, a sensitivity value of negative 1.2, means that for a 1 percent increase in component value,  $F_c$  decreases by 1.2 percent.

Note that this information provides insight on how to fine tune the cutoff frequency, if necessary. It should be also noted that  $R_4$  and  $R_5$  of each circuit also caused variations in the pass band gain. Increasing  $R_4$  by ten percent, increased the gain by 0.4 dB, while increasing  $R_5$  by ten percent, decreased the gain by 0.4 dB.

**Table 1. Component Sensitivity Measurements**

| Component (LPF) | Sensitivity (LPF) | Component (HPF) | Sensitivity (HPF) |
|-----------------|-------------------|-----------------|-------------------|
| $R_a$           | -1.2              | $C_a$           | -0.7              |
| $C_1$           | -0.1              | $R_b$           | -1.0              |
| $R_2$           | -1.1              | $R_1$           | +0.1              |
| $R_3$           | +0.7              | $C_2$           | -0.1              |
| $C_3$           | -1.5              | $R_3$           | +0.1              |
| $R_4$           | -0.6              | $R_4$           | -0.1              |
| $R_5$           | +0.6              | $R_5$           | +0.1              |

## 8.3 Do's and Don'ts

Do properly bypass the power supplies.

Do add series resistance to the output when driving capacitive loads, particularly cables, Muxes and ADC inputs.

Do not exceed the input common mode range. The input is not "Rail to Rail" and will limit upper output swing when configured as followers or other low-gain applications. See the Input Common Mode Voltage Range section of the Electrical Table.

Do add series current limiting resistors and external schottky clamp diodes if input voltage is expected to exceed the supplies. Limit the current to 1mA or less (1K $\Omega$  per volt).

## 9 Power Supply Recommendations

For proper operation, the power supplies must be properly decoupled. For decoupling the supply lines it is suggested that 10 nF capacitors be placed as close as possible to the op amp power supply pins. For single supply, place a capacitor between  $V^+$  and  $V^-$  supply leads. For dual supplies, place one capacitor between  $V^+$  and ground, and one capacitor between  $V^-$  and ground.

## 10 Layout

### 10.1 Layout Guidelines

The V+ pin should be bypassed to ground with a low ESR capacitor.

The optimum placement is closest to the V+ and ground pins.

Care should be taken to minimize the loop area formed by the bypass capacitor connection between V+ and ground.

The ground pin should be connected to the PCB ground plane at the pin of the device.

The feedback components should be placed as close to the device as possible minimizing strays.

### 10.2 Layout Example

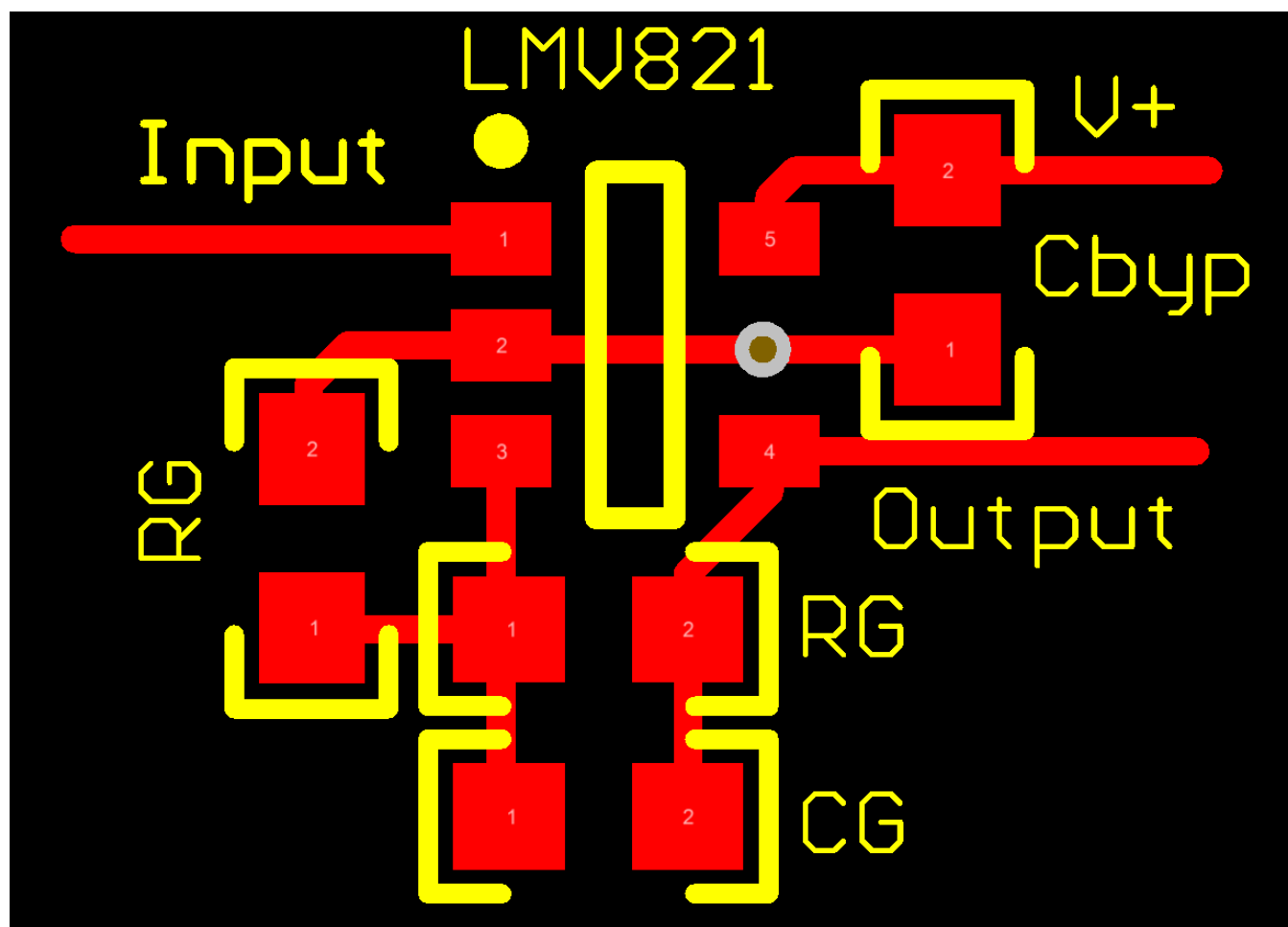


Figure 47. 2-D Layout

## Layout Example (continued)

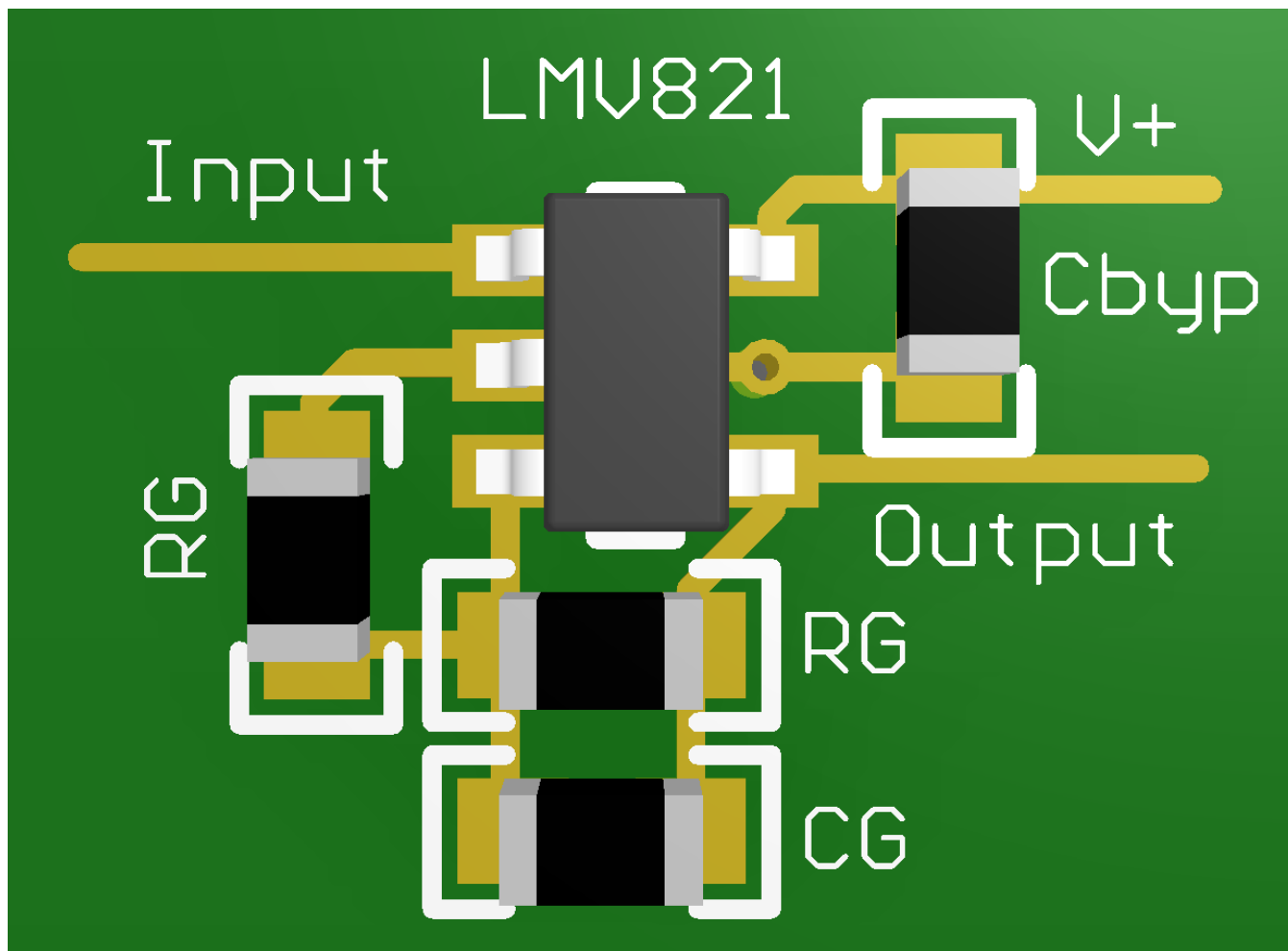


Figure 48. 3-D Layout

## 11 デバイスおよびドキュメントのサポート

### 11.1 ドキュメントのサポート

#### 11.1.1 関連資料

関連資料については、以下を参照してください。

- [TI Filterproソフトウェア](#)
- [TIユニバーサル・オペアンプ評価モジュール](#)
- [TINA-TI SPICE ベースのアナログ・シミュレーション・プログラム](#)

### 11.2 ドキュメントの更新通知を受け取る方法

ドキュメントの更新についての通知を受け取るには、[ti.com](http://ti.com)のデバイス製品フォルダを開いてください。右上の隅にある「通知を受け取る」をクリックして登録すると、変更されたすべての製品情報に関するダイジェストを毎週受け取れます。変更の詳細については、修正されたドキュメントに含まれている改訂履歴をご覧ください。

### 11.3 コミュニティ・リソース

The following links connect to TI community resources. Linked contents are provided "AS IS" by the respective contributors. They do not constitute TI specifications and do not necessarily reflect TI's views; see TI's [Terms of Use](#).

**TI E2E™ オンライン・コミュニティ** *TIのE2E (Engineer-to-Engineer) コミュニティ*。エンジニア間の共同作業を促進するために開設されたものです。e2e.ti.comでは、他のエンジニアに質問し、知識を共有し、アイデアを検討して、問題解決に役立てることができます。

**設計サポート** *TIの設計サポート* 役に立つE2Eフォーラムや、設計サポート・ツールをすばやく見つけることができます。技術サポート用の連絡先情報も参照できます。

### 11.4 関連リンク

次の表に、クイック・アクセス・リンクを示します。カテゴリには、技術資料、サポートおよびコミュニティ・リソース、ツールとソフトウェア、およびサンプル注文またはご購入へのクイック・アクセスが含まれます。

**表 2. 関連リンク**

| 製品          | プロダクト・フォルダ              | サンプルとご購入                | 技術資料                    | ツールとソフトウェア              | サポートとコミュニティ             |
|-------------|-------------------------|-------------------------|-------------------------|-------------------------|-------------------------|
| LMV821-N    | <a href="#">ここをクリック</a> | <a href="#">ここをクリック</a> | <a href="#">ここをクリック</a> | <a href="#">ここをクリック</a> | <a href="#">ここをクリック</a> |
| LMV822-N    | <a href="#">ここをクリック</a> | <a href="#">ここをクリック</a> | <a href="#">ここをクリック</a> | <a href="#">ここをクリック</a> | <a href="#">ここをクリック</a> |
| LMV822-N-Q1 | <a href="#">ここをクリック</a> | <a href="#">ここをクリック</a> | <a href="#">ここをクリック</a> | <a href="#">ここをクリック</a> | <a href="#">ここをクリック</a> |
| LMV824-N    | <a href="#">ここをクリック</a> | <a href="#">ここをクリック</a> | <a href="#">ここをクリック</a> | <a href="#">ここをクリック</a> | <a href="#">ここをクリック</a> |
| LMV824-N-Q1 | <a href="#">ここをクリック</a> | <a href="#">ここをクリック</a> | <a href="#">ここをクリック</a> | <a href="#">ここをクリック</a> | <a href="#">ここをクリック</a> |

### 11.5 商標

E2E is a trademark of Texas Instruments.

All other trademarks are the property of their respective owners.

### 11.6 静電気放電に関する注意事項



これらのデバイスは、限定的なESD(静電破壊)保護機能を内蔵しています。保存時または取り扱い時は、MOSゲートに対する静電破壊を防止するために、リード線同士をショートさせておくか、デバイスを導電フォームに入れる必要があります。

### 11.7 Glossary

[SLYZ022](#) — *TI Glossary*.

This glossary lists and explains terms, acronyms, and definitions.

## 12 メカニカル、パッケージ、および注文情報

以降のページには、メカニカル、パッケージ、および注文に関する情報が記載されています。この情報は、そのデバイスについて利用可能な最新のデータです。このデータは予告なく変更されることがあり、ドキュメントが改訂される場合もあります。本データシートのブラウザ版を使用されている場合は、画面左側の説明をご覧ください。

## PACKAGING INFORMATION

| Orderable part number            | Status<br>(1) | Material type<br>(2) | Package   Pins   | Package qty   Carrier | RoHS<br>(3) | Lead finish/<br>Ball material<br>(4) | MSL rating/<br>Peak reflow<br>(5) | Op temp (°C) | Part marking<br>(6) |
|----------------------------------|---------------|----------------------|------------------|-----------------------|-------------|--------------------------------------|-----------------------------------|--------------|---------------------|
| <a href="#">LMV821M5/NOPB</a>    | Active        | Production           | SOT-23 (DBV)   5 | 1000   SMALL T&R      | Yes         | SN                                   | Level-1-260C-UNLIM                | -40 to 85    | A14                 |
| LMV821M5/NOPB.A                  | Active        | Production           | SOT-23 (DBV)   5 | 1000   SMALL T&R      | Yes         | SN                                   | Level-1-260C-UNLIM                | -40 to 85    | A14                 |
| <a href="#">LMV821M5X/NOPB</a>   | Active        | Production           | SOT-23 (DBV)   5 | 3000   LARGE T&R      | Yes         | SN                                   | Level-1-260C-UNLIM                | -40 to 85    | A14                 |
| LMV821M5X/NOPB.A                 | Active        | Production           | SOT-23 (DBV)   5 | 3000   LARGE T&R      | Yes         | SN                                   | Level-1-260C-UNLIM                | -40 to 85    | A14                 |
| <a href="#">LMV821M7/NOPB</a>    | Active        | Production           | SC70 (DCK)   5   | 1000   SMALL T&R      | Yes         | SN                                   | Level-1-260C-UNLIM                | -40 to 85    | A15                 |
| LMV821M7/NOPB.A                  | Active        | Production           | SC70 (DCK)   5   | 1000   SMALL T&R      | Yes         | SN                                   | Level-1-260C-UNLIM                | -40 to 85    | A15                 |
| <a href="#">LMV821M7X/NOPB</a>   | Active        | Production           | SC70 (DCK)   5   | 3000   LARGE T&R      | Yes         | SN                                   | Level-1-260C-UNLIM                | -40 to 85    | A15                 |
| LMV821M7X/NOPB.A                 | Active        | Production           | SC70 (DCK)   5   | 3000   LARGE T&R      | Yes         | SN                                   | Level-1-260C-UNLIM                | -40 to 85    | A15                 |
| <a href="#">LMV822M/NOPB</a>     | Active        | Production           | SOIC (D)   8     | 95   TUBE             | Yes         | SN                                   | Level-1-260C-UNLIM                | -40 to 85    | LMV<br>822M         |
| LMV822M/NOPB.A                   | Active        | Production           | SOIC (D)   8     | 95   TUBE             | Yes         | SN                                   | Level-1-260C-UNLIM                | -40 to 85    | LMV<br>822M         |
| <a href="#">LMV822MM/NOPB</a>    | Active        | Production           | VSSOP (DGK)   8  | 1000   SMALL T&R      | Yes         | NIPDAUAG   SN                        | Level-1-260C-UNLIM                | -40 to 85    | V822                |
| LMV822MM/NOPB.A                  | Active        | Production           | VSSOP (DGK)   8  | 1000   SMALL T&R      | Yes         | SN                                   | Level-1-260C-UNLIM                | -40 to 85    | V822                |
| <a href="#">LMV822MMX/NOPB</a>   | Active        | Production           | VSSOP (DGK)   8  | 3500   LARGE T&R      | Yes         | NIPDAUAG   SN                        | Level-1-260C-UNLIM                | -40 to 85    | V822                |
| LMV822MMX/NOPB.A                 | Active        | Production           | VSSOP (DGK)   8  | 3500   LARGE T&R      | Yes         | SN                                   | Level-1-260C-UNLIM                | -40 to 85    | V822                |
| <a href="#">LMV822MX/NOPB</a>    | Active        | Production           | SOIC (D)   8     | 2500   LARGE T&R      | Yes         | SN                                   | Level-1-260C-UNLIM                | -40 to 85    | LMV<br>822M         |
| LMV822MX/NOPB.A                  | Active        | Production           | SOIC (D)   8     | 2500   LARGE T&R      | Yes         | SN                                   | Level-1-260C-UNLIM                | -40 to 85    | LMV<br>822M         |
| <a href="#">LMV822Q1MM/NOPB</a>  | Active        | Production           | VSSOP (DGK)   8  | 1000   SMALL T&R      | Yes         | NIPDAU   SN                          | Level-1-260C-UNLIM                | -40 to 125   | AKAA                |
| LMV822Q1MM/NOPB.A                | Active        | Production           | VSSOP (DGK)   8  | 1000   SMALL T&R      | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 125   | AKAA                |
| <a href="#">LMV822Q1MMX/NOPB</a> | Active        | Production           | VSSOP (DGK)   8  | 3500   LARGE T&R      | Yes         | NIPDAU   SN                          | Level-1-260C-UNLIM                | -40 to 125   | AKAA                |
| LMV822Q1MMX/NOPB.A               | Active        | Production           | VSSOP (DGK)   8  | 3500   LARGE T&R      | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 125   | AKAA                |
| <a href="#">LMV824M/NOPB</a>     | Active        | Production           | SOIC (D)   14    | 55   TUBE             | Yes         | SN                                   | Level-1-260C-UNLIM                | -40 to 85    | LMV824M             |
| LMV824M/NOPB.A                   | Active        | Production           | SOIC (D)   14    | 55   TUBE             | Yes         | SN                                   | Level-1-260C-UNLIM                | -40 to 85    | LMV824M             |
| <a href="#">LMV824MT/NOPB</a>    | Active        | Production           | TSSOP (PW)   14  | 94   TUBE             | Yes         | NIPDAU   SN                          | Level-1-260C-UNLIM                | -40 to 85    | LMV824<br>MT        |
| LMV824MT/NOPB.A                  | Active        | Production           | TSSOP (PW)   14  | 94   TUBE             | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 85    | LMV824<br>MT        |

| Orderable part number            | Status<br>(1) | Material type<br>(2) | Package   Pins   | Package qty   Carrier | RoHS<br>(3) | Lead finish/<br>Ball material<br>(4) | MSL rating/<br>Peak reflow<br>(5) | Op temp (°C) | Part marking<br>(6) |
|----------------------------------|---------------|----------------------|------------------|-----------------------|-------------|--------------------------------------|-----------------------------------|--------------|---------------------|
| <a href="#">LMV824MTX/NOPB</a>   | Active        | Production           | TSSOP (PW)   14  | 2500   LARGE T&R      | Yes         | NIPDAU   SN                          | Level-1-260C-UNLIM                | -40 to 85    | LMV824<br>MT        |
| LMV824MTX/NOPB.A                 | Active        | Production           | TSSOP (PW)   14  | 2500   LARGE T&R      | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 85    | LMV824<br>MT        |
| <a href="#">LMV824MX/NOPB</a>    | Active        | Production           | SOIC (D)   14    | 2500   LARGE T&R      | Yes         | SN                                   | Level-1-260C-UNLIM                | -40 to 85    | LMV824M             |
| LMV824MX/NOPB.A                  | Active        | Production           | SOIC (D)   14    | 2500   LARGE T&R      | Yes         | SN                                   | Level-1-260C-UNLIM                | -40 to 85    | LMV824M             |
| <a href="#">LMV824NDGVR</a>      | Active        | Production           | TVSOP (DGV)   14 | 2000   LARGE T&R      | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 125   | MV824N              |
| LMV824NDGVR.A                    | Active        | Production           | TVSOP (DGV)   14 | 2000   LARGE T&R      | Yes         | NIPDAU                               | Level-1-260C-UNLIM                | -40 to 125   | MV824N              |
| <a href="#">LMV824Q1MA/NOPB</a>  | Active        | Production           | SOIC (D)   14    | 55   TUBE             | Yes         | SN                                   | Level-1-260C-UNLIM                | -40 to 125   | LMV824Q1<br>MA      |
| LMV824Q1MA/NOPB.A                | Active        | Production           | SOIC (D)   14    | 55   TUBE             | Yes         | SN                                   | Level-1-260C-UNLIM                | -40 to 125   | LMV824Q1<br>MA      |
| <a href="#">LMV824Q1MAX/NOPB</a> | Active        | Production           | SOIC (D)   14    | 2500   LARGE T&R      | Yes         | SN                                   | Level-1-260C-UNLIM                | -40 to 125   | LMV824Q1<br>MA      |
| LMV824Q1MAX/NOPB.A               | Active        | Production           | SOIC (D)   14    | 2500   LARGE T&R      | Yes         | SN                                   | Level-1-260C-UNLIM                | -40 to 125   | LMV824Q1<br>MA      |
| <a href="#">LMV824Q1MT/NOPB</a>  | Active        | Production           | TSSOP (PW)   14  | 94   TUBE             | Yes         | SN                                   | Level-1-260C-UNLIM                | -40 to 125   | LMV824<br>Q1MT      |
| LMV824Q1MT/NOPB.A                | Active        | Production           | TSSOP (PW)   14  | 94   TUBE             | Yes         | SN                                   | Level-1-260C-UNLIM                | -40 to 125   | LMV824<br>Q1MT      |
| <a href="#">LMV824Q1MTX/NOPB</a> | Active        | Production           | TSSOP (PW)   14  | 2500   LARGE T&R      | Yes         | SN                                   | Level-1-260C-UNLIM                | -40 to 125   | LMV824<br>Q1MT      |
| LMV824Q1MTX/NOPB.A               | Active        | Production           | TSSOP (PW)   14  | 2500   LARGE T&R      | Yes         | SN                                   | Level-1-260C-UNLIM                | -40 to 125   | LMV824<br>Q1MT      |

<sup>(1)</sup> **Status:** For more details on status, see our [product life cycle](#).

<sup>(2)</sup> **Material type:** When designated, preproduction parts are prototypes/experimental devices, and are not yet approved or released for full production. Testing and final process, including without limitation quality assurance, reliability performance testing, and/or process qualification, may not yet be complete, and this item is subject to further changes or possible discontinuation. If available for ordering, purchases will be subject to an additional waiver at checkout, and are intended for early internal evaluation purposes only. These items are sold without warranties of any kind.

<sup>(3)</sup> **RoHS values:** Yes, No, RoHS Exempt. See the [TI RoHS Statement](#) for additional information and value definition.

<sup>(4)</sup> **Lead finish/Ball material:** Parts may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

<sup>(5)</sup> **MSL rating/Peak reflow:** The moisture sensitivity level ratings and peak solder (reflow) temperatures. In the event that a part has multiple moisture sensitivity ratings, only the lowest level per JEDEC standards is shown. Refer to the shipping label for the actual reflow temperature that will be used to mount the part to the printed circuit board.

<sup>(6)</sup> **Part marking:** There may be an additional marking, which relates to the logo, the lot trace code information, or the environmental category of the part.

Multiple part markings will be inside parentheses. Only one part marking contained in parentheses and separated by a "~" will appear on a part. If a line is indented then it is a continuation of the previous line and the two combined represent the entire part marking for that device.

**Important Information and Disclaimer:** The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

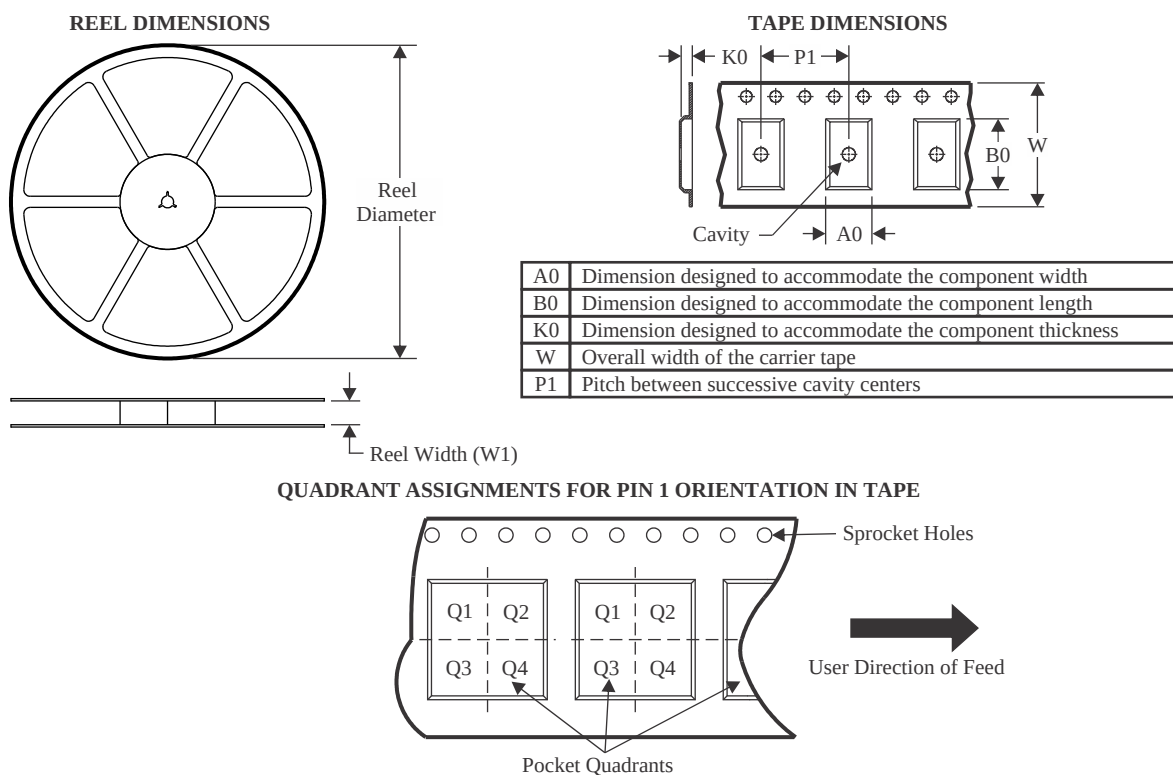
**OTHER QUALIFIED VERSIONS OF LMV822-N, LMV822-N-Q1, LMV824-N, LMV824-N-Q1 :**

- Catalog : [LMV822-N](#), [LMV824-N](#)
- Automotive : [LMV822-N-Q1](#), [LMV824-N-Q1](#)

NOTE: Qualified Version Definitions:

- Catalog - TI's standard catalog product
- Automotive - Q100 devices qualified for high-reliability automotive applications targeting zero defects

## TAPE AND REEL INFORMATION

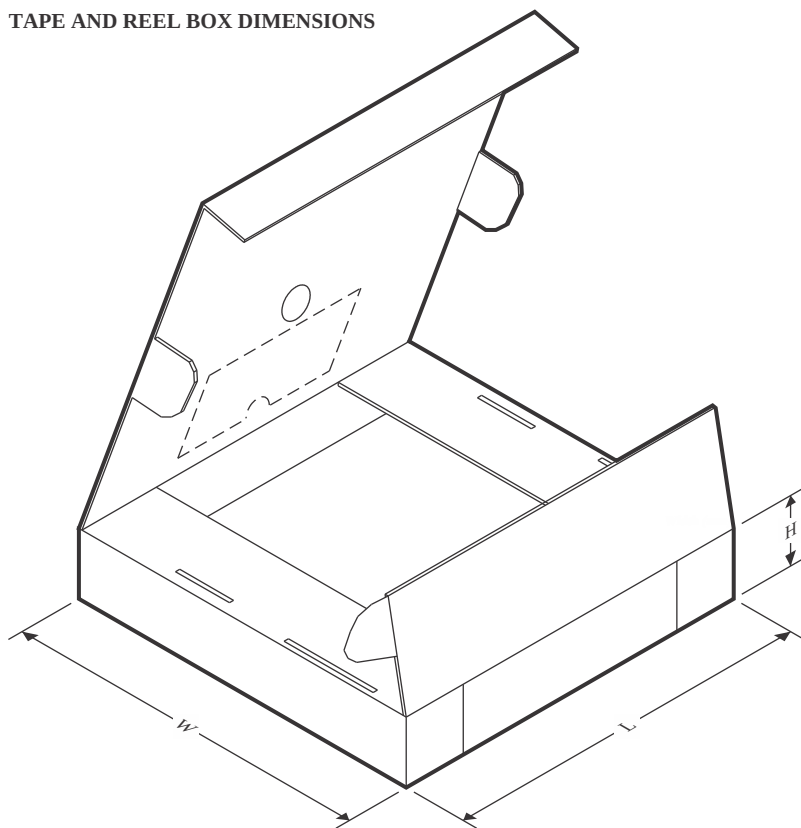


\*All dimensions are nominal

| Device           | Package Type | Package Drawing | Pins | SPQ  | Reel Diameter (mm) | Reel Width W1 (mm) | A0 (mm) | B0 (mm) | K0 (mm) | P1 (mm) | W (mm) | Pin1 Quadrant |
|------------------|--------------|-----------------|------|------|--------------------|--------------------|---------|---------|---------|---------|--------|---------------|
| LMV821M5/NOPB    | SOT-23       | DBV             | 5    | 1000 | 178.0              | 8.4                | 3.2     | 3.2     | 1.4     | 4.0     | 8.0    | Q3            |
| LMV821M5/NOPB    | SOT-23       | DBV             | 5    | 1000 | 180.0              | 8.4                | 3.2     | 3.2     | 1.4     | 4.0     | 8.0    | Q3            |
| LMV821M5X/NOPB   | SOT-23       | DBV             | 5    | 3000 | 180.0              | 8.4                | 3.2     | 3.2     | 1.4     | 4.0     | 8.0    | Q3            |
| LMV821M5X/NOPB   | SOT-23       | DBV             | 5    | 3000 | 178.0              | 8.4                | 3.2     | 3.2     | 1.4     | 4.0     | 8.0    | Q3            |
| LMV821M7/NOPB    | SC70         | DCK             | 5    | 1000 | 178.0              | 8.4                | 2.25    | 2.45    | 1.2     | 4.0     | 8.0    | Q3            |
| LMV821M7X/NOPB   | SC70         | DCK             | 5    | 3000 | 178.0              | 8.4                | 2.25    | 2.45    | 1.2     | 4.0     | 8.0    | Q3            |
| LMV822MM/NOPB    | VSSOP        | DGK             | 8    | 1000 | 177.8              | 12.4               | 5.3     | 3.4     | 1.4     | 8.0     | 12.0   | Q1            |
| LMV822MMX/NOPB   | VSSOP        | DGK             | 8    | 3500 | 330.0              | 12.4               | 5.3     | 3.4     | 1.4     | 8.0     | 12.0   | Q1            |
| LMV822MX/NOPB    | SOIC         | D               | 8    | 2500 | 330.0              | 12.4               | 6.5     | 5.4     | 2.0     | 8.0     | 12.0   | Q1            |
| LMV822Q1MM/NOPB  | VSSOP        | DGK             | 8    | 1000 | 180.0              | 12.4               | 5.3     | 3.4     | 1.4     | 8.0     | 12.0   | Q1            |
| LMV822Q1MM/NOPB  | VSSOP        | DGK             | 8    | 1000 | 177.8              | 12.4               | 5.3     | 3.4     | 1.4     | 8.0     | 12.0   | Q1            |
| LMV822Q1MMX/NOPB | VSSOP        | DGK             | 8    | 3500 | 330.0              | 12.4               | 5.3     | 3.4     | 1.4     | 8.0     | 12.0   | Q1            |
| LMV822Q1MMX/NOPB | VSSOP        | DGK             | 8    | 3500 | 330.0              | 12.4               | 5.3     | 3.4     | 1.4     | 8.0     | 12.0   | Q1            |
| LMV824MTX/NOPB   | TSSOP        | PW              | 14   | 2500 | 330.0              | 12.4               | 6.9     | 5.6     | 1.6     | 8.0     | 12.0   | Q1            |
| LMV824MTX/NOPB   | TSSOP        | PW              | 14   | 2500 | 330.0              | 12.4               | 6.9     | 5.6     | 1.6     | 8.0     | 12.0   | Q1            |
| LMV824MX/NOPB    | SOIC         | D               | 14   | 2500 | 330.0              | 16.4               | 6.5     | 9.35    | 2.3     | 8.0     | 16.0   | Q1            |

| Device           | Package Type | Package Drawing | Pins | SPQ  | Reel Diameter (mm) | Reel Width W1 (mm) | A0 (mm) | B0 (mm) | K0 (mm) | P1 (mm) | W (mm) | Pin1 Quadrant |
|------------------|--------------|-----------------|------|------|--------------------|--------------------|---------|---------|---------|---------|--------|---------------|
| LMV824NDGVR      | TVSOP        | DGV             | 14   | 2000 | 330.0              | 12.4               | 6.8     | 4.0     | 1.6     | 8.0     | 12.0   | Q1            |
| LMV824Q1MAX/NOPB | SOIC         | D               | 14   | 2500 | 330.0              | 16.4               | 6.5     | 9.35    | 2.3     | 8.0     | 16.0   | Q1            |
| LMV824Q1MTX/NOPB | TSSOP        | PW              | 14   | 2500 | 330.0              | 12.4               | 6.9     | 5.6     | 1.6     | 8.0     | 12.0   | Q1            |

## TAPE AND REEL BOX DIMENSIONS



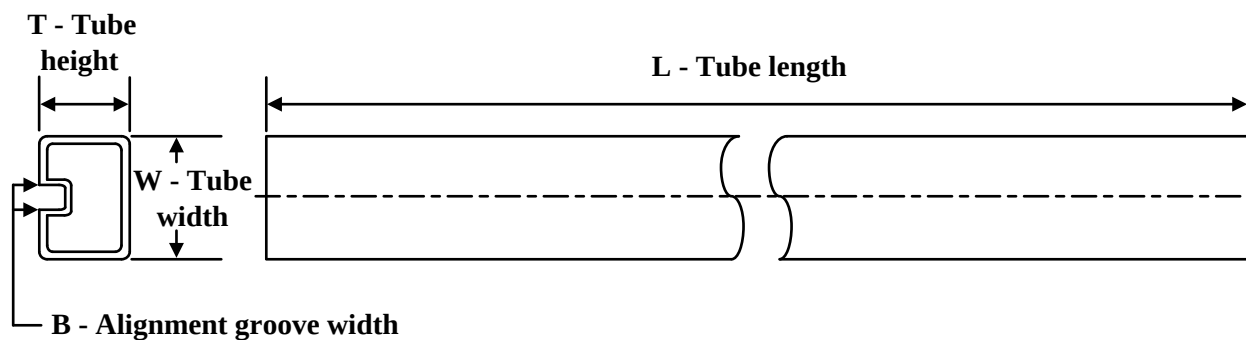
\*All dimensions are nominal

| Device           | Package Type | Package Drawing | Pins | SPQ  | Length (mm) | Width (mm) | Height (mm) |
|------------------|--------------|-----------------|------|------|-------------|------------|-------------|
| LMV821M5/NOPB    | SOT-23       | DBV             | 5    | 1000 | 208.0       | 191.0      | 35.0        |
| LMV821M5/NOPB    | SOT-23       | DBV             | 5    | 1000 | 210.0       | 185.0      | 35.0        |
| LMV821M5X/NOPB   | SOT-23       | DBV             | 5    | 3000 | 210.0       | 185.0      | 35.0        |
| LMV821M5X/NOPB   | SOT-23       | DBV             | 5    | 3000 | 208.0       | 191.0      | 35.0        |
| LMV821M7/NOPB    | SC70         | DCK             | 5    | 1000 | 208.0       | 191.0      | 35.0        |
| LMV821M7X/NOPB   | SC70         | DCK             | 5    | 3000 | 208.0       | 191.0      | 35.0        |
| LMV822MM/NOPB    | VSSOP        | DGK             | 8    | 1000 | 208.0       | 191.0      | 35.0        |
| LMV822MMX/NOPB   | VSSOP        | DGK             | 8    | 3500 | 367.0       | 367.0      | 35.0        |
| LMV822MX/NOPB    | SOIC         | D               | 8    | 2500 | 367.0       | 367.0      | 35.0        |
| LMV822Q1MM/NOPB  | VSSOP        | DGK             | 8    | 1000 | 213.0       | 191.0      | 35.0        |
| LMV822Q1MM/NOPB  | VSSOP        | DGK             | 8    | 1000 | 208.0       | 191.0      | 35.0        |
| LMV822Q1MMX/NOPB | VSSOP        | DGK             | 8    | 3500 | 353.0       | 353.0      | 32.0        |
| LMV822Q1MMX/NOPB | VSSOP        | DGK             | 8    | 3500 | 367.0       | 367.0      | 35.0        |
| LMV824MTX/NOPB   | TSSOP        | PW              | 14   | 2500 | 353.0       | 353.0      | 32.0        |
| LMV824MTX/NOPB   | TSSOP        | PW              | 14   | 2500 | 367.0       | 367.0      | 35.0        |
| LMV824MX/NOPB    | SOIC         | D               | 14   | 2500 | 356.0       | 356.0      | 35.0        |
| LMV824NDGVR      | TVSOP        | DGV             | 14   | 2000 | 353.0       | 353.0      | 32.0        |
| LMV824Q1MAX/NOPB | SOIC         | D               | 14   | 2500 | 367.0       | 367.0      | 35.0        |

---

| Device           | Package Type | Package Drawing | Pins | SPQ  | Length (mm) | Width (mm) | Height (mm) |
|------------------|--------------|-----------------|------|------|-------------|------------|-------------|
| LMV824Q1MTX/NOPB | TSSOP        | PW              | 14   | 2500 | 367.0       | 367.0      | 35.0        |

## TUBE

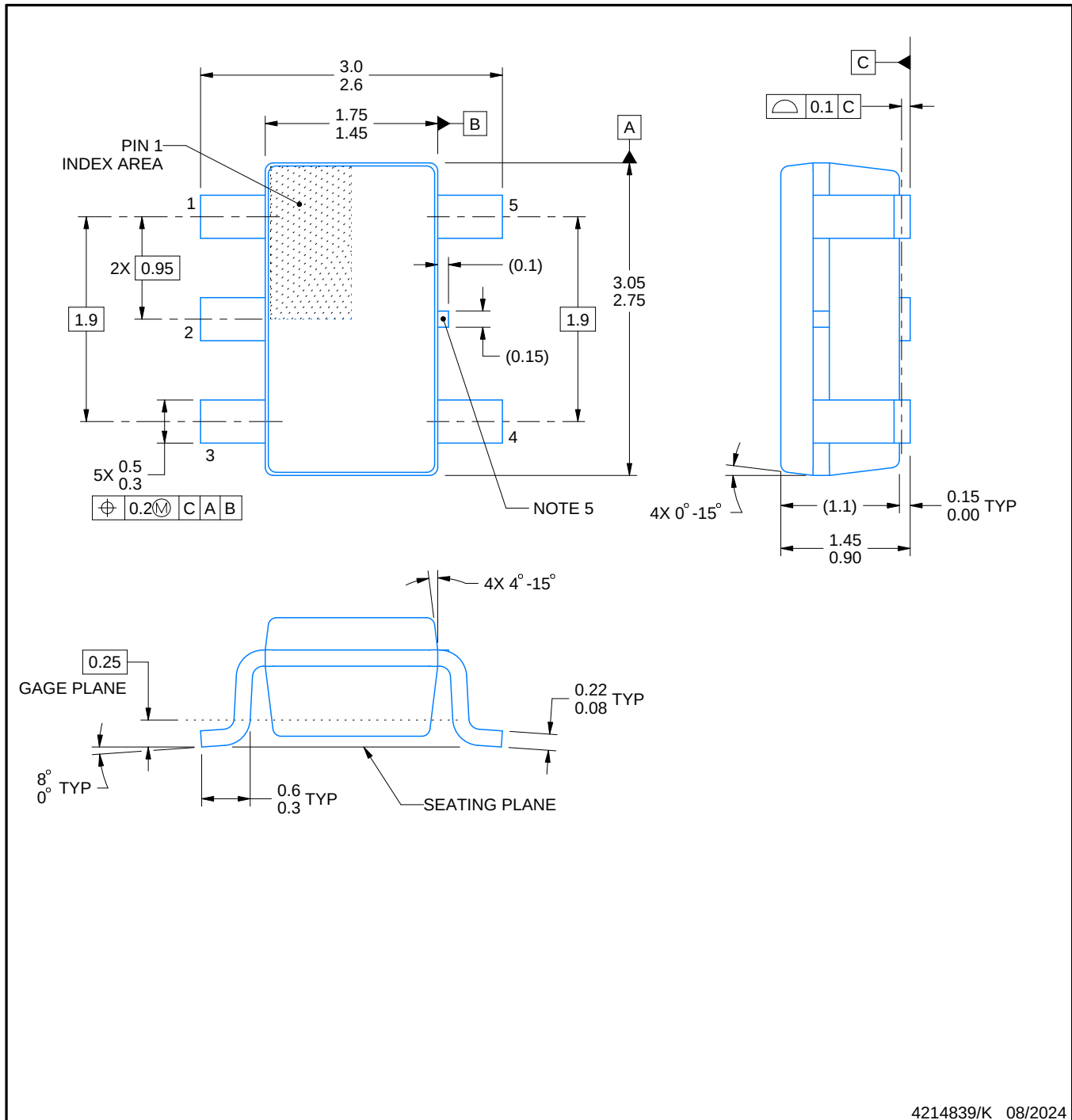


\*All dimensions are nominal

| Device            | Package Name | Package Type | Pins | SPQ | L (mm) | W (mm) | T (μm) | B (mm) |
|-------------------|--------------|--------------|------|-----|--------|--------|--------|--------|
| LMV822M/NOPB      | D            | SOIC         | 8    | 95  | 495    | 8      | 4064   | 3.05   |
| LMV822M/NOPB.A    | D            | SOIC         | 8    | 95  | 495    | 8      | 4064   | 3.05   |
| LMV824M/NOPB      | D            | SOIC         | 14   | 55  | 495    | 8      | 4064   | 3.05   |
| LMV824M/NOPB.A    | D            | SOIC         | 14   | 55  | 495    | 8      | 4064   | 3.05   |
| LMV824MT/NOPB     | PW           | TSSOP        | 14   | 94  | 495    | 8      | 2514.6 | 4.06   |
| LMV824MT/NOPB     | PW           | TSSOP        | 14   | 94  | 530    | 10.2   | 3600   | 3.5    |
| LMV824MT/NOPB.A   | PW           | TSSOP        | 14   | 94  | 495    | 8      | 2514.6 | 4.06   |
| LMV824MT/NOPB.A   | PW           | TSSOP        | 14   | 94  | 530    | 10.2   | 3600   | 3.5    |
| LMV824Q1MA/NOPB   | D            | SOIC         | 14   | 55  | 495    | 8      | 4064   | 3.05   |
| LMV824Q1MA/NOPB.A | D            | SOIC         | 14   | 55  | 495    | 8      | 4064   | 3.05   |
| LMV824Q1MT/NOPB   | PW           | TSSOP        | 14   | 94  | 495    | 8      | 2514.6 | 4.06   |
| LMV824Q1MT/NOPB.A | PW           | TSSOP        | 14   | 94  | 495    | 8      | 2514.6 | 4.06   |

**DBV0005A****PACKAGE OUTLINE****SOT-23 - 1.45 mm max height**

SMALL OUTLINE TRANSISTOR



4214839/K 08/2024

**NOTES:**

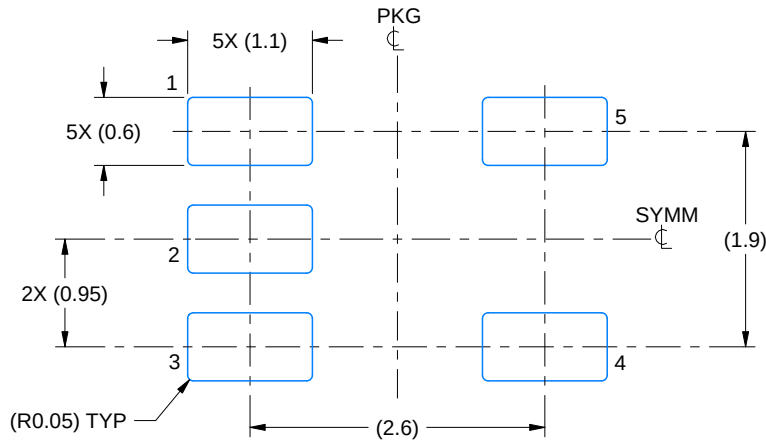
1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. Reference JEDEC MO-178.
4. Body dimensions do not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.25 mm per side.
5. Support pin may differ or may not be present.

# EXAMPLE BOARD LAYOUT

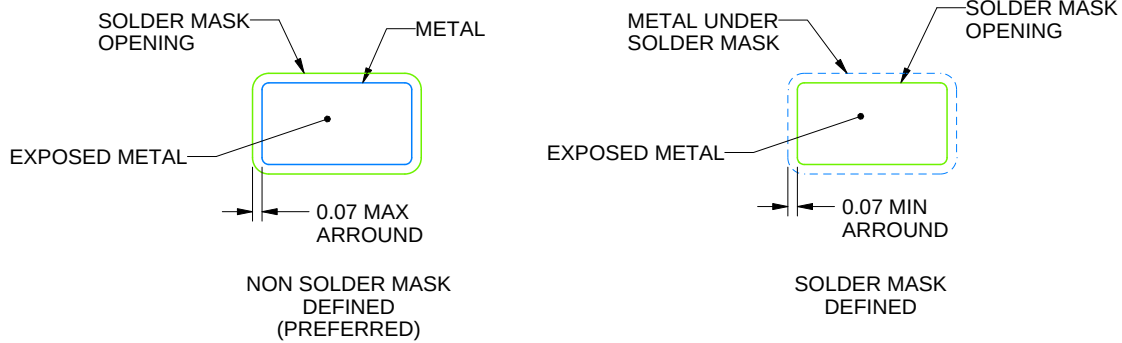
DBV0005A

SOT-23 - 1.45 mm max height

SMALL OUTLINE TRANSISTOR



LAND PATTERN EXAMPLE  
EXPOSED METAL SHOWN  
SCALE:15X



SOLDER MASK DETAILS

4214839/K 08/2024

NOTES: (continued)

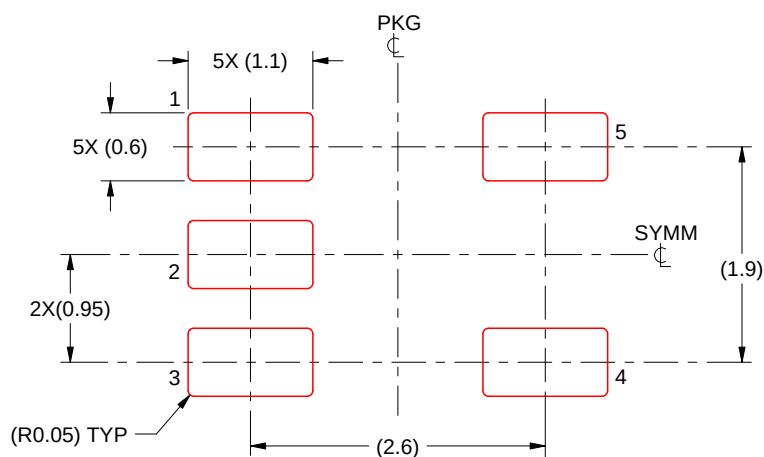
6. Publication IPC-7351 may have alternate designs.
7. Solder mask tolerances between and around signal pads can vary based on board fabrication site.

## EXAMPLE STENCIL DESIGN

DBV0005A

SOT-23 - 1.45 mm max height

SMALL OUTLINE TRANSISTOR



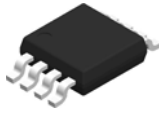
SOLDER PASTE EXAMPLE  
BASED ON 0.125 mm THICK STENCIL  
SCALE:15X

4214839/K 08/2024

NOTES: (continued)

8. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
9. Board assembly site may have different recommendations for stencil design.

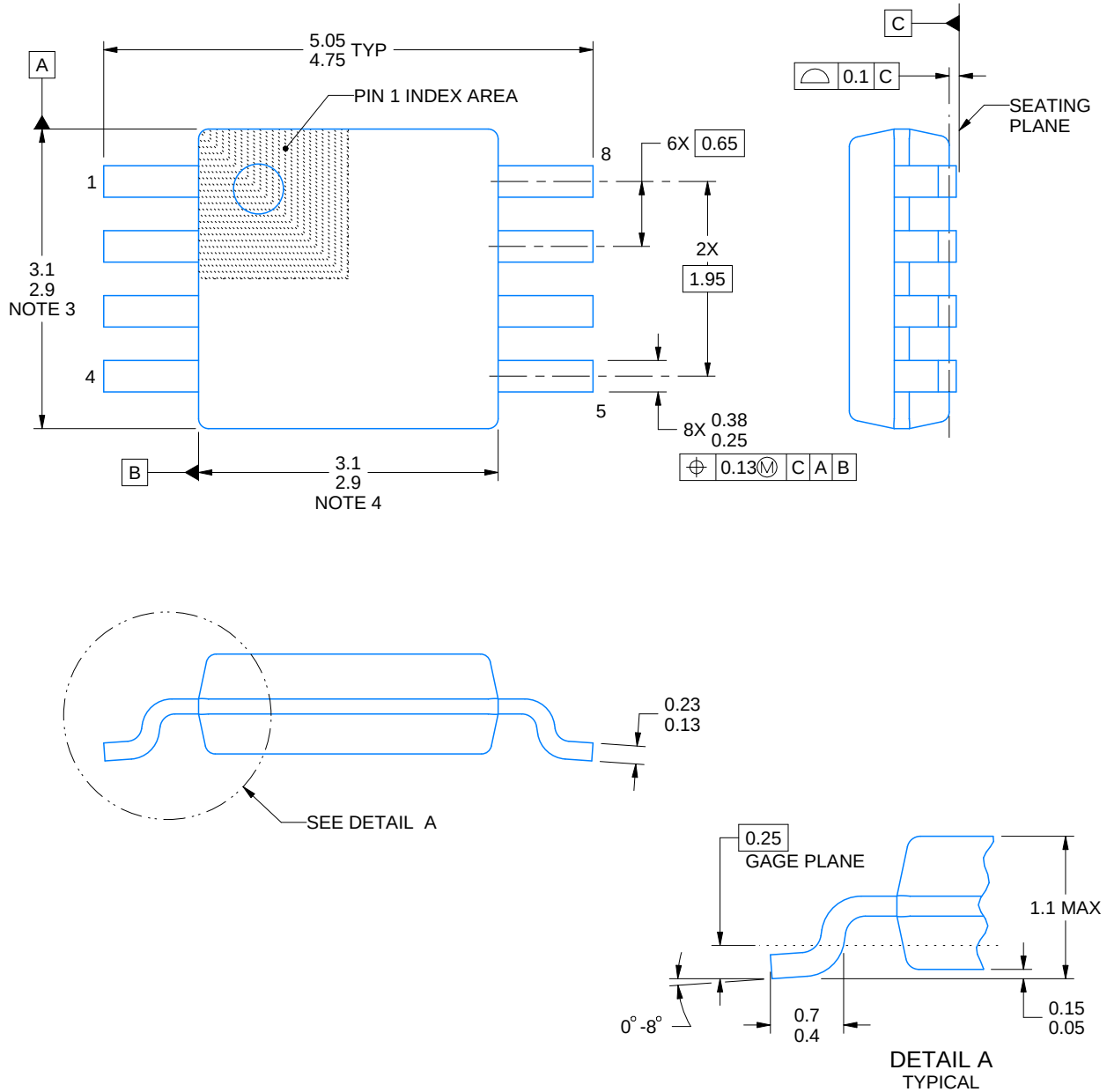
DGK0008A



# PACKAGE OUTLINE

## VSSOP - 1.1 mm max height

SMALL OUTLINE PACKAGE



4214862/A 04/2023

### NOTES:

PowerPAD is a trademark of Texas Instruments.

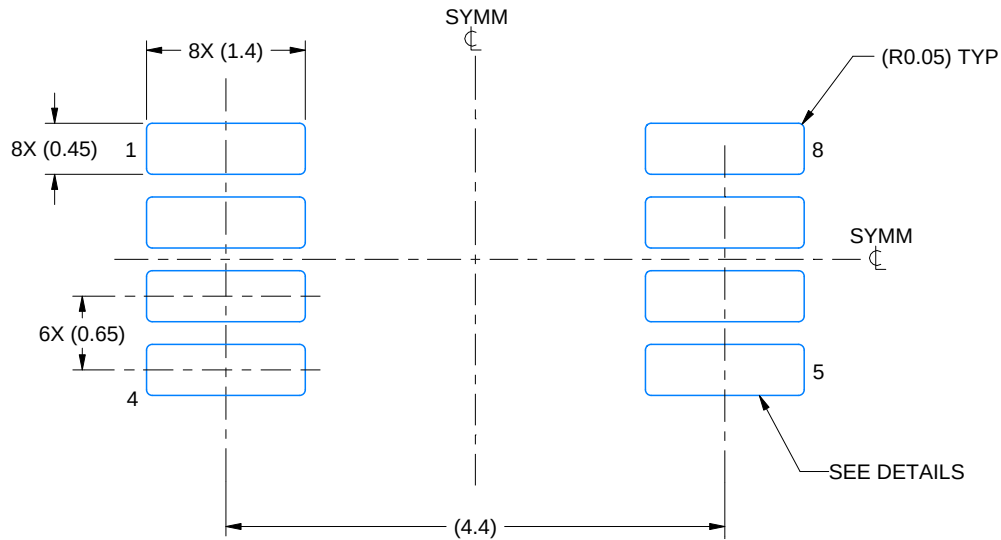
1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. This dimension does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.15 mm per side.
4. This dimension does not include interlead flash. Interlead flash shall not exceed 0.25 mm per side.
5. Reference JEDEC registration MO-187.

# EXAMPLE BOARD LAYOUT

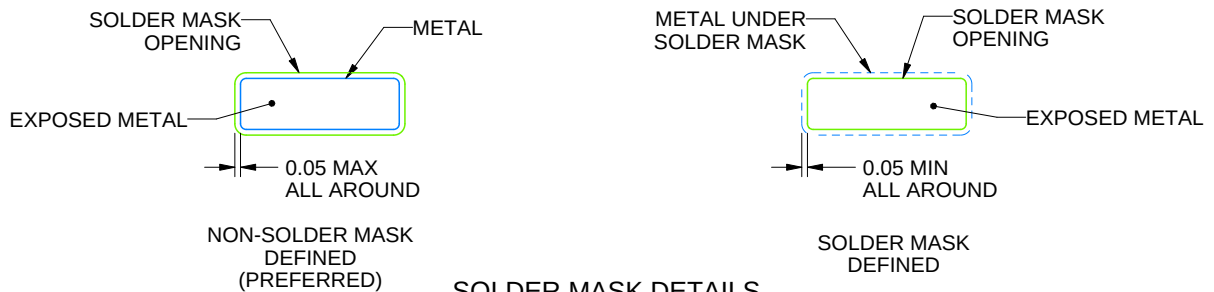
DGK0008A

<sup>TM</sup> VSSOP - 1.1 mm max height

SMALL OUTLINE PACKAGE



LAND PATTERN EXAMPLE  
EXPOSED METAL SHOWN  
SCALE: 15X



SOLDER MASK DETAILS

4214862/A 04/2023

NOTES: (continued)

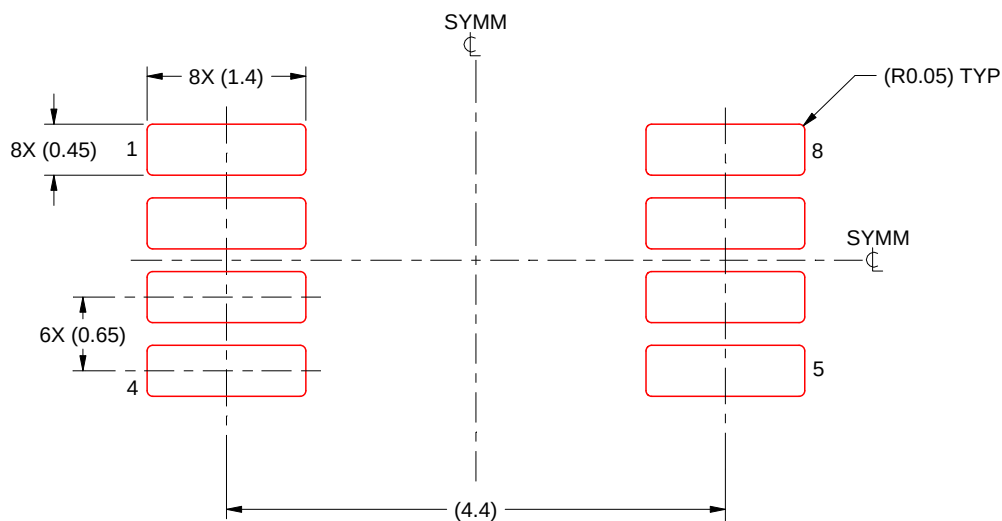
6. Publication IPC-7351 may have alternate designs.
7. Solder mask tolerances between and around signal pads can vary based on board fabrication site.
8. Vias are optional depending on application, refer to device data sheet. If any vias are implemented, refer to their locations shown on this view. It is recommended that vias under paste be filled, plugged or tented.
9. Size of metal pad may vary due to creepage requirement.

## EXAMPLE STENCIL DESIGN

DGK0008A

<sup>TM</sup> VSSOP - 1.1 mm max height

SMALL OUTLINE PACKAGE

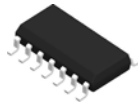


SOLDER PASTE EXAMPLE  
SCALE: 15X

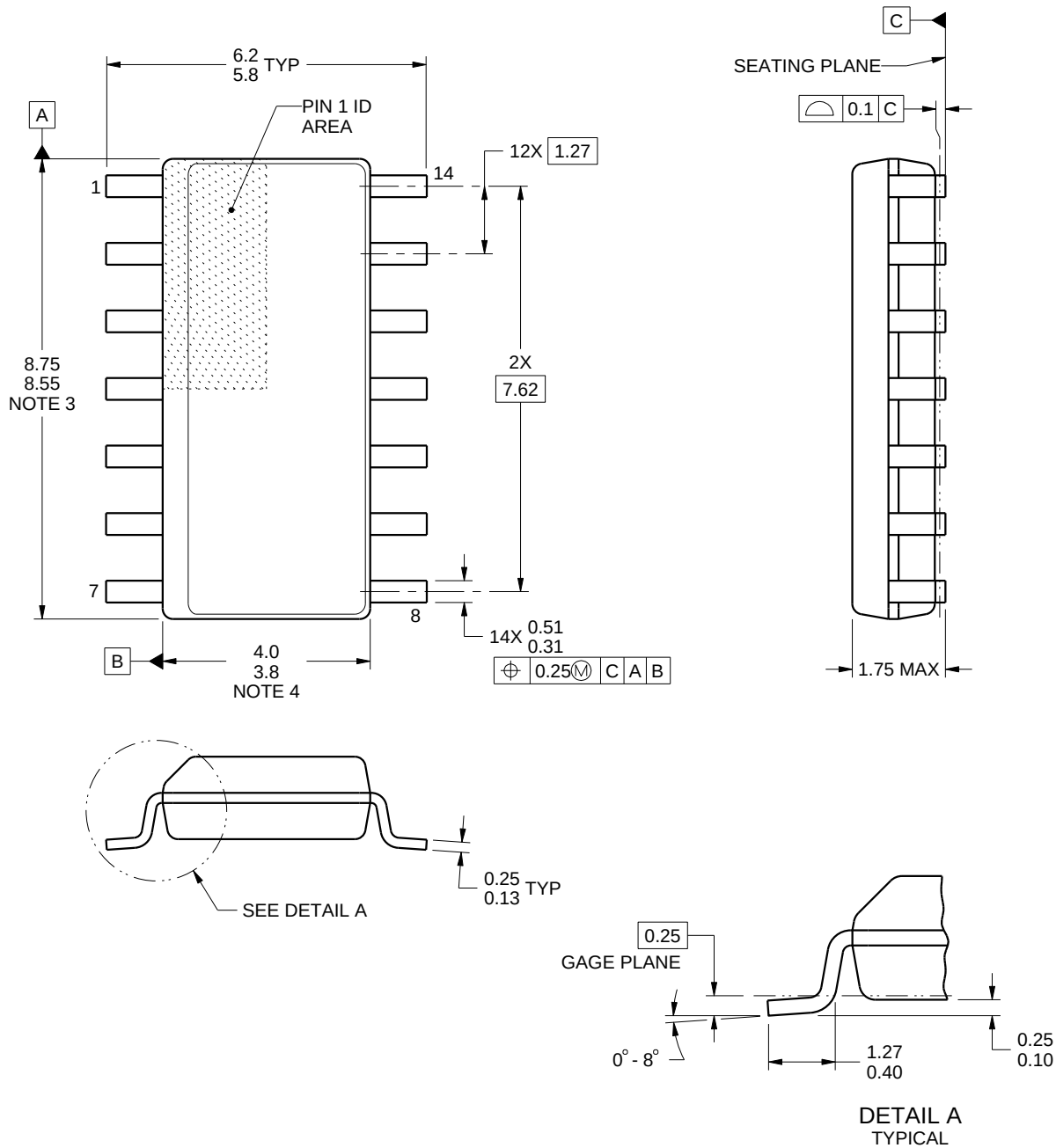
4214862/A 04/2023

NOTES: (continued)

11. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
12. Board assembly site may have different recommendations for stencil design.

**D0014A****PACKAGE OUTLINE****SOIC - 1.75 mm max height**

SMALL OUTLINE INTEGRATED CIRCUIT



4220718/A 09/2016

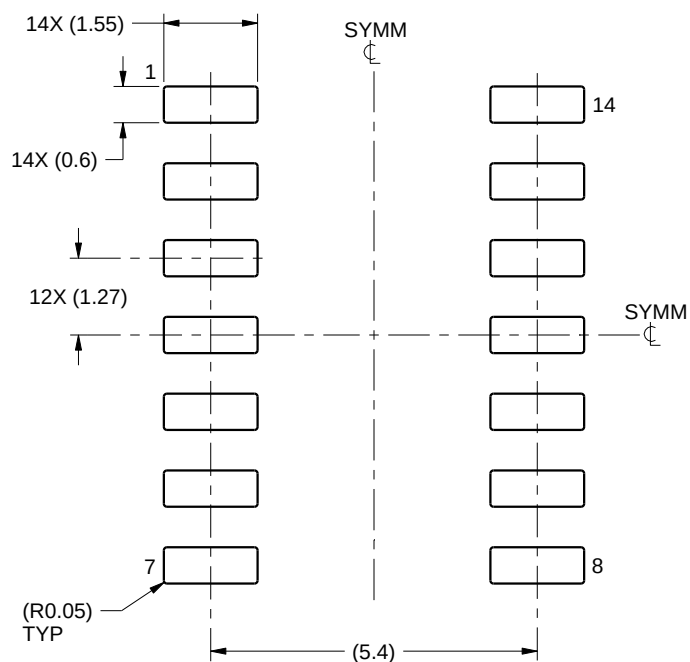
**NOTES:**

1. All linear dimensions are in millimeters. Dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. This dimension does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.15 mm, per side.
4. This dimension does not include interlead flash. Interlead flash shall not exceed 0.43 mm, per side.
5. Reference JEDEC registration MS-012, variation AB.

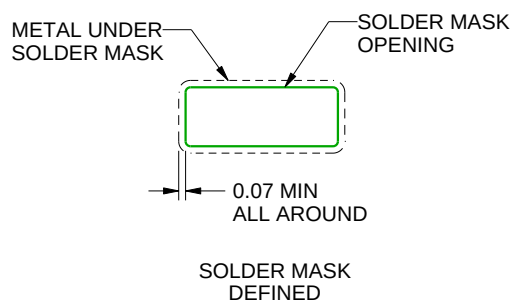
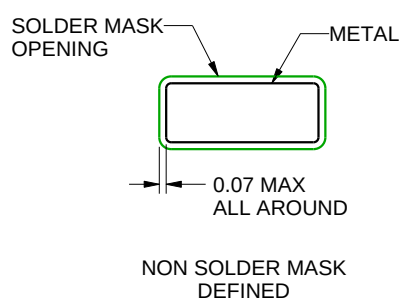
**D0014A**

**SOIC - 1.75 mm max height**

SMALL OUTLINE INTEGRATED CIRCUIT



LAND PATTERN EXAMPLE  
SCALE:8X



## SOLDER MASK DETAILS

4220718/A 09/2016

NOTES: (continued)

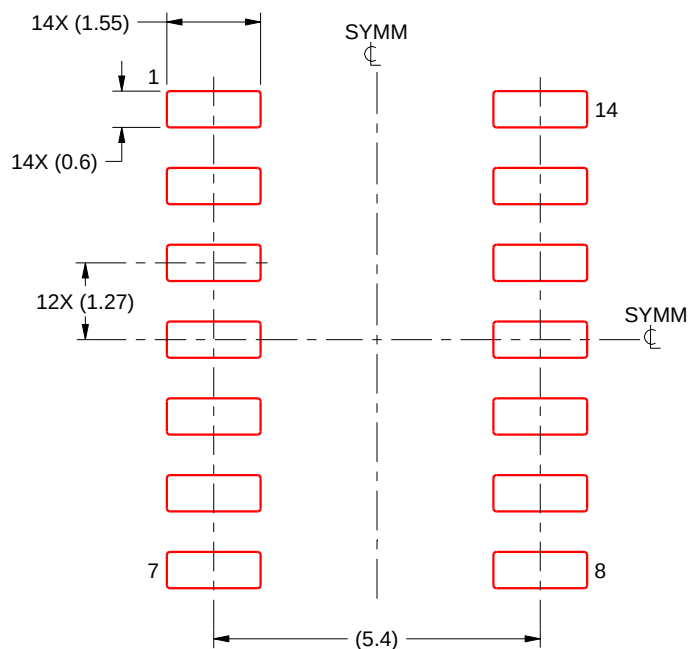
6. Publication IPC-7351 may have alternate designs.  
7. Solder mask tolerances between and around signal pads can vary based on board fabrication site.

## EXAMPLE STENCIL DESIGN

D0014A

SOIC - 1.75 mm max height

SMALL OUTLINE INTEGRATED CIRCUIT

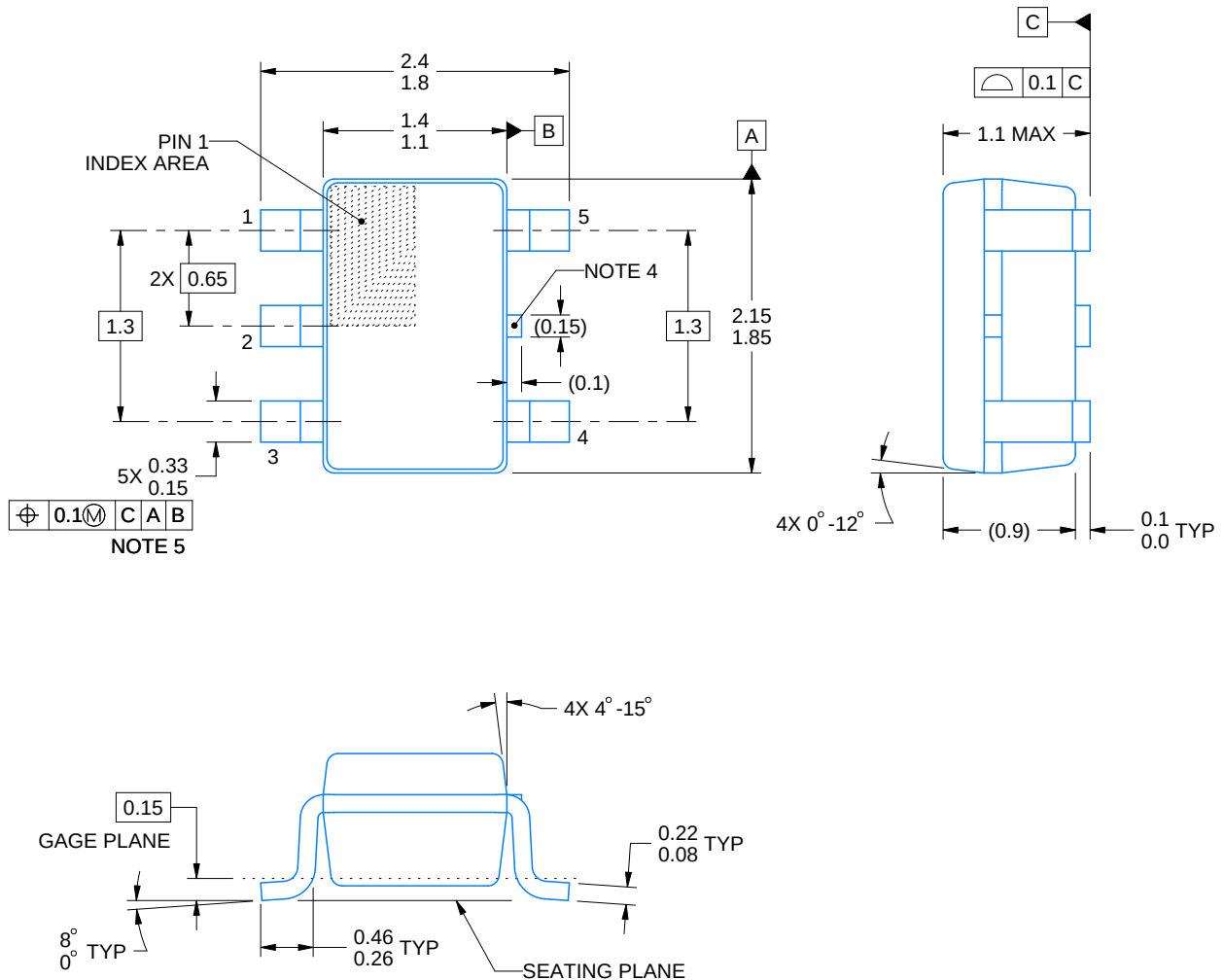


SOLDER PASTE EXAMPLE  
BASED ON 0.125 mm THICK STENCIL  
SCALE:8X

4220718/A 09/2016

NOTES: (continued)

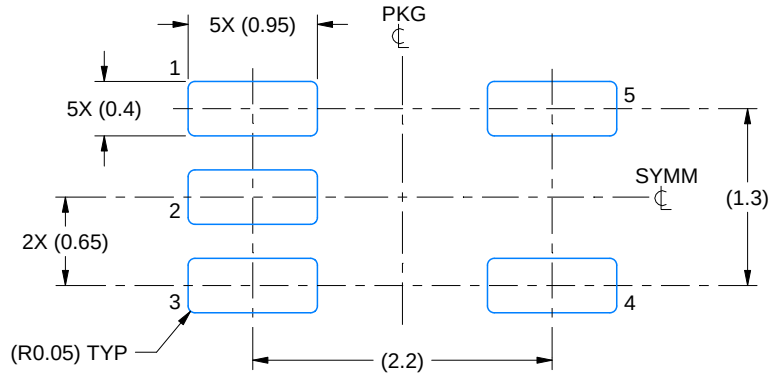
8. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
9. Board assembly site may have different recommendations for stencil design.



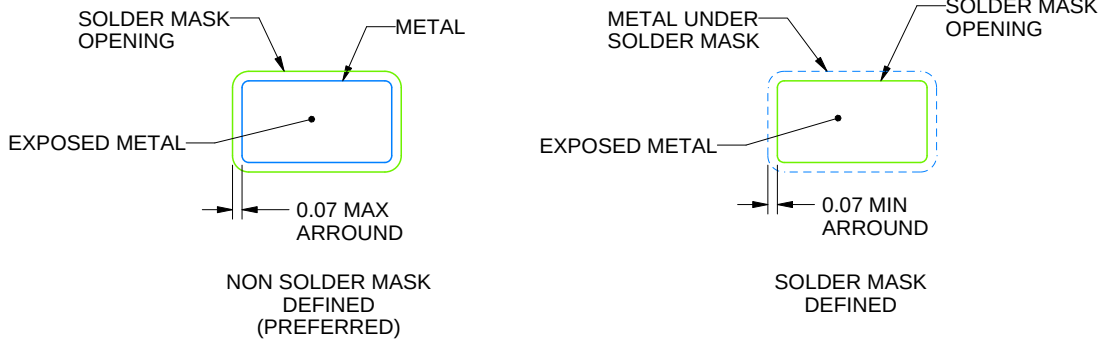
4214834/G 11/2024

## NOTES:

1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. Reference JEDEC MO-203.
4. Support pin may differ or may not be present.
5. Lead width does not comply with JEDEC.
6. Body dimensions do not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.25mm per side



LAND PATTERN EXAMPLE  
EXPOSED METAL SHOWN  
SCALE:18X



SOLDER MASK DETAILS

4214834/G 11/2024

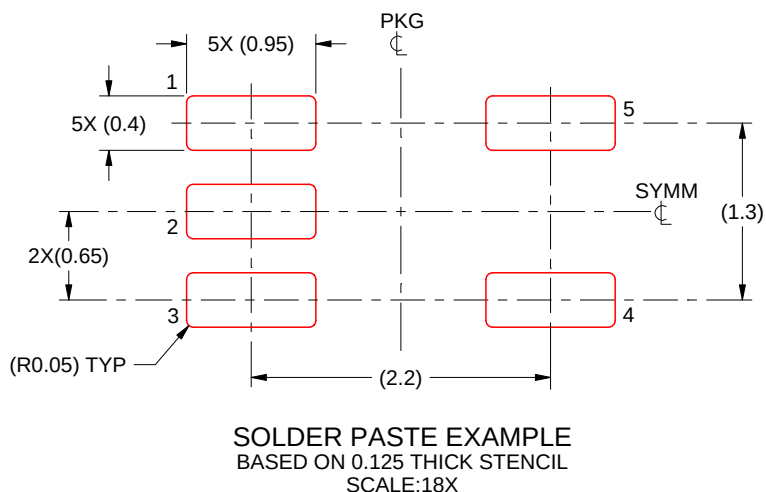
NOTES: (continued)

7. Publication IPC-7351 may have alternate designs.
8. Solder mask tolerances between and around signal pads can vary based on board fabrication site.

**DCK0005A**

## SOT - 1.1 max height

## SMALL OUTLINE TRANSISTOR



4214834/G 11/2024

NOTES: (continued)

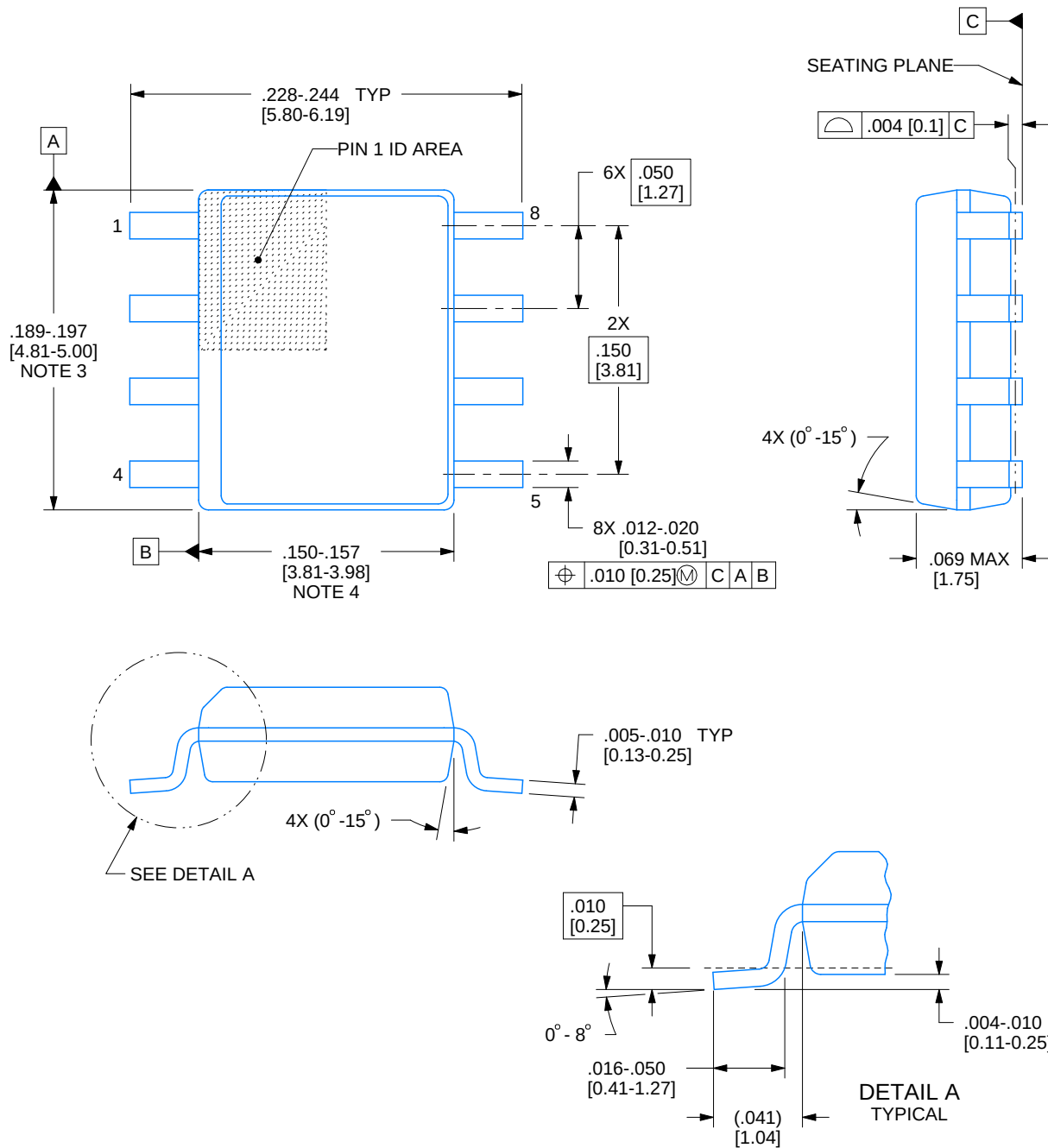
9. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
10. Board assembly site may have different recommendations for stencil design.

**D0008A**

## PACKAGE OUTLINE

**SOIC - 1.75 mm max height**

SMALL OUTLINE INTEGRATED CIRCUIT



4214825/C 02/2019

NOTES:

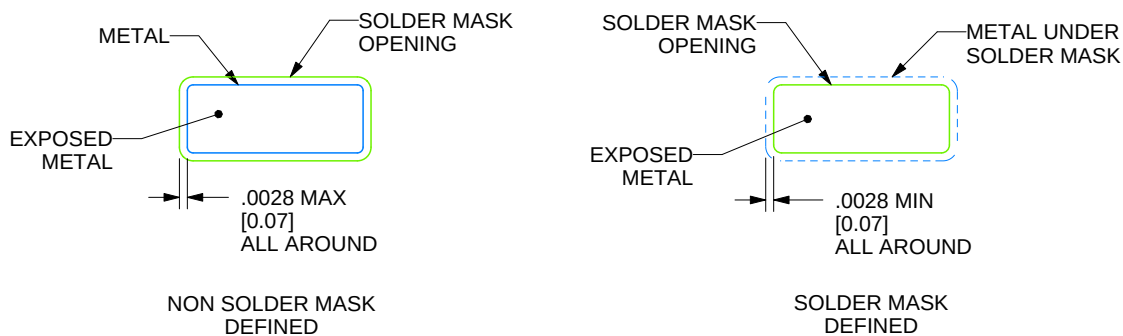
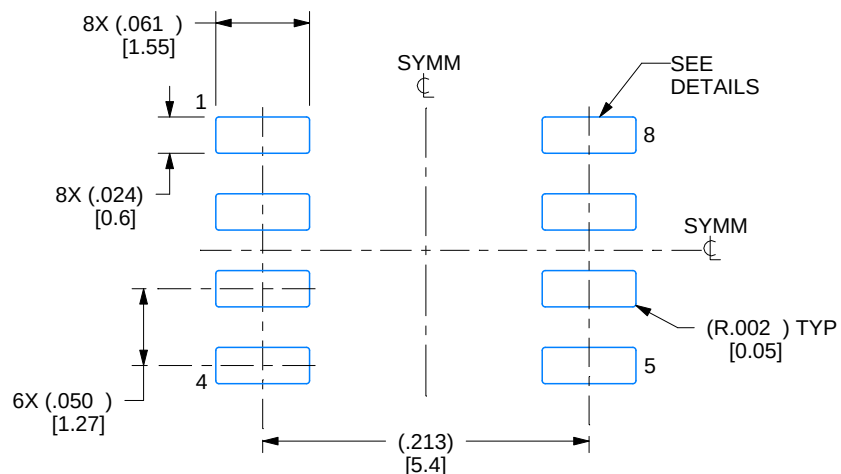
1. Linear dimensions are in inches [millimeters]. Dimensions in parenthesis are for reference only. Controlling dimensions are in inches. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. This dimension does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed .006 [0.15] per side.
4. This dimension does not include interlead flash.
5. Reference JEDEC registration MS-012, variation AA.

# EXAMPLE BOARD LAYOUT

D0008A

SOIC - 1.75 mm max height

SMALL OUTLINE INTEGRATED CIRCUIT



4214825/C 02/2019

NOTES: (continued)

6. Publication IPC-7351 may have alternate designs.

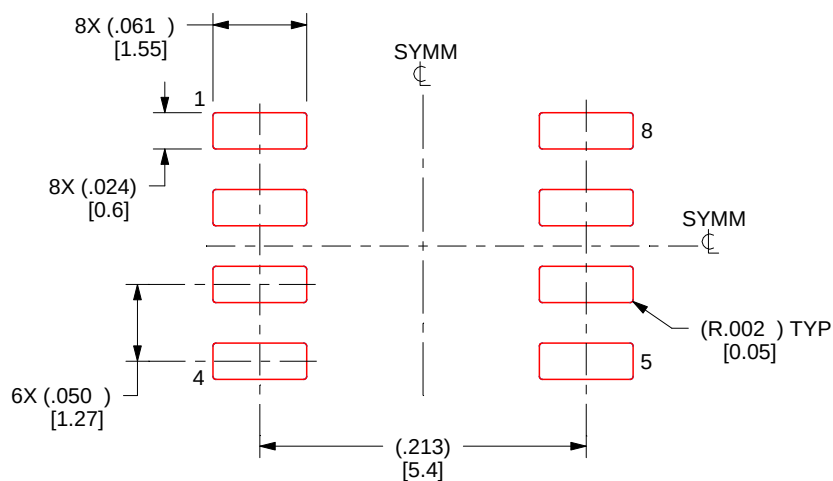
7. Solder mask tolerances between and around signal pads can vary based on board fabrication site.

## EXAMPLE STENCIL DESIGN

D0008A

SOIC - 1.75 mm max height

SMALL OUTLINE INTEGRATED CIRCUIT



SOLDER PASTE EXAMPLE  
BASED ON .005 INCH [0.125 MM] THICK STENCIL  
SCALE:8X

4214825/C 02/2019

NOTES: (continued)

8. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
9. Board assembly site may have different recommendations for stencil design.

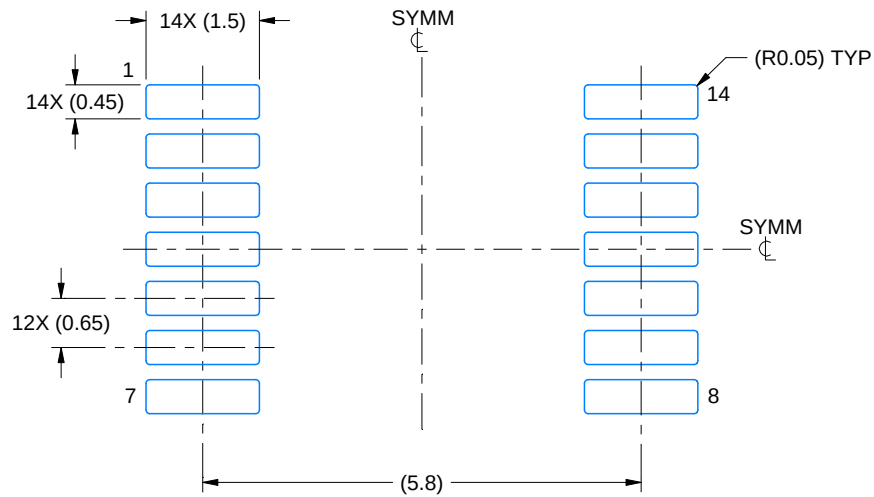


# EXAMPLE BOARD LAYOUT

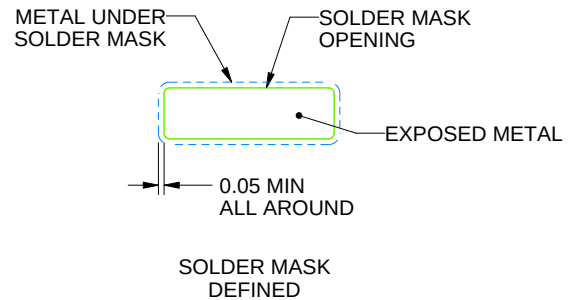
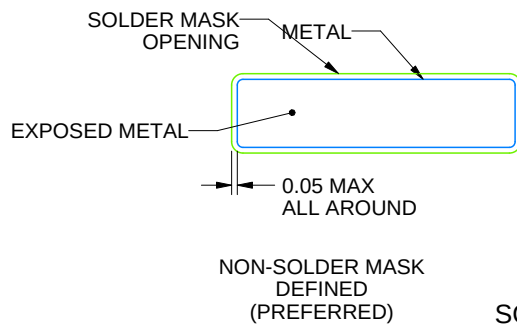
PW0014A

TSSOP - 1.2 mm max height

SMALL OUTLINE PACKAGE



LAND PATTERN EXAMPLE  
EXPOSED METAL SHOWN  
SCALE: 10X



SOLDER MASK DETAILS

4220202/B 12/2023

NOTES: (continued)

6. Publication IPC-7351 may have alternate designs.

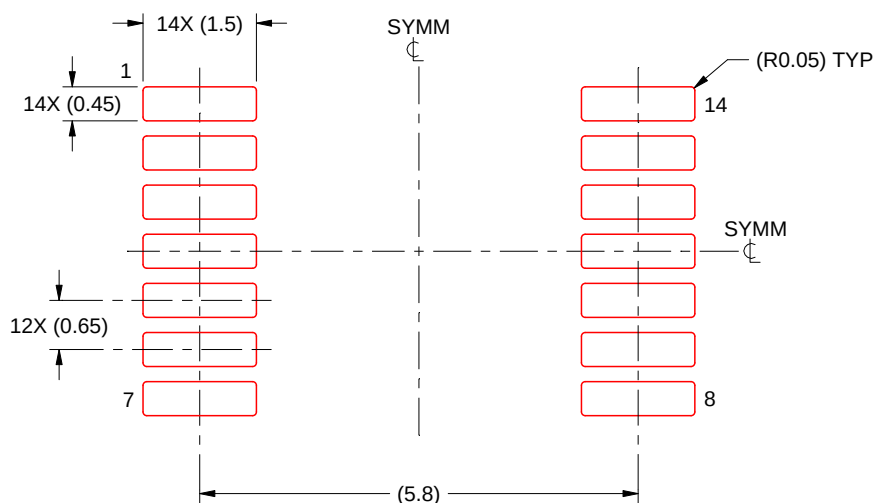
7. Solder mask tolerances between and around signal pads can vary based on board fabrication site.

# EXAMPLE STENCIL DESIGN

PW0014A

TSSOP - 1.2 mm max height

SMALL OUTLINE PACKAGE



SOLDER PASTE EXAMPLE  
BASED ON 0.125 mm THICK STENCIL  
SCALE: 10X

4220202/B 12/2023

NOTES: (continued)

8. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
9. Board assembly site may have different recommendations for stencil design.

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最終更新日：2025 年 10 月