

TVS2700 27Vフラットクランプ・サージ保護デバイス

1 特長

- 産業用信号ライン向け $\pm 1\text{kV}$ 、 42Ω のIEC 61000-4-5サージ・テストに耐える保護機能
- 最大クランプ電圧: サージ電流(8/20 μs) 40Aで35V
- スタンドオフ電圧: 27V
- 4mm²の小さい占有面積
- 125°Cで35Aのサージ電流(8/20 μs)の反復ストライクを5,000回吸収
- 強力なサージ保護
 - IEC61000-4-5 (8/20 μs): 40A
 - IEC61643-321 (10/1000 μs): 4A
- 低リーク電流
 - 27°Cで1.8nA (標準値)
 - 85°Cで14.5nA (標準値)
- 低容量: 100pF
- レベル4 IEC 61000-4-2に準拠したESD保護機能を内蔵

2 アプリケーション

- 産業用センサI/O
- 医療用機器
- PLC I/Oモジュール
- 家電製品
- フィールド・トランスミッタ

3 概要

TVS2700は、最大40AのIEC 61000-4-5フォルト電流を確実にシャントして、システムを高電力過渡事象や落雷から保護します。一般的な産業用信号ラインのEMC要件向けのソリューションとして、 42Ω のインピーダンスにより結合される、最大 $\pm 1\text{kV}$ のIEC 61000-4-5開路電圧に耐えられます。TVS2700は、独自の帰還メカニズムの採用により、フォルト時に高精度のフラット・クランプングを実現し、システムがさらされる電圧を35V未満に抑えます。電圧レギュレーションが正確であるため、許容電圧の低いシステム部品を安心して選択でき、堅牢性を犠牲にすることなくシステムのコストと複雑さを抑えることができます。

また、TVS2700は占有面積が小さい2mm \times 2mmのSONパッケージで供給されるため、スペースの制約があるアプリケーションに最適であり、業界標準のSMA/SMBパッケージに比べて占有面積を70%削減できます。リーク電流と容量が極めて低いことから、保護するラインへの影響も最小限に抑えられます。製品のライフサイクル全体にわたる堅牢な保護を保証するために、TIはTVS2700に対し高温で5,000回の反復サージが発生するテストを実施し、性能に変化がないことを確認しています。

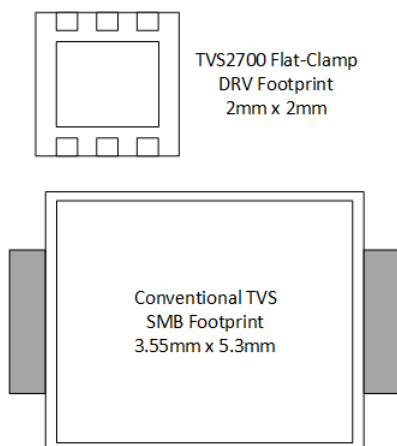
TVS2700はTIのフラットクランプ・サージ・デバイス・ファミリの製品です。このファミリに含まれる他のデバイスの詳細については、「[デバイス比較表](#)」を参照してください。

製品情報⁽¹⁾

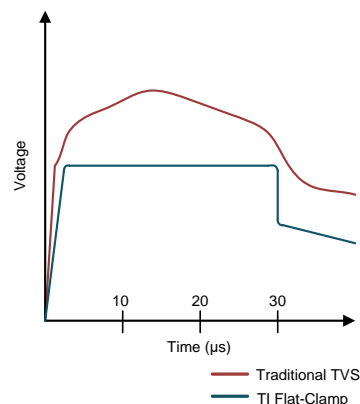
型番	パッケージ	本体サイズ(公称)
TVS2700	SON (6)	2.00mm \times 2.00mm

(1) 利用可能なすべてのパッケージについては、このデータシートの末尾にある注文情報を参照してください。

占有面積の比較



8/20 μs のサージ・イベントに対する電圧クランプの応答



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4 改訂履歴

2017年12月発行のものから更新

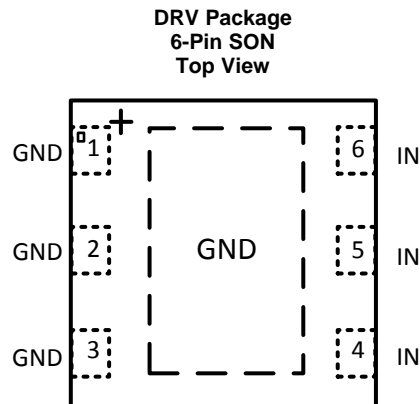
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•	製品のステータスを「事前情報」から「量産データ」に変更	1
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5 Device Comparison Table

Device	V_{rwm}	V_{clamp} at I_{pp}	I_{pp} (8/20 μ s)	V_{rwm} leakage (nA)	Package Options	Polarity
TVS0500	5	9.2	43	0.07	SON	Unidirectional
TVS1400	14	18.4	43	2	SON	Unidirectional
TVS1800	18	22.8	40	0.5	SON	Unidirectional
TVS2200	22	27.7	40	3.2	SON	Unidirectional
TVS2700	27	32.5	40	1.7	SON	Unidirectional
TVS3300	33	38	35	19	WCSP, SON	Unidirectional

6 Pin Configuration and Functions



Pin Functions

PIN		TYPE	DESCRIPTION
NAME	No.		
IN	4, 5, 6	I	ESD and surge protected channel
GND	1, 2, 3, exposed thermal pad	GND	Ground

7 Specifications

7.1 Absolute Maximum Ratings

 $T_A = 27^\circ\text{C}$ (unless otherwise noted)⁽¹⁾

		MIN	MAX	UNIT
Maximum Surge	IEC 61000-4-5 Current (8/20 μs)		40	A
	IEC 61000-4-5 Power (8/20 μs)		1250	W
	IEC 61643-321 Current (10/1000 μs)		4	A
	IEC 61643-321 Power (10/1000 μs)		120	W
Maximum Forward Surge	IEC 61000-4-5 Current (8/20 μs)		50	A
	IEC 61000-4-5 Power (8/20 μs)		80	W
	IEC 61643-321 Current (10/1000 μs)		23	A
	IEC 61643-321 Power (10/1000 μs)		60	W
EFT	IEC 61000-4-4 EFT Protection		80	A
I_{BR}	DC Breakdown current		12	mA
I_F	DC Forward Current		500	mA
T_A	Ambient Operating Temperature	-40	125	$^\circ\text{C}$
T_{stg}	Storage Temperature	-65	150	$^\circ\text{C}$

(1) Stresses beyond those listed under *Absolute Maximum Rating* may cause permanent damage to the device. These are stress ratings only, which do not imply functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Condition*. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

7.2 ESD Ratings - JEDEC

		VALUE	UNIT
$V_{(ESD)}$	Electrostatic discharge	Human body model (HBM), per ANSI/ESDA/JEDEC JS-001, all pins ⁽¹⁾	± 2000
		Charged device model (CDM), per JEDEC specification JESD22-C101, all pins ⁽²⁾	± 500

(1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.

(2) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

7.3 ESD Ratings - IEC

		VALUE	UNIT
$V_{(ESD)}$	Electrostatic discharge	IEC 61000-4-2 contact discharge	± 16
		IEC 61000-4-2 air-gap discharge	± 30

7.4 Recommended Operating Conditions

over operating free-air temperature range (unless otherwise noted)

PARAMETER		MIN	NOM	MAX	UNIT
V_{RWM}	Reverse Stand-off Voltage		27		V

7.5 Thermal Information

THERMAL METRIC ⁽¹⁾		TVS2700		
		DRV (SON)		
		6 PINS		
				UNIT
R_{qJA}	Junction-to-ambient thermal resistance		70.4	$^\circ\text{C}/\text{W}$
$R_{qJC(top)}$	Junction-to-case (top) thermal resistance		73.7	$^\circ\text{C}/\text{W}$
R_{qJB}	Junction-to-board thermal resistance		40	$^\circ\text{C}/\text{W}$
Y_{JT}	Junction-to-top characterization parameter		2.2	$^\circ\text{C}/\text{W}$

(1) For more information about traditional and new thermal metrics, see the [Semiconductor and IC Package Thermal Metrics](#) application report.

Thermal Information (continued)

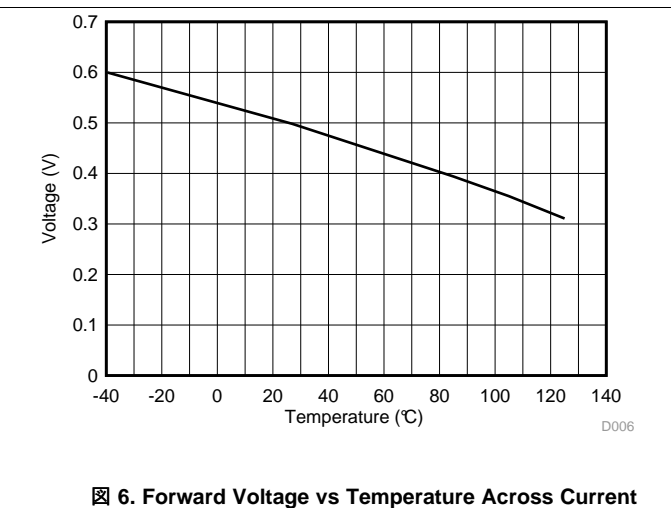
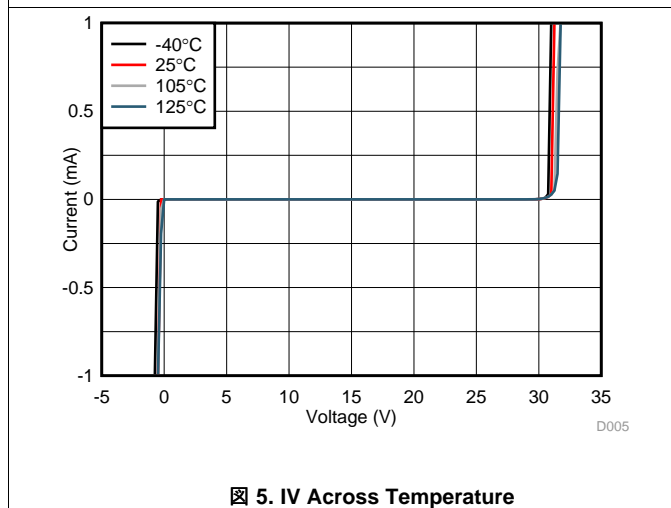
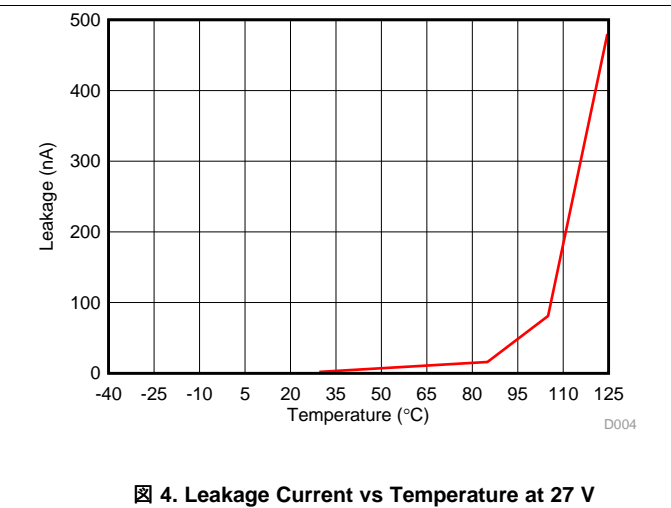
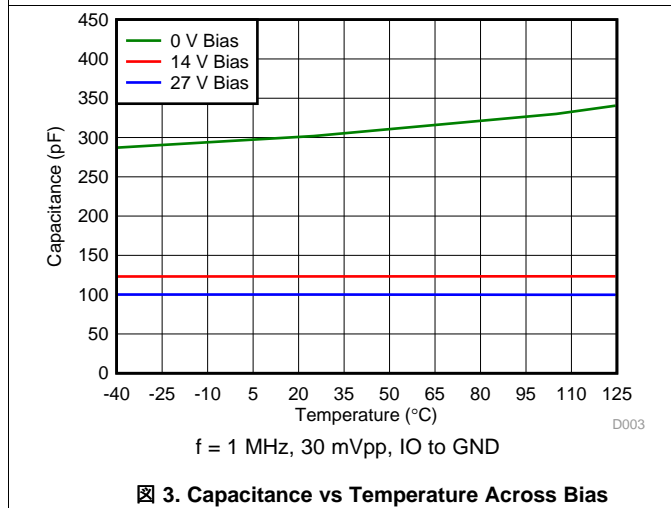
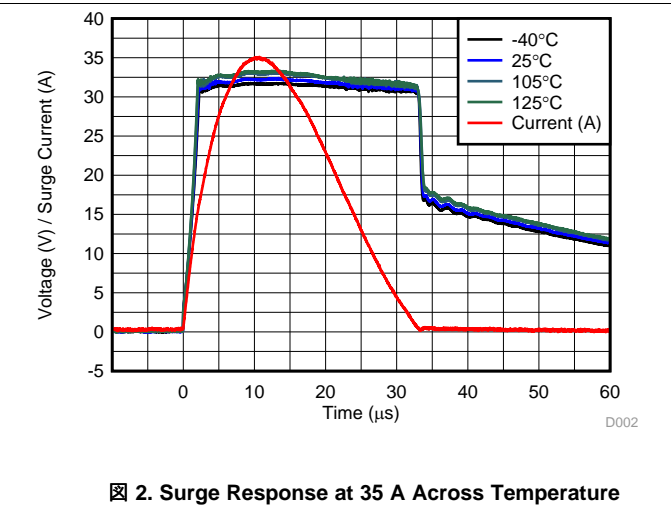
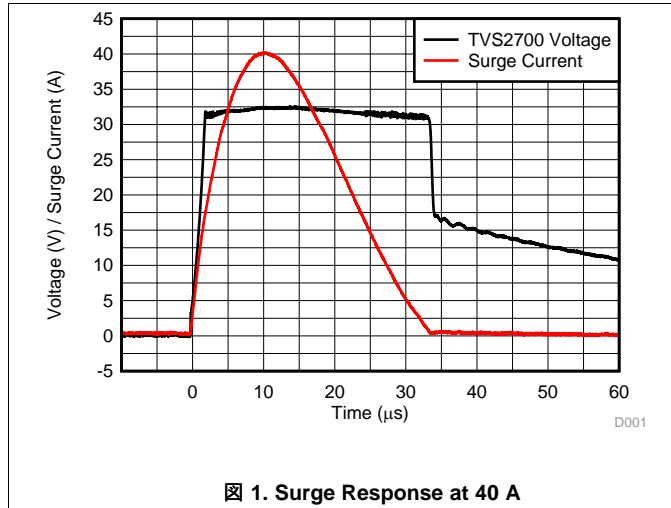
THERMAL METRIC ⁽¹⁾		TVS2700	UNIT
		DRV (SON)	
		6 PINS	
Y_{JB}	Junction-to-board characterization parameter	40.3	°C/W
$R_{qJC(bot)}$	Junction-to-case (bottom) thermal resistance	11	°C/W

7.6 Electrical Characteristics

over operating free-air temperature range (unless otherwise noted)

PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT
V_{RWM}	Reverse Stand-off Voltage		-0.5		27	V
I_{LEAK}	Leakage Current	Measured at $V_{IN} = V_{RWM}$, $T_A = 27^\circ\text{C}$		1.8	60	nA
		Measured at $V_{IN} = V_{RWM}$, $T_A = 85^\circ\text{C}$		14.5	450	nA
		Measured at $V_{IN} = V_{RWM}$, $T_A = 105^\circ\text{C}$		65	1500	nA
V_F	Forward Voltage	$I_{IN} = 1\text{ mA}$ from GND to IO	0.25	0.5	0.65	V
V_{BR}	Break-down Voltage	$I_{IN} = 1\text{ mA}$ from IO to GND	29.3	31.2	33.9	V
V_{FCLAMP}	Forward Clamp Voltage	40 A IEC 61000-4-5 Surge (8/20 μs) from GND to IO, 27°C	1	2	5	V
V_{CLAMP}	Clamp Voltage	24 A IEC 61000-4-5 Surge (8/20 μs) from IO to GND, $V_{IN} = 0\text{ V}$ before surge, 27°C		32.2	33.9	V
		40 A IEC 61000-4-5 Surge (8/20 μs) from IO to GND, $V_{IN} = 0\text{ V}$ before surge, 27°C		32.5	34.1	V
		35 A IEC 61000-4-5 Surge (8/20 μs) from IO to GND, $V_{IN} = V_{RWM}$ before surge, $T_A = 125^\circ\text{C}$		33	35	V
R_{DYN}	8/20 μs surge dynamic resistance	Calculated from V_{CLAMP} at $.5 \cdot I_{pp}$ and I_{pp} surge current levels, 27°C		30		m Ω
C_{IN}	Input pin capacitance	$V_{IN} = V_{RWM}$, $f = 1\text{ MHz}$, 30 mV $_{pp}$, IO to GND		100		pF
SR	Maximum Slew Rate	0- V_{RWM} rising edge, sweep rise time and measure slew rate when $I_{PK} = 1\text{ mA}$, 27°C		2.5		V/ μs
		0- V_{RWM} rising edge, sweep rise time and measure slew rate when $I_{PK} = 1\text{ mA}$, 105°C		0.7		V/ μs

7.7 Typical Characteristics



Typical Characteristics (continued)

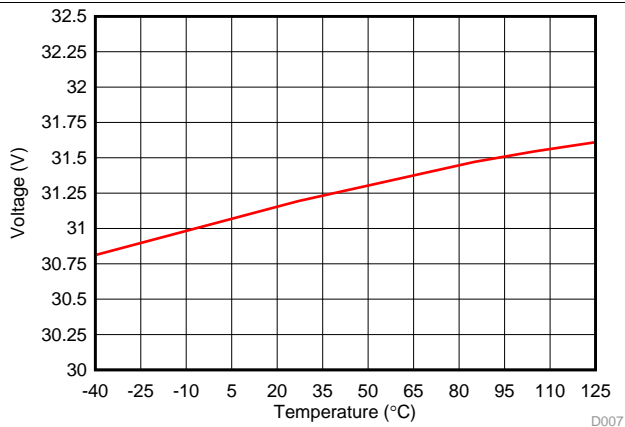


Figure 7. Breakdown Voltage at 1 mA vs Temperature

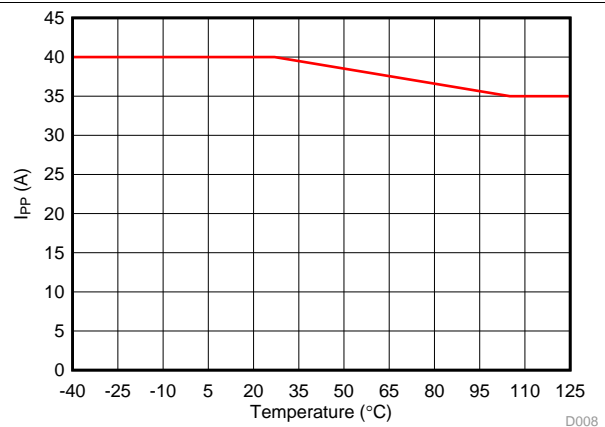


Figure 8. Max Surge Current (8/20 μs) vs Temperature

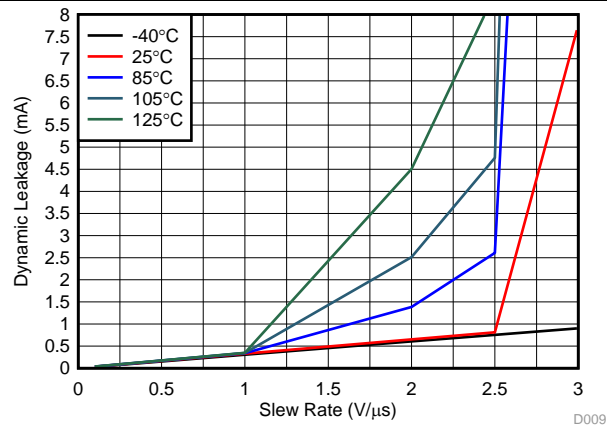


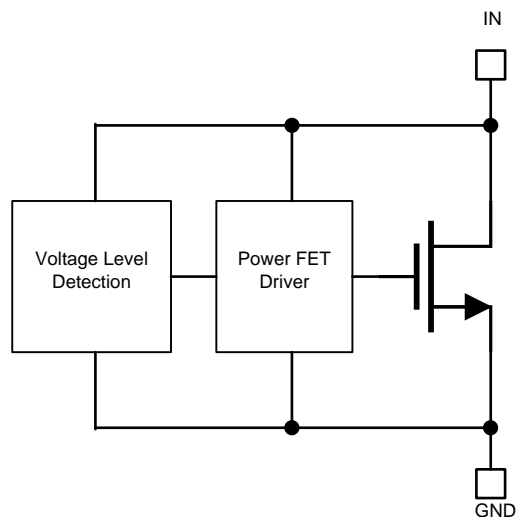
Figure 9. Maximum Leakage vs Signal Slew Rate across Temperature

8 Detailed Description

8.1 Overview

The TVS2700 is a precision clamp with a low, flat clamping voltage during transient overvoltage events like surge and protecting the system with zero voltage overshoot. For a detailed overview of the Flat-Clamp family of devices, please reference TI's [Flat-Clamp surge protection technology for efficient system protection](#) white paper. This document explains in detail the functional operation of the devices and how they impact and improve system design.

8.2 Functional Block Diagram



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8.3 Feature Description

The TVS2700 is a precision clamp that handles 40 A of IEC 61000-4-5 8/20 μ s surge pulse. The flat clamping feature helps keep the clamping voltage very low to keep the downstream circuits from being stressed. The flat clamping feature can also help end-equipment designers save cost by opening up the possibility to use lower-cost lower voltage tolerant downstream ICs. The TVS2700 has minimal leakage under the standoff voltage of 27 V, making it an ideal candidate for applications where low leakage and power dissipation is a necessity. IEC 61000-4-2 and IEC 61000-4-4 ratings make it a robust protection solution for ESD and EFT events. Wide ambient temperature range of -40°C to $+125^{\circ}\text{C}$ makes it a good candidate for most applications. Compact packages enable it to be used in small devices and save board area.

8.4 Reliability Testing

To ensure device reliability, the TV2700 is characterized against 5000 repetitive pulses of 35 A IEC 61000-4-5 8/20 μ s surge pulses at 125°C . The test is performed with less than 10 seconds between each pulse at high temperature to simulate worst case scenarios for fault regulation. After each surge pulse, the TVS2700 clamping voltage, breakdown voltage, and leakage are recorded to ensure that there is no variation or performance degradation. By ensuring robust, reliable, high temperature protection, the TVS2700 enables fault protection in applications that must withstand years of continuous operation with no performance change.

8.5 Device Functional Modes

8.5.1 Protection Specifications

The TVS2700 is specified according to both the IEC 61000-4-5 and IEC 61643-321 standards. This enables usage in systems regardless of which standard is required in relevant product standards or best matches measured fault conditions. The IEC 61000-4-5 standard requires protection against a pulse with a rise time of 8 μs and a half length of 20 μs while the IEC 61643-321 standard requires protection against a much longer pulse with a rise time of 10 μs and a half length of 1000 μs .

The positive and negative surges are imposed to the TVS2700 by a combinational waveform generator (CWG) with a 2- Ω coupling resistor at different peak voltage levels. For powered on transient tests that need power supply bias, inductances are usually used to decouple the transient stress and protect the power supply. The TVS2700 is post tested by guaranteeing that there is no shift in device breakdown or leakage at V_{rwm} .

In addition, the TVS2700 has been tested according to IEC 61000-4-5 to pass a ± 1 kV surge test through a 42- Ω coupling resistor and a 0.5 μF capacitor. This test is a common test requirement for industrial signal I/O lines and the TVS2700 will serve an ideal protection solution for applications with that requirement.

The TVS2700 also integrates IEC 61000-4-2 Level 4 ESD Protection and 80 A of IEC 61000-4-4 EFT Protection. These combine to ensure that the device can protect against all transient conditions regardless of length or type.

For more information on TI's test methods for Surge, ESD, and EFT testing, reference [TI's IEC 61000-4-x Testing Application Note](#).

8.5.2 Minimal Derating

Unlike traditional diodes the TVS2700 has very little derating of max power dissipation and ensures robust performance up to 125°C shown in [Figure 8](#). Traditional TVS diodes lose up to 50% of their current carrying capability when at high temperatures, so a surge pulse above 85°C ambient can cause failures that are not seen at room temperature. The TVS2700 prevents this and ensures that you will see the same level of protection regardless of temperature.

8.5.3 Transient Performance

During large transient swings, the TVS2700 will begin clamping the input signal to protect downstream conditions. While this prevents damage during fault conditions, it can cause leakage when the intended input signal has a fast slew rate. In order to keep power dissipation low and remove the chance of signal distortion, it is recommended to keep the slew rate of any input signal on the TVS2700 below 2.5 V/ μs at room temperature and below 0.7 V/ μs at 125°C shown in [Figure 9](#). Faster slew rates will cause the device to clamp the input signal and draw current through the device for a few microseconds, increasing the rise time of the signal. This will not cause any harm to the system or to the device, however if the fast input voltage swings occur regularly it can cause device overheating.

9 Application and Implementation

注

Information in the following applications sections is not part of the TI component specification, and TI does not warrant its accuracy or completeness. TI's customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

9.1 Application Information

The TVS2700 can be used to protect any power, analog, or digital signal from transient fault conditions caused by the environment or other electrical components.

9.2 Typical Application

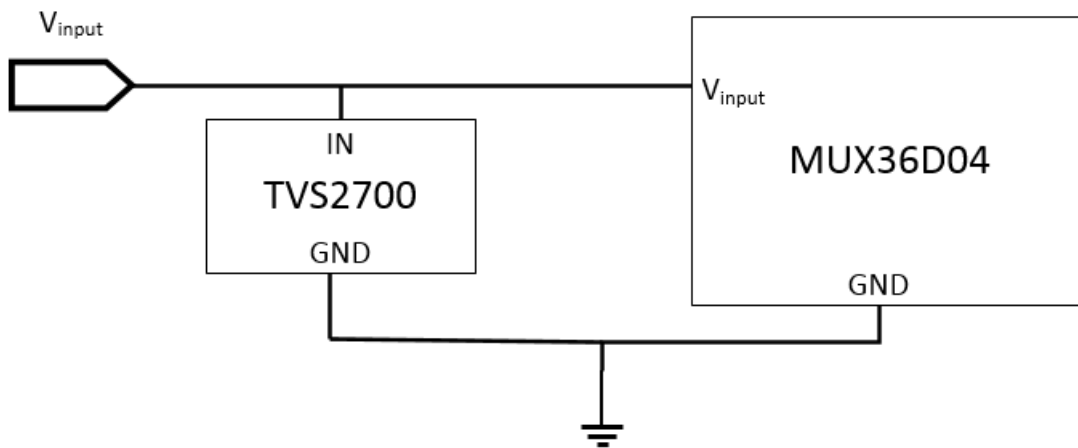


图 10. TVS2700 Application Schematic

9.2.1 Design Requirements

A typical operation for the TVS2700 would be in a multi-channel PLC protecting an analog input, shown in [图 10](#). In this example, a TVS2700 is protecting the input to a MUX36D04, a four channel input multiplexer with a supply max at 36 V. Without any input protection, if a surge event is caused by lightning, coupling, ringing, or any other fault condition this input voltage will rise to hundreds of volts for multiple microseconds, violating the absolute maximum input voltage and harming the device. An ideal surge protection diode will maximize the useable voltage range while still clamping at a safe level for the system, so TI's Flat-Clamp technology provides the best protection solution.

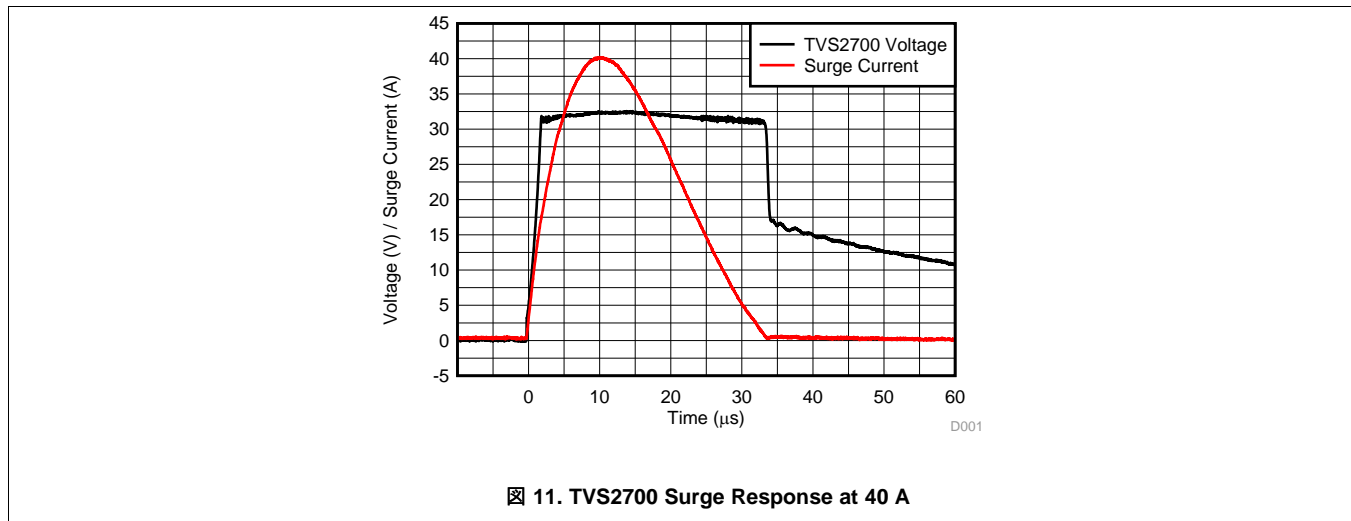
9.2.2 Detailed Design Procedure

If the TVS2700 is in place to protect the device, during a surge event the voltage will rise to the breakdown of the diode at 31.2 V, and then the TVS2700 will turn on, shunting the surge current to ground. With the low dynamic resistance of the TVS2700, even large amounts of surge current will have minimal impact on the clamping voltage. The dynamic resistance of the TVS2700 is around 30 m Ω , which means 40 A of surge current will cause a voltage raise of $40 \text{ A} \times 30 \text{ m}\Omega = 1.2 \text{ V}$. Because the device turns on at 31 V, this means the module input will be exposed to a maximum of $31.2 \text{ V} + 1.2 \text{ V} = 33.3 \text{ V}$ during surge pulses, well within the MUX36D04 absolute maximum. This pulse is shown in [Figure 11](#) and ensures robust protection of the circuit.

Typical Application (continued)

Finally, the small size of the device also improves fault protection by lowering the effect of fault current coupling onto neighboring traces. The small form factor of the TVS2700 allows the device to be placed extremely close to the input connector, lowering the length of the path fault current will take through the system compared to larger protection solutions.

9.2.3 Application Curves



9.2.4 Configuration Options

The TVS2700 can be used in either unidirectional or bidirectional configuration. The TVS2700 shows unidirectional usage to protect an input. By placing two TVS2700's in series with reverse orientation, bidirectional operation can be utilized which will allow a working voltage of ± 27 V. The TVS2700 operation in bidirectional will be similar to unidirectional operation, with a minor increase in breakdown voltage and clamping voltage. The TVS3300 bidirectional performance has been characterized in the [TVS3300 Configurations Characterization](#). While the TVS2700 in bidirectional configuration has not specifically been characterized, it will have similar relative changes to the TVS3300 in bidirectional configuration.

10 Power Supply Recommendations

The TVS2700 is a clamping device so there is no need to power it. Be careful not to violate the recommended V_{IN} voltage range (0 V to 27 V) to ensure the device functions properly.

11 Layout

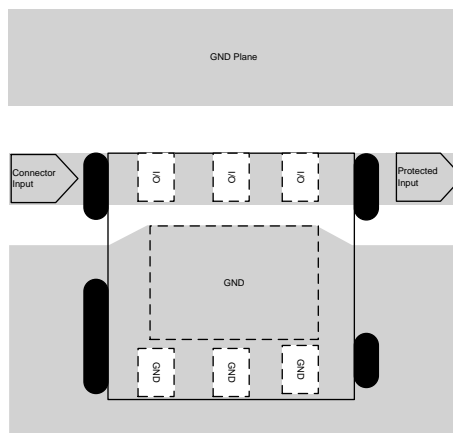
11.1 Layout Guidelines

The optimum placement is as close to the connector as possible. EMI during an ESD event can couple from the trace being struck to other nearby unprotected traces, resulting in early system failures. The PCB designer must minimize the possibility of EMI coupling by keeping any unprotected traces away from the protected traces which are between the TVS and the connector.

Route the protected traces as straight as possible.

Eliminate any sharp corners on the protected traces between the TVS2700 and the connector by using rounded corners with the largest radii possible. Electric fields tend to build up on corners, increasing EMI coupling.

11.2 Layout Example



☒ 12. TVS2700 Layout

12 デバイスおよびドキュメントのサポート

12.1 ドキュメントの更新通知を受け取る方法

ドキュメントの更新についての通知を受け取るには、ti.comのデバイス製品フォルダを開いてください。右上の隅にある「通知を受け取る」をクリックして登録すると、変更されたすべての製品情報に関するダイジェストを毎週受け取れます。変更の詳細については、修正されたドキュメントに含まれている改訂履歴をご覧ください。

12.2 コミュニティ・リソース

The following links connect to TI community resources. Linked contents are provided "AS IS" by the respective contributors. They do not constitute TI specifications and do not necessarily reflect TI's views; see TI's [Terms of Use](#).

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設計サポート *TIの設計サポート* 役に立つE2Eフォーラムや、設計サポート・ツールをすばやく見つけることができます。技術サポート用の連絡先情報も参照できます。

12.3 商標

E2E is a trademark of Texas Instruments.

12.4 静電気放電に関する注意事項



すべての集積回路は、適切なESD保護方法を用いて、取扱いと保存を行うようにして下さい。

静電気放電はわずかな性能の低下から完全なデバイスの故障に至るまで、様々な損傷を与えます。高精度の集積回路は、損傷に対して敏感であり、極めてわずかなパラメータの変化により、デバイスに規定された仕様に適合しなくなる場合があります。

12.5 Glossary

SLYZ022 — *TI Glossary*.

This glossary lists and explains terms, acronyms, and definitions.

13 メカニカル、パッケージ、および注文情報

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PACKAGING INFORMATION

Orderable part number	Status (1)	Material type (2)	Package Pins	Package qty Carrier	RoHS (3)	Lead finish/ Ball material (4)	MSL rating/ Peak reflow (5)	Op temp (°C)	Part marking (6)
TVS2700DRVR	Active	Production	WSON (DRV) 6	3000 LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	1HTH
TVS2700DRVR.A	Active	Production	WSON (DRV) 6	3000 LARGE T&R	Yes	NIPDAU	Level-1-260C-UNLIM	-40 to 125	1HTH

(1) **Status:** For more details on status, see our [product life cycle](#).

(2) **Material type:** When designated, preproduction parts are prototypes/experimental devices, and are not yet approved or released for full production. Testing and final process, including without limitation quality assurance, reliability performance testing, and/or process qualification, may not yet be complete, and this item is subject to further changes or possible discontinuation. If available for ordering, purchases will be subject to an additional waiver at checkout, and are intended for early internal evaluation purposes only. These items are sold without warranties of any kind.

(3) **RoHS values:** Yes, No, RoHS Exempt. See the [TI RoHS Statement](#) for additional information and value definition.

(4) **Lead finish/Ball material:** Parts may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

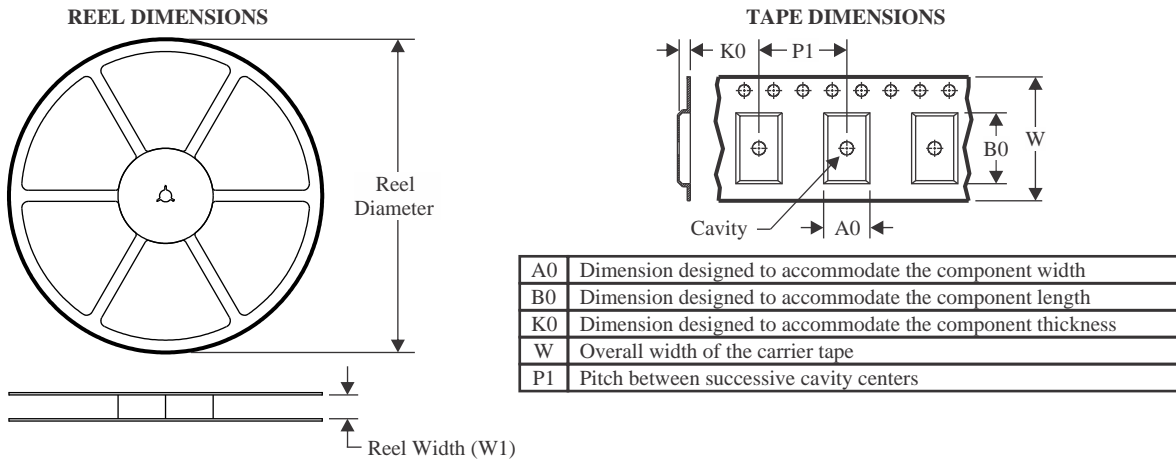
(5) **MSL rating/Peak reflow:** The moisture sensitivity level ratings and peak solder (reflow) temperatures. In the event that a part has multiple moisture sensitivity ratings, only the lowest level per JEDEC standards is shown. Refer to the shipping label for the actual reflow temperature that will be used to mount the part to the printed circuit board.

(6) **Part marking:** There may be an additional marking, which relates to the logo, the lot trace code information, or the environmental category of the part.

Multiple part markings will be inside parentheses. Only one part marking contained in parentheses and separated by a "~" will appear on a part. If a line is indented then it is a continuation of the previous line and the two combined represent the entire part marking for that device.

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TAPE AND REEL INFORMATION

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE


*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TVS2700DRVR	WS0N	DRV	6	3000	178.0	8.4	2.3	2.3	1.15	4.0	8.0	Q2

TAPE AND REEL BOX DIMENSIONS


*All dimensions are nominal

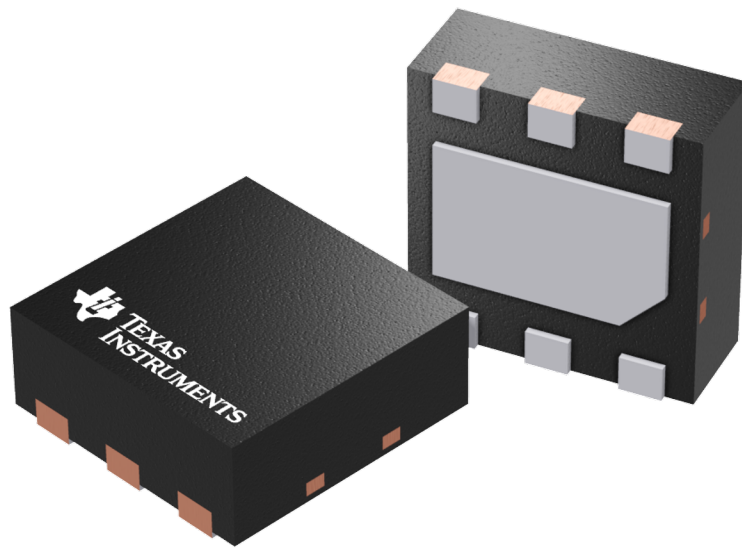
Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TVS2700DRVR	WSON	DRV	6	3000	208.0	191.0	35.0

GENERIC PACKAGE VIEW

DRV 6

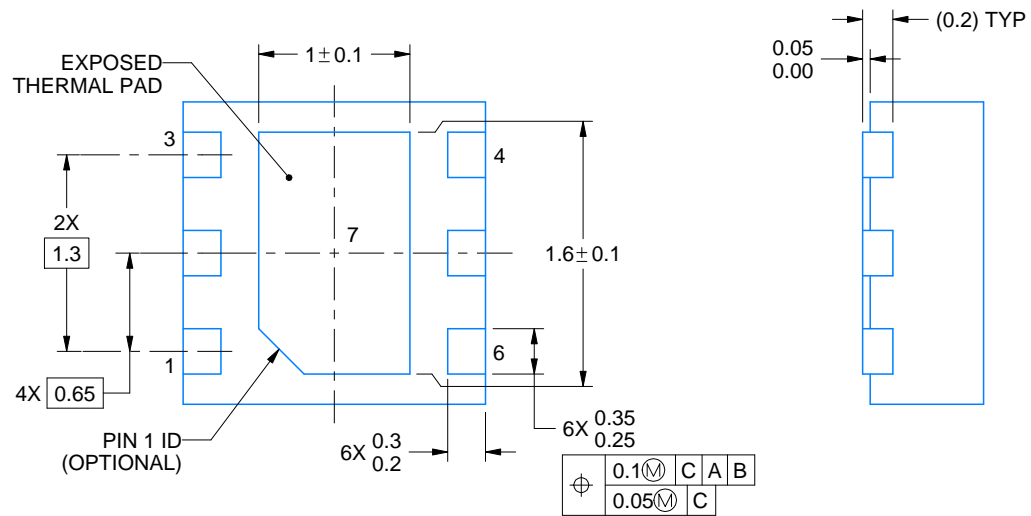
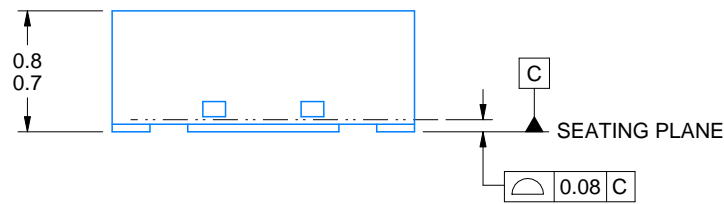
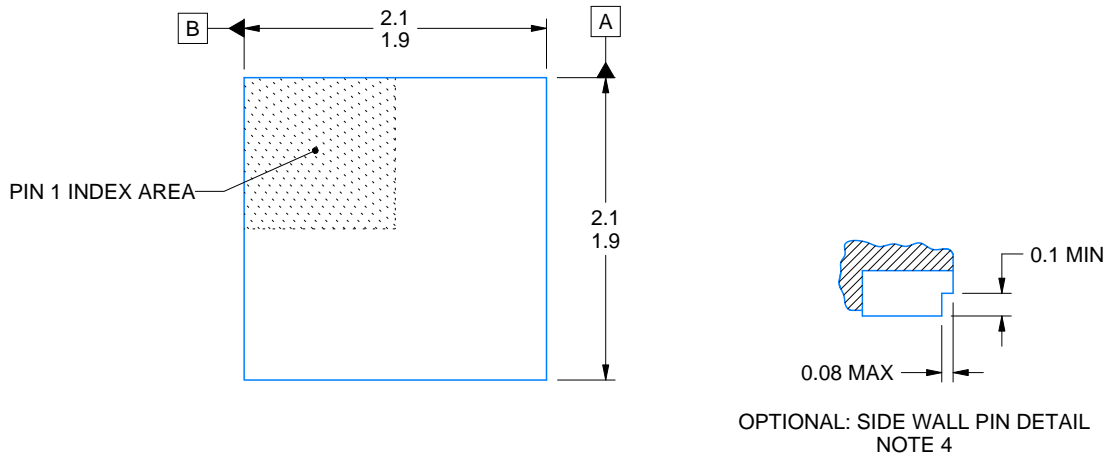
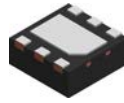
WSON - 0.8 mm max height

PLASTIC SMALL OUTLINE - NO LEAD



Images above are just a representation of the package family, actual package may vary.
Refer to the product data sheet for package details.

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NOTES:

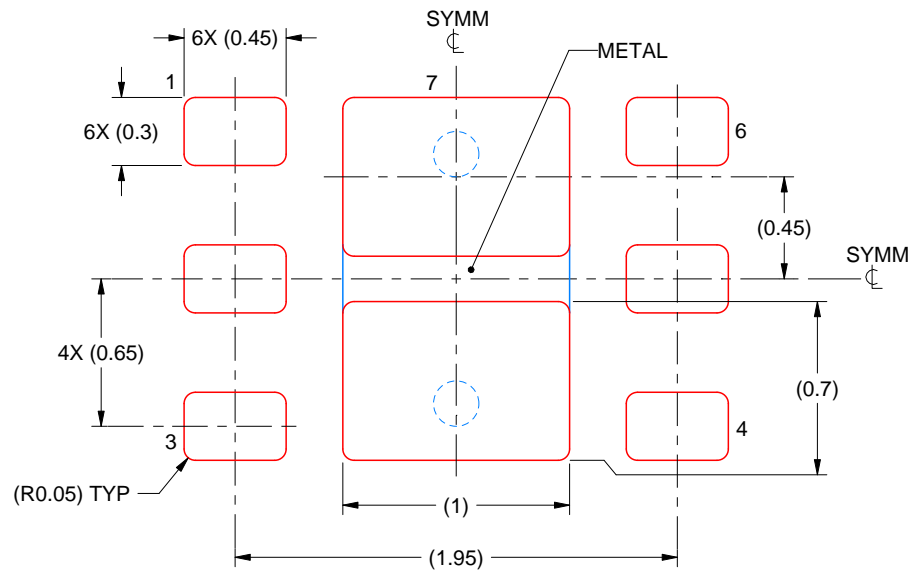
1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. The package thermal pad must be soldered to the printed circuit board for thermal and mechanical performance.
4. Minimum 0.1 mm solder wetting on pin side wall. Available for wettable flank version only.

EXAMPLE STENCIL DESIGN

DRV0006A

WSON - 0.8 mm max height

PLASTIC SMALL OUTLINE - NO LEAD



SOLDER PASTE EXAMPLE
BASED ON 0.125 mm THICK STENCIL

EXPOSED PAD #7
88% PRINTED SOLDER COVERAGE BY AREA UNDER PACKAGE
SCALE:30X

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NOTES: (continued)

7. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.

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最終更新日 : 2025 年 10 月