

4221755/B 08/2015

MicroSiP is a trademark of Texas Instruments.

NOTES:

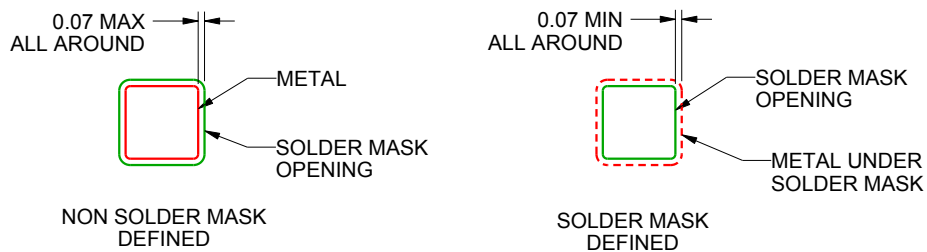
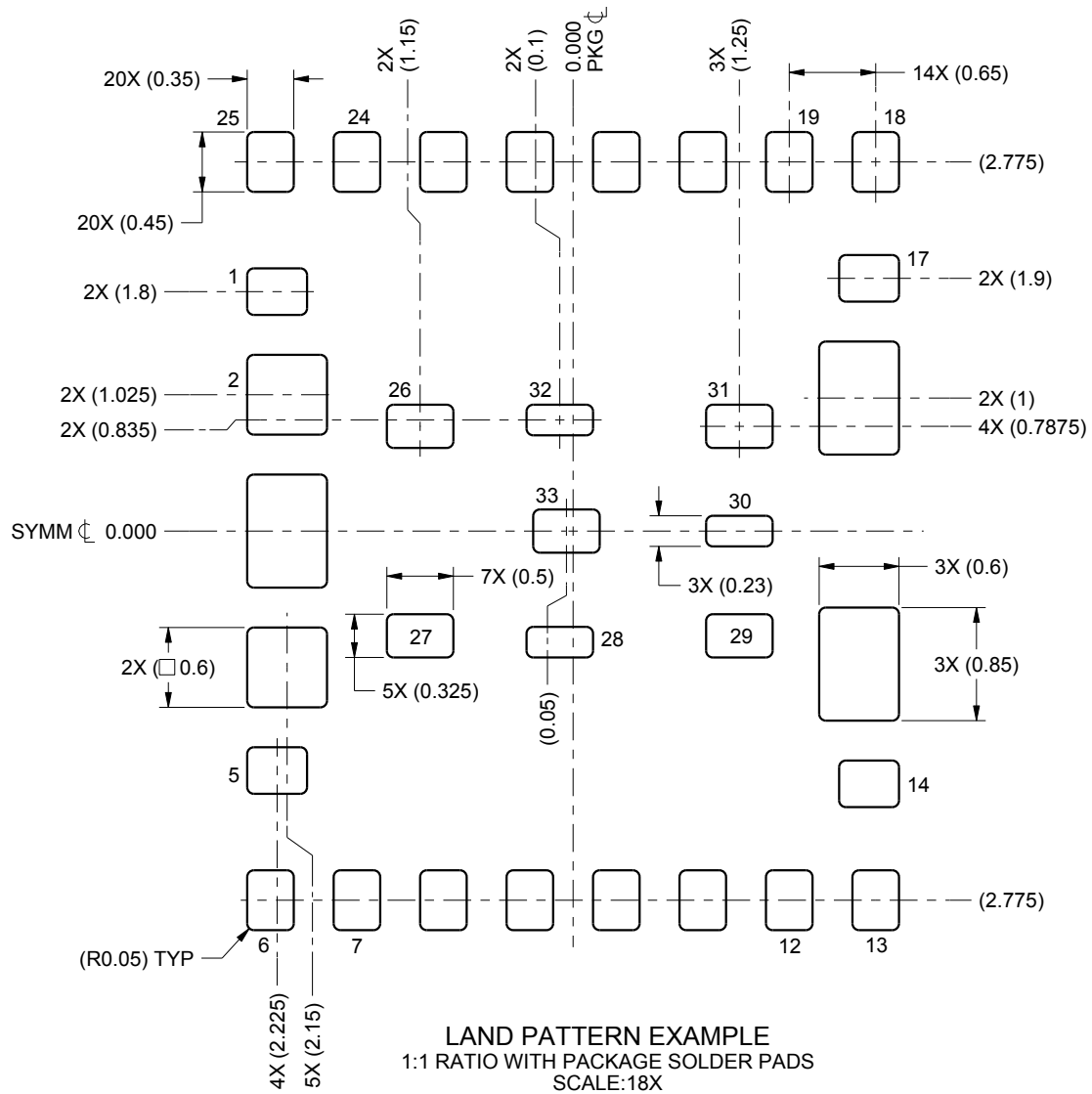
1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. Location, size and quantity of components are for reference only and could vary.

EXAMPLE BOARD LAYOUT

SIL0033A

MicroSiP™ - 2.7 mm max height

MICRO SYSTEM IN PACKAGE



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NOTES: (continued)

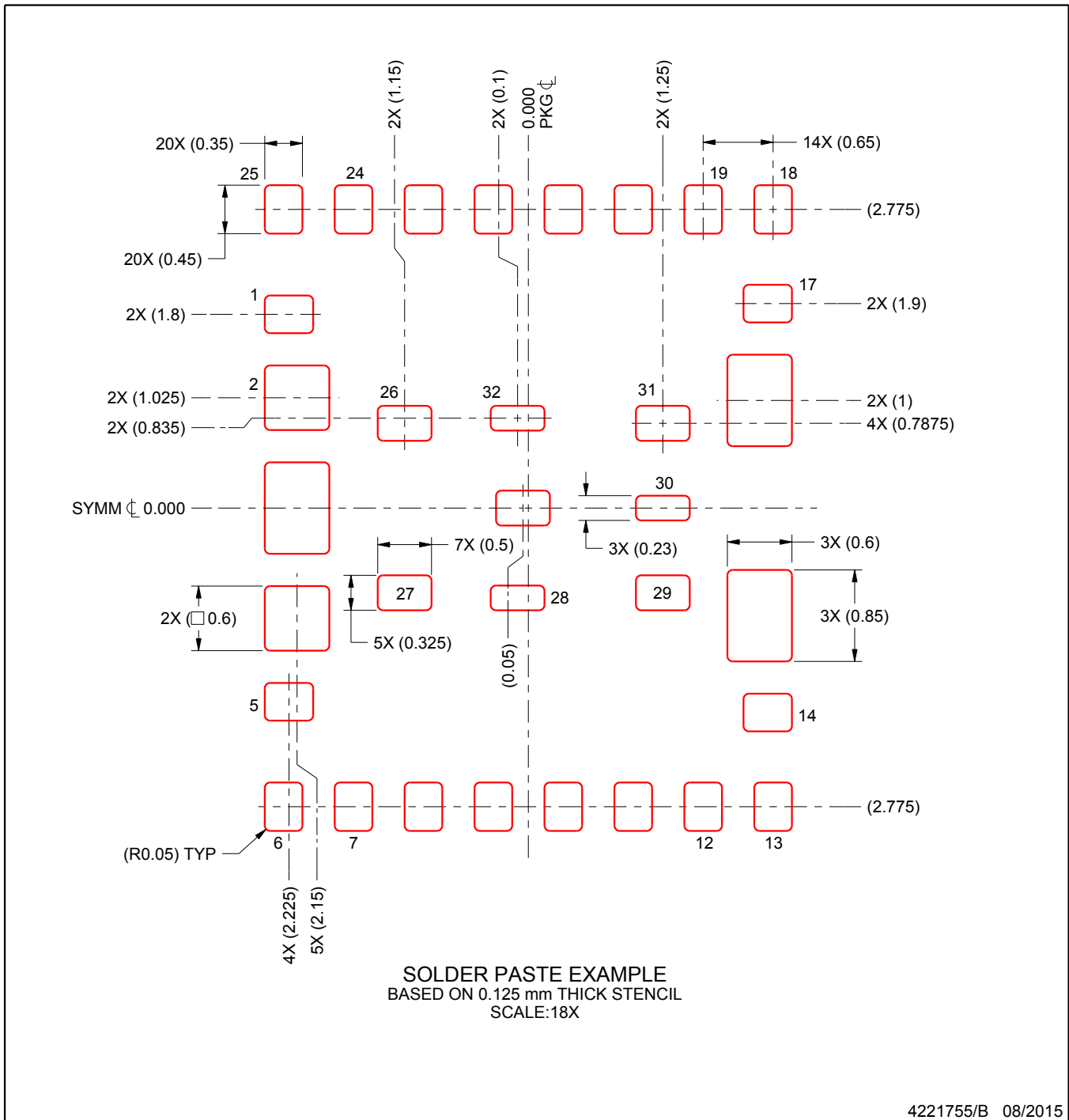
4. This package is designed to be soldered to thermal pads on the board. For more information, see Texas Instruments literature number SLUA271 (www.ti.com/lit/slua271).

EXAMPLE STENCIL DESIGN

SIL0033A

MicroSiP™ - 2.7 mm max height

MICRO SYSTEM IN PACKAGE



NOTES: (continued)

5. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.

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