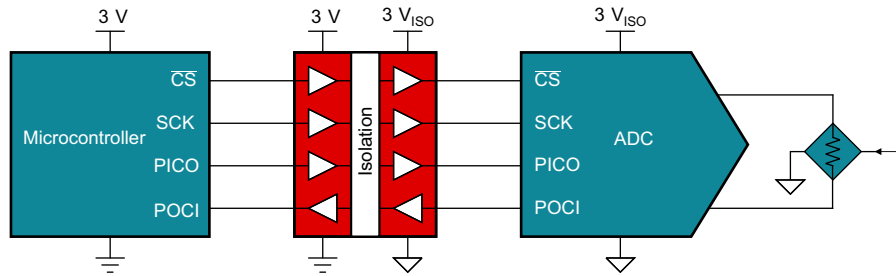


# Product Overview

## Isolating SPI Signals



Example SPI Isolation Block Diagram

### Design Considerations

- Prevents DC and unwanted AC currents between controller devices and peripheral ICs
- Allows signal and power transfer between controller devices and peripheral ICs
- Protects low voltage parts in a system from high voltage circuits
- Diminishes the effect of ground potential difference
- [\[FAQ\] How to connect SPI MCU to multiple isolated SPI nodes?](#)
- [\[FAQ\] How to calculate the maximum SPI speed supported by a digital isoлятор?](#)
- [Digital Isolator Design Guide](#)
- [Top Design Questions About Digital Isolators](#)
- Need additional assistance? Ask our engineers a question on the [TI E2E™ Isolation Support Forum](#)

### Recommended Parts

Part Number	AEC-Q100	Voltage Range	Data Rate	Features
<a href="#">ISO6741</a>		1.71 - 5.5 V	50 Mpps	High CMTI Reinforced and basic isolation General purpose
<a href="#">ISO6741-Q1</a>	✓			
<a href="#">ISO7741</a>		2.25 - 5.5 V	100 Mbps	High CMTI Reinforced and basic isolation High-speed 30 krad (Si) Total Ionizing Dose (TID) 600 V working voltage Space enhanced plastic
<a href="#">ISO7741-Q1</a>	✓			
<a href="#">ISOS141-SEP</a>				
<a href="#">ISO7041</a>			4 Mbps	Ultra low power
<a href="#">ISO7041-Q1</a>	✓			
<a href="#">ISOW7741</a>		1.71 - 5.5 V	100 Mbps	Digital isolator with integrated power Low-emissions
<a href="#">ISO7841</a>		2.25 - 5.5V		Extra-wide creepage and clearance package

For more devices, browse through the [online parametric tool](#) where you can sort by desired voltage, channel numbers, and other features.

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