# **Audio Codec Overview**

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### **Audio Portfolio**

Texas Instruments' complete line of audio products feature over 2000 devices within our DAC, ADC, CODEC, AMP, Line driver, Interface, USB Audio, and other product families.

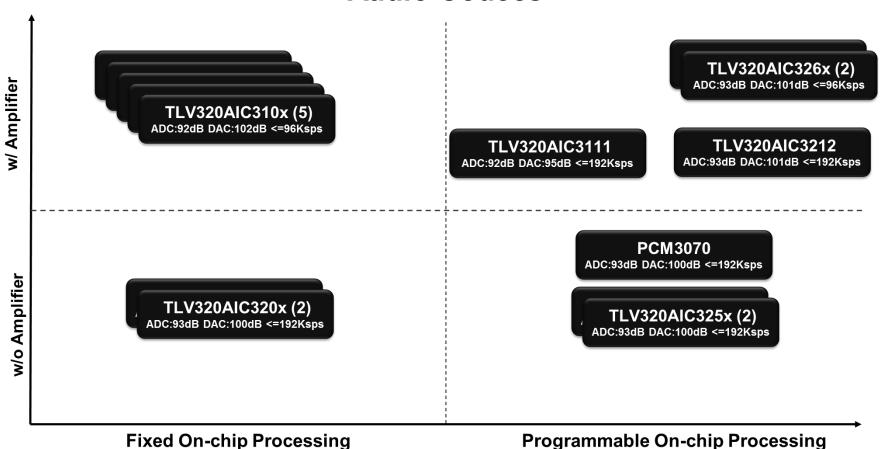
ADC		-AMP-		CODEC		DAC		Smart AMP	
HW / SW Controlled  1.8-3.3-5V Power  Audio PLL		Stereo/Mono Class AB & D w/Boost Analog Input		Power and Performance Optimized Integrated Amplifiers		Current Output, Voltage Output, 2VRMS Output, Delta Sigma Integrated Amplifiers		Low and Mid Power Amplifiers with Smart Speaker Protection Integrated IV Sense,	
Energy Sense		Low Power options w/ PurePath <sup>TM</sup>		Integrated DSP		DSP integration		Excursion and Temp sensing options	
#ADC	1-4	Inputs	1-6	#ADC/DAC	0-6 / 1-8 72-112dB	#DAC	1-8	Output	7.5 – 50W
SNR PWR	92-123dB 17-600mW	Output Power	0.35-400W	SNR PWR	6.7mw +	SNR THD (%)	92-127dB 0.0004-0.04	PSRR Inputs	75 dB Analog/Dig.
Packages: 5.3 - 144mm²		Packages: 2 - 517mm <sup>2</sup>		Packages: 7.8 - 170mm²		Packages: 6.4 - 144mm²		Packages: 7.5 - 76mm²	
1K: \$0.80 - \$8.55		1K: \$0.26 - \$10.75		1K: \$1.23 - \$19.25		1K: \$0.89 - \$11.18		1K: \$1.39 - \$3.75	

## **Audio Codec Features**

- Low power without sacrificing performance
  - Less than 10mW power up to 102dB SNR
  - PowerTune<sup>™</sup> Power vs. SNR
- Optional Amplifier integration
  - Class D, Headphone (DirectPath<sup>™</sup>)
- Wide range of inputs/outputs
  - Up to 10 inputs / 7 outputs (AIC3106)
- Integrated Signal Processing
  - EQ, Bass Boost, Treble, Algorithm
- Various Control and Audio I/F options
  - I2C, SPI, H/W, DSP, L, R, I2S, etc
- Multiple packaging options
  - as small as 7.8mm<sup>2</sup>
  - Multiple pin to pin options
  - TQFP, CSP, BGA, TSSOP

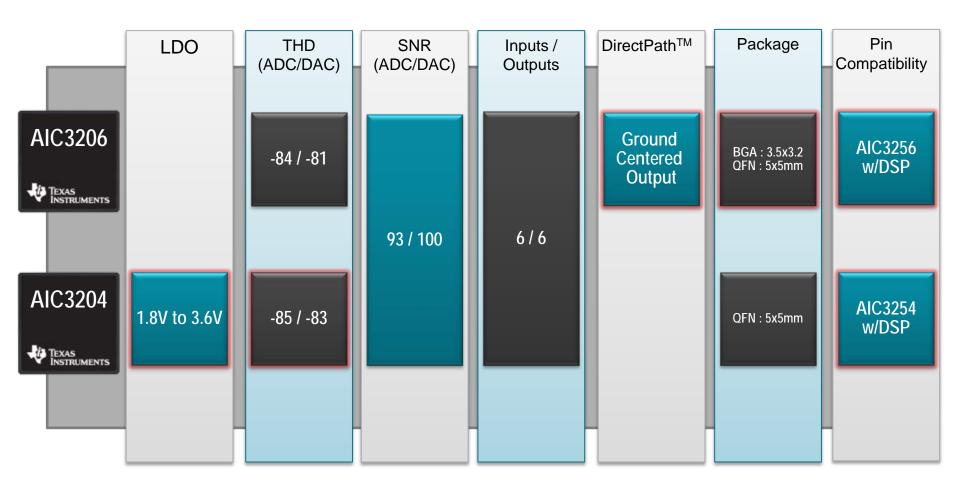


### **Audio Codecs**



Additional CODEC solutions are available. These are our most popular!

# TLV320AIC320x Series



## TLV320AIC320x

1.8V Low Power Stereo Codecs

#### **Features**

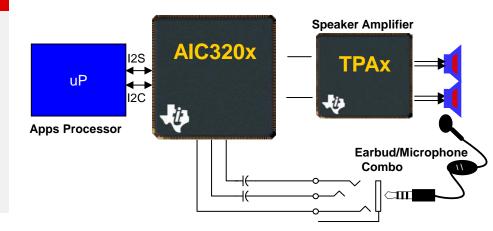
- Stereo 8-192ksps audio DAC up to 100dB SNR
- Stereo 8-192ksps audio ADC up to 93dB SNR
- PowerTune™ adjustable Power vs. SNR
- DirectPath <sup>™</sup> Headphone AMP (3206 only)
- Integrated LDOs (3204 only)
- Extensive record/playback processing blocks
- Integrated PLL

#### **Benefits**

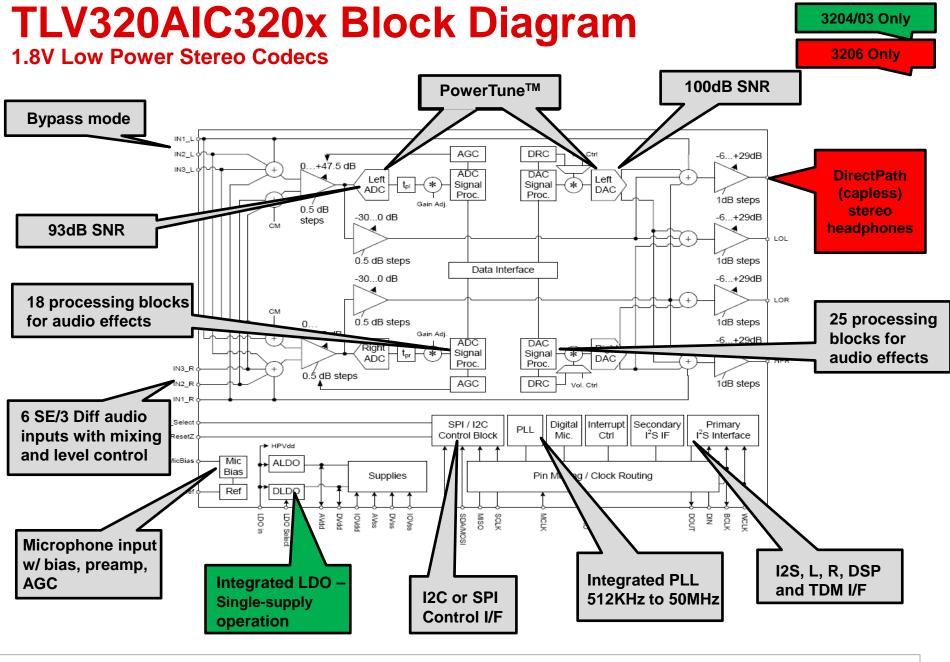
- User controls power vs. SNR tradeoff
- Eliminates DC-Blocking Caps (3206 only)
- Allow single-supply operation (1.9V 3.6V)
- Flexible audio clock generation (512KHz to 50MHz input clock)

### **Applications**

Cell Phones
Portable navigation Devices (PND)
Headsets

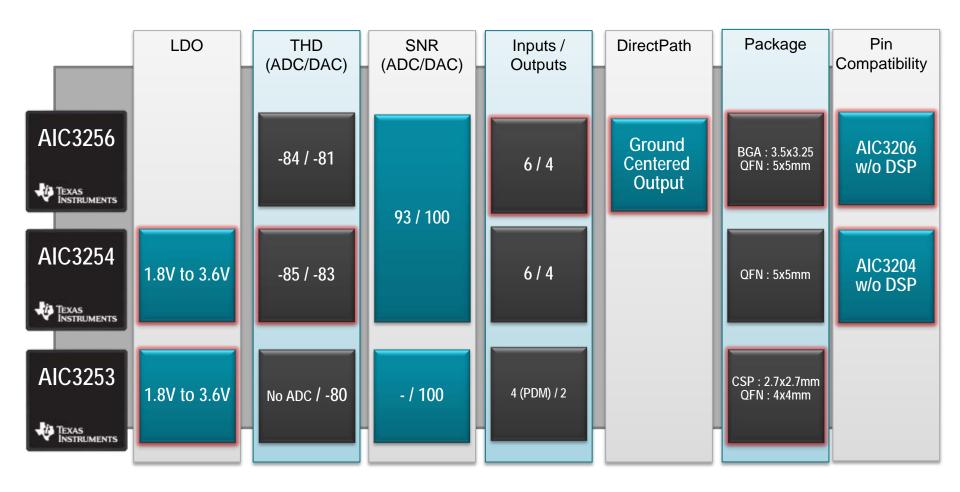








## **TLV320AIC325x Series**



## TLV320AIC325x

#### Low Power Stereo Codecs w/ MiniDSP

#### **Features**

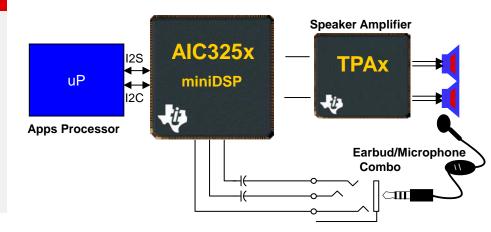
- Stereo 8-192ksps audio DAC up to 100dB SNR
- Stereo 8-192ksps audio ADC up to 93dB SNR
  - ADC not included in AIC3253
- PowerTune™ adjustable Power vs. SNR
- DirectPath ™ Headphone AMP (3206 only)
- Integrated LDOs (3204 only)
- Integrated PLL
- Integrated DSPs

#### **Benefits**

- User controls power vs. SNR tradeoff
- Eliminates DC-Blocking Caps
- Allow single-supply operation (1.9V 3.6V)
- Flexible audio clock generation (512KHz to 50MHz input clock)
- Offload host processor

### **Applications**

Cell Phones
Portable navigation Devices (PND)
Headsets





# TLV320AIC325x Series

operation

Low Power Stereo Codecs w/ MiniDSP PowerTune™ 100dB SNR Bypass mode IN1 L AGC IN2 L Vol. Ctrl 0...+47.5 dB IN3 L ADC DAC Signal Signal 93dB SNR ADC DAC Proc. Proc. 1dB step Gain Adj. (No ADCs 0.5 dB **DirectPath** -6...+29dB steps -30...0 dB on 3253) (capless) +LOL stereo 0.5 dB steps headphones 1dB steps Data miniDSP miniDSP Interface **MiniDSPs** -6...+29dB + ) 0.5 dB steps 1dB steps 0... +47.5 dB Gain Adj. -6...+29dB 6 SE/3 Diff audio ADC DAC Right Right Signal Signal inputs with mixing AĎC DAC Proc. Proc. and level control 0.5 dB steps AGC DRC Vol. Ctrl 1dB steps IN1 R GND Sense SPI Select SPI/I2C Dig Sec Primary PLL VNEG Mic I2S VF I<sup>2</sup>S Interface rupt Control Block Reset Microphone input Fly\_N Charge w/ bias, preamp, MicBias ALDO Fly\_P Mic **AGC** Bias Slock Routing Pin Muxi Supplies Ref DLDO Ref DVID G SDA 8 **Integrated LDO -**12S, L, R, DSP **Integrated PLL** Single-supply

I2C or SPI

Control I/F

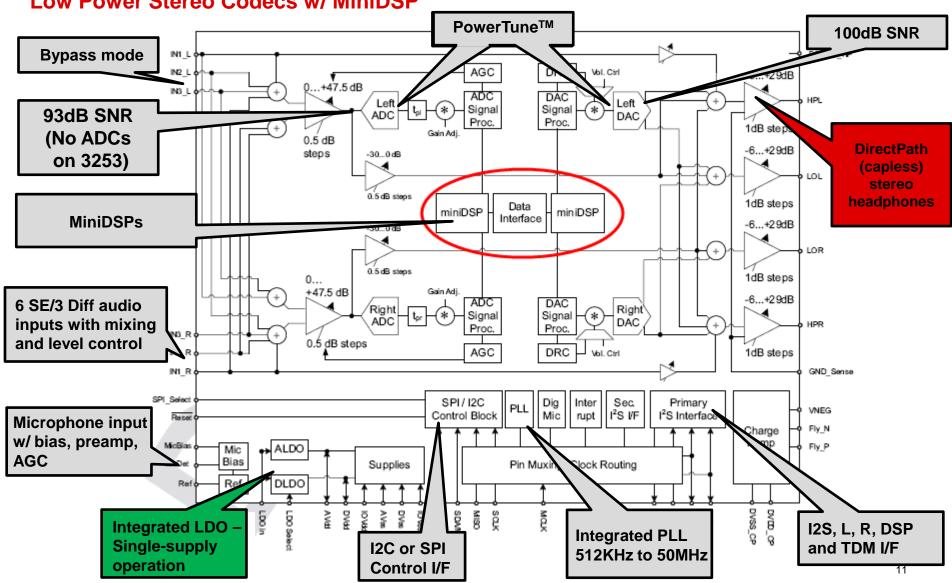


512KHz to 50MHz

and TDM I/F

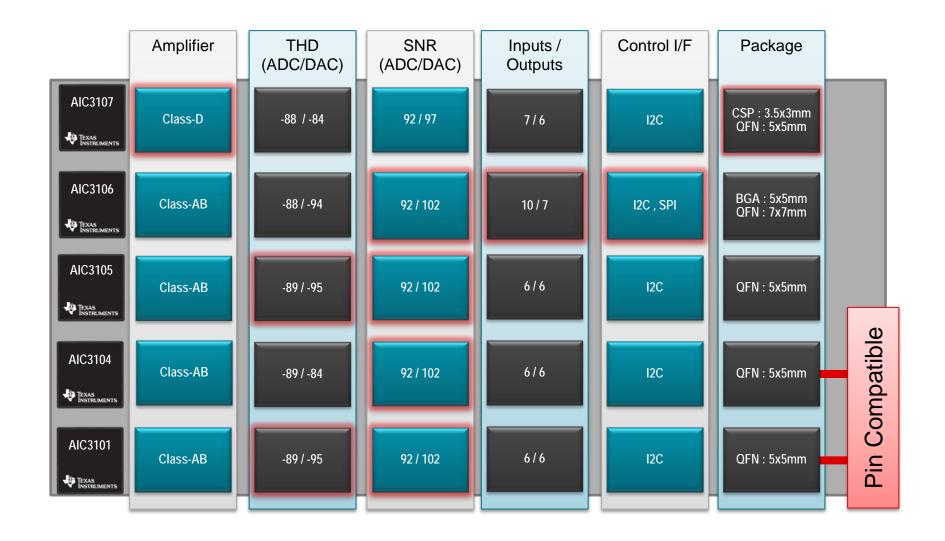
## TLV320AIC325x Series

Low Power Stereo Codecs w/ MiniDSP





## **TLV320AIC310x Series**



# **TLV320AIC310x**

#### **Low Power Stereo Codecs**

#### **Features**

- Stereo 8-96ksps audio DAC up to 102dB SNR
- Stereo 8-96ksps audio ADC up to 92dB SNR
- Integrated Amplifier
  - AIC3107 (Class D)
  - AIC3101-6(Class AB)
- Built-in Headphone Detect (3106/3107)
- Integrated PLL
- Programmable Input/Output Gains

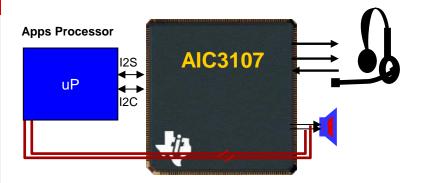
### **Benefits**

- No external Amp required Single chip solution
- Flexible audio clock generation (512KHz to 50MHz input clock)
- Longer AGC time constraints

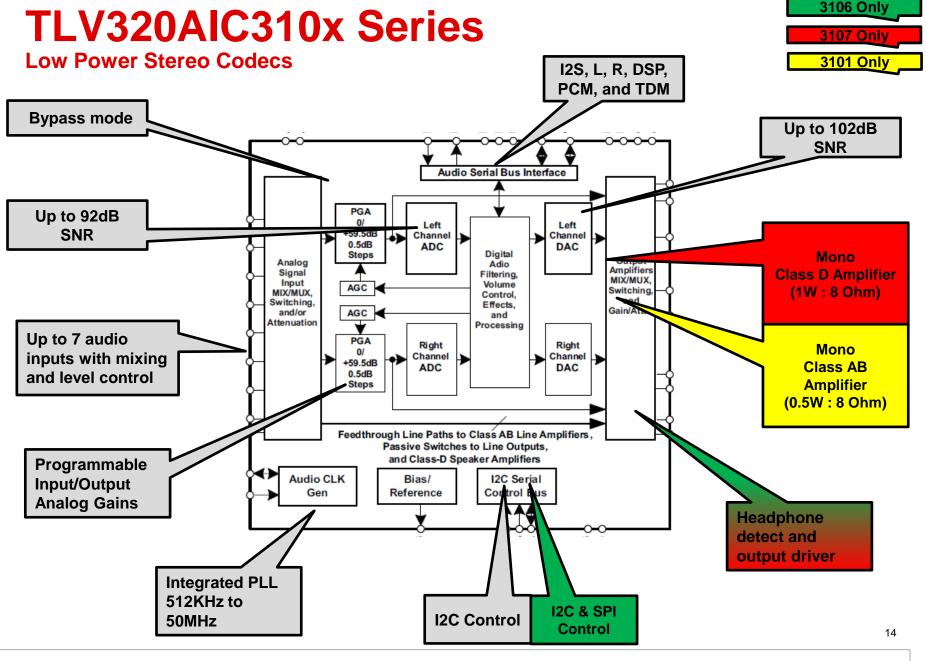
### **Applications**

Cell Phones
Portable navigation Devices (PND)
Headsets
Voice/audio processors
Conference / IP Phones

General Portable Audio Equipment



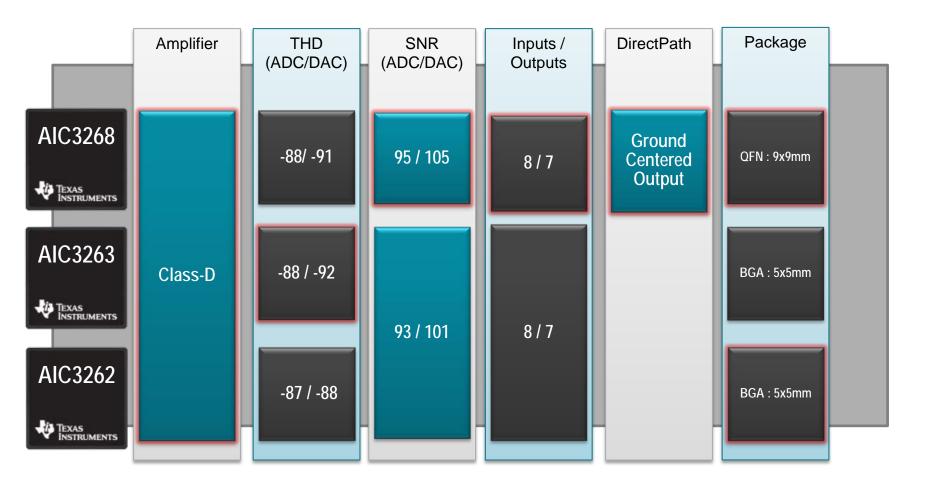






## TLV320AIC326x Series

Low Power Stereo Codecs w/ Amplifier and miniDSP



## TLV320AIC326x

### Low Power Stereo Codecs w/ Amplifier/On-chip Processing

#### **Features**

- Stereo 8-192ksps audio DAC up to 105dB SNR
- Stereo 8-192ksps audio ADC up to 95dB SNR
- Integrated Stereo Class-D Amplifier
- PowerTune™ adjustable Power vs. SNR
- DirectPath ™ Headphone Driver
- Fully Programmable Integrated DSPs
- 12-bit Secondary ADC w/ Temp Sensor
- Integrated PLL

#### **Benefits**

- No external Amp required Single chip solution
- User controls power vs. SNR tradeoff
- Eliminates DC-Blocking Caps
- Extensive Signal Processing Options
- Voltage measurements for suspend settings
- Flexible audio clock generation (512KHz to 50MHz input clock)

### **Applications**

Cell Phones

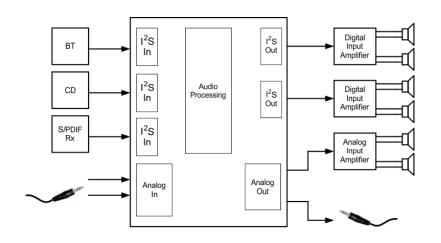
Portable navigation Devices (PND)

Headsets

Voice/audio processors

Conference / IP Phones

General Portable Audio Equipment

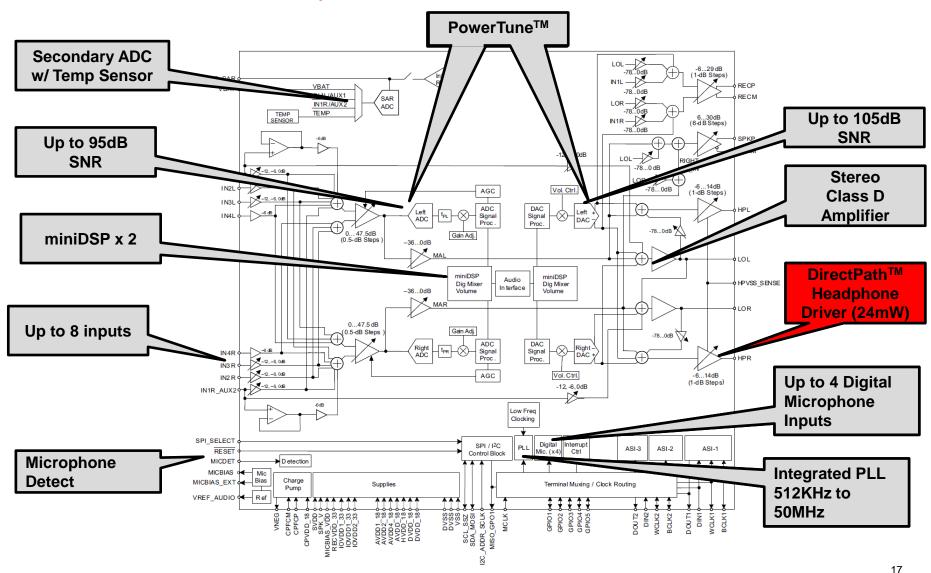




#### **3268Only**

## TLV320AIC326x Series

Low Power Stereo Codecs w/ Amplifier/miniDSP



## **TLV320AIC3212**

### Low Power Stereo Codec w/ Amplifier / HP Driver

#### **Features**

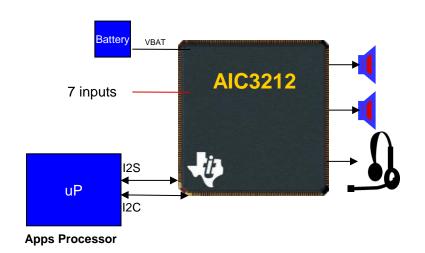
- Stereo 8-192ksps audio DAC up to 101dB SNR
- Stereo 8-192ksps audio ADC up to 93dB SNR
- Integrated Stereo Class-D Amplifier
- PowerTune™ adjustable Power vs. SNR
- DirectPath ™ Headphone Driver
- Selectable audio-processing blocks
- 12-bit Secondary ADC w/ Temp Sensor
- Integrated PLL
- Small 4.81x4.81, 81-ball WCSP (YZF)

### **Applications**

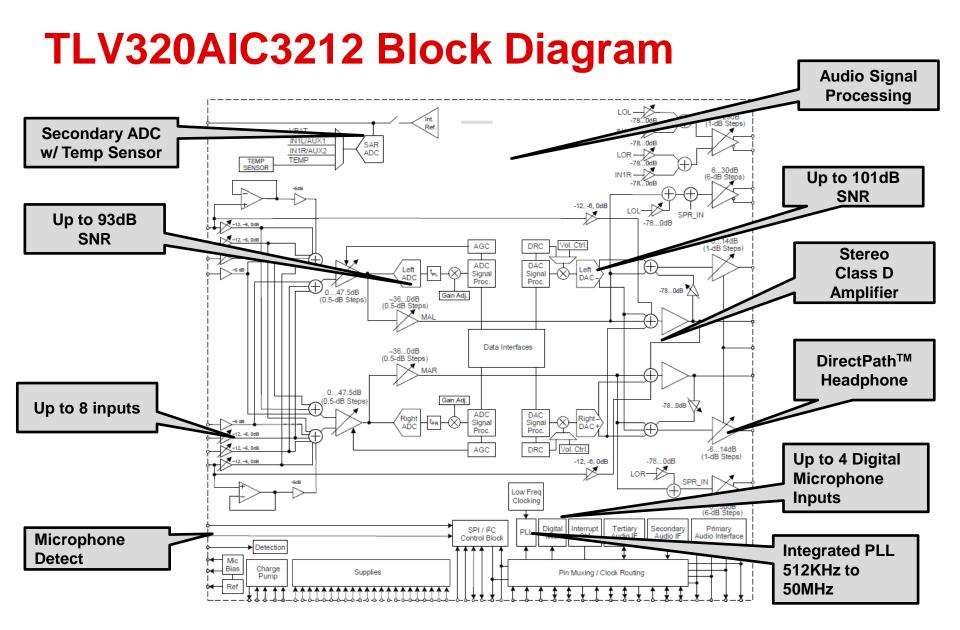
Cell Phones
Portable Computing
Tablets / E-Books
Headsets
Portable navigation Devices (PND)

#### **Benefits**

- No external Amp required Single chip solution
- User controls power vs. SNR tradeoff
- Eliminates DC-Blocking Caps
- Noise removal of mechanical coupling
- Voltage measurements for suspend settings
- Flexible audio clock generation (512KHz to 50MHz input clock)
- Perfect for low power and small form factor designs







## TLV320AIC3111

Low Power / High Performance Audio Codec w/ MiniDSP and Class-D Amplifier

#### **Features**

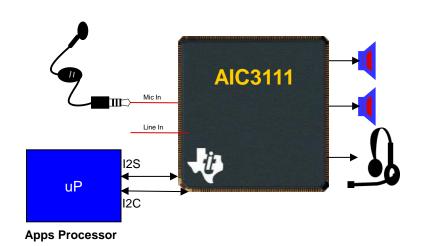
- Stereo 8-192ksps audio DAC up to 95dB SNR
- Mono 8-192ksps audio ADC up to 93dB SNR
- Integrated Stereo Class-D Amplifier
- MiniDSP
- Selectable audio-processing blocks
- Integrated PLL
- Small 5x5mm, 32-pin QFN (RHB)

### **Benefits**

- No external Amp required Single chip solution
- Advanced Audio Algorithms including SRS WOW
- Bass Boost/Treble/EQ
- Voltage measurements for suspend settings
- Flexible audio clock generation (512KHz to 50MHz input clock)
- Perfect for low power and small form factor designs

### **Applications**

Cell Phones
Portable Computing
Tablets / E-Books
Headsets
Portable navigation Devices (PND)



### **PCM3070**

### High Performance Audio Codec w/ Programmable On-Chip Processing

#### **Features**

- Stereo 8-192ksps audio DAC up to 100dB SNR
- Stereo 8-192ksps audio ADC up to 93dB SNR
- Programmable On-Chip Processing
- Selectable audio-processing blocks
- Integrated PLL
- Up to six analog inputs, SE or Differenetial
- Core of TI's Value SoundBar reference design kit

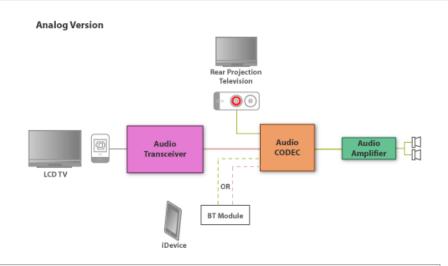
### **Applications**

Soundbars

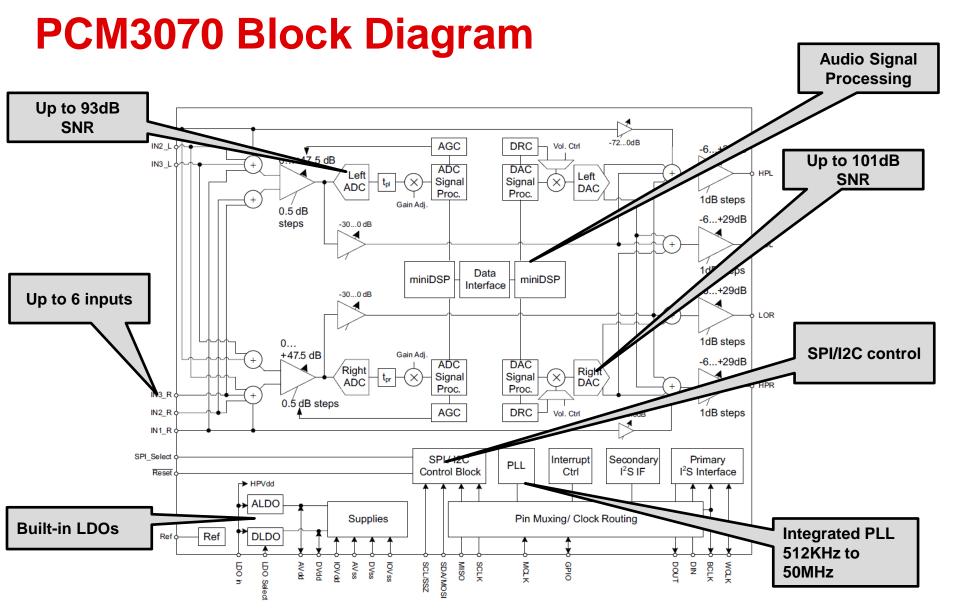
Docking Stations TVs 2.0/2.1 Home Audio Systems

#### **Benefits**

- Advanced Audio Algorithms including SRS WOW Bass Boost/Treble/EQ
- Voltage measurements for suspend settings
- Flexible audio clock generation (512KHz to 50MHz input clock)
- Flexible inputs for analog, SPDIF, or USB
- Ready for production, 30% lower cost when compared to discrete solutions







# R-E-D TODAY!

### Research



www.ti.com/audio - Learn about all our audio products
Parametric Search - Intuitive comparison tool
Audio Selection Tool - Let us help narrow your options
Technical Documents - Datasheets, package info, etc

### **Evaluate**



EVM & Development Kits – Low cost, easy to use

PurePath Software – No Hassle, free software

Training videos – 24/7, anytime, anywhere

End Equipment Block Diagrams – TI Companion devices

### Design



Audio Reference Designs -schematics, gerbers, etc.

Free Samples - Try before you buy (qty limits)

Models & Tools - IBIS, symbols, calculators and more

E2E Forum - Get support from TI experts & community

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DSP **Energy and Lighting** dsp.ti.com www.ti.com/energy Clocks and Timers www.ti.com/clocks Industrial www.ti.com/industrial Interface interface.ti.com Medical www.ti.com/medical logic.ti.com Logic Security www.ti.com/security

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