

ONET1130EC and ONET1130EP EVM User's Guide

ABSTRACT

This document describes the main features and functionality of the evaluation module (EVM) board for the part numbers ONET1130EC and ONET1130EP.

The ONET1130EC and ONET1130EP are 11.7Gbps transceivers with integrated limiting amplifier and modulator driver. The ONET1130EC includes dual CDRs and the ONET1130EP does not.

The EVM can be used to evaluate the performance of the parts in conjunction with an electroabsorptive modulated laser (EML) transmit optical subassembly (TOSA) and a receive optical subassembly (ROSA) in standard XMD compliant packages.

The EVM is controlled with a graphical user interface (GUI).

Preliminary

Document History

Version	Date	Author	Notes
1.0	May 2015	A. Davidson	First release
1.1	June 2015	A. Davidson	Updated GUI Screenshots

Preliminary

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1. Hardware and Equipment

The following hardware and equipment are required to evaluate the EVM.

- 1) An ONET1130EC/EP EVM
- 2) An XMD compliant EML TOSA
- 3) An XMD compliant PIN or APD ROSA
- 4) A USB Dongle
- 5) A USB cable with standard-A to Mini-B connector
- 6) A PC with USB A port
- 7) RF cables with SMA connectors.
- 8) An external thermoelectric controller.
- 9) Single-mode fiber patch cords
- 10) Electrical cables with banana jack connections

2. EVM Block Diagram

Figure 1 represents the block diagram of the ONET1130EC/EP EVM. The board is designed to be powered from a 2.5V supply using cables with banana jacks.

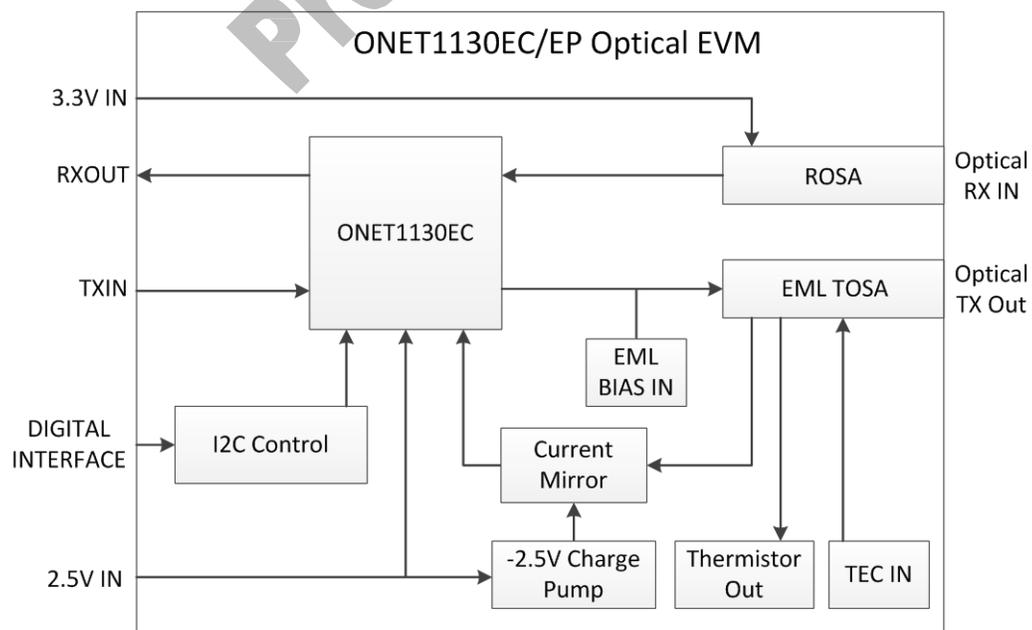


Figure 1. ONET1130EC/EP EVM Block Diagram

3. EVM Connections

Figure 2 shows the location of the major inputs and outputs on the EVM board.

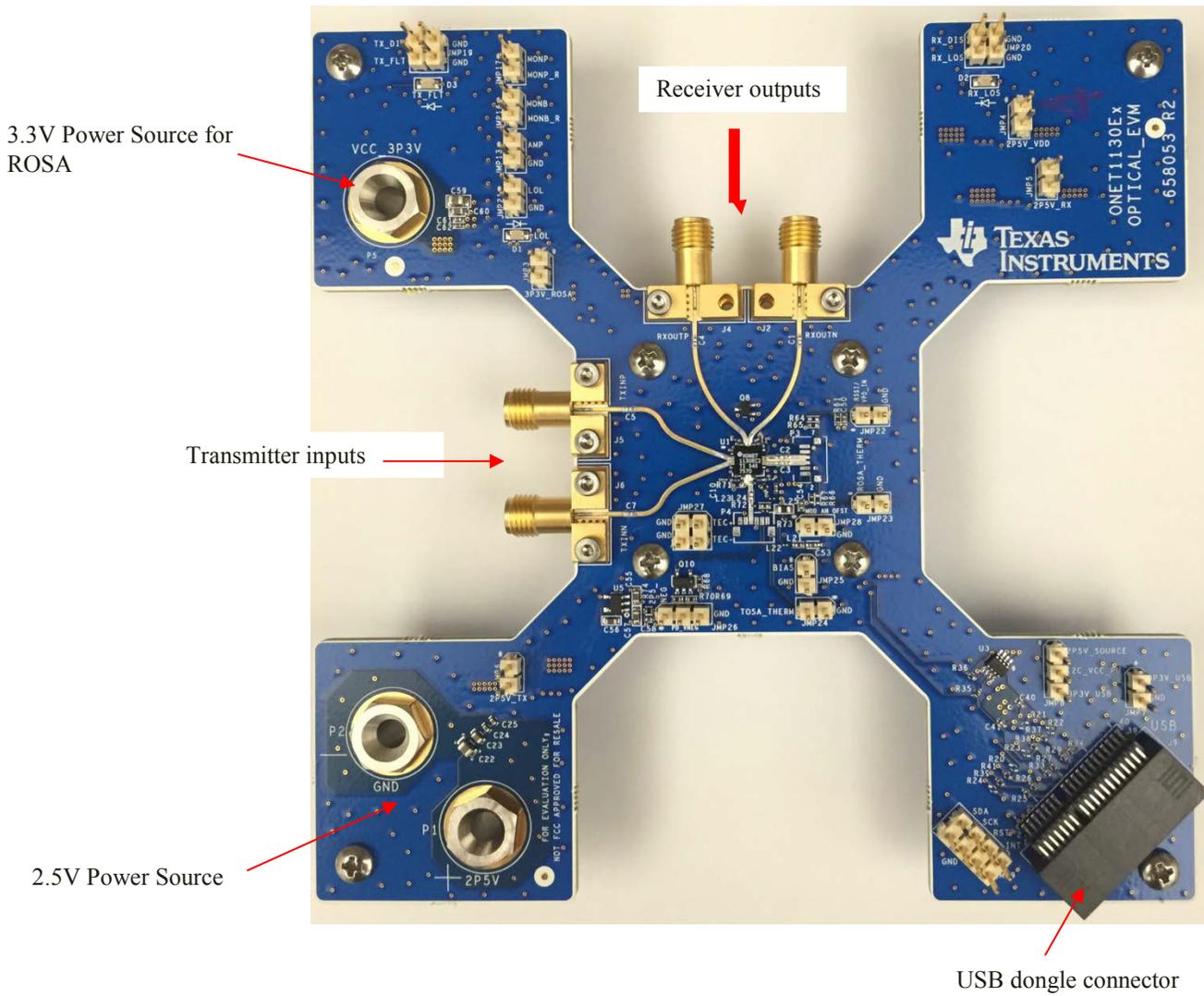


Figure 2. ONET1130EC/EP EVM Connections

A ROSA and TOSA are to be supplied by the customer. In addition, connections for the EML bias voltage and TEC controller are also required. Figure 3 shows the locations for the ROSA and TOSA, EML bias voltage, TOSA thermistor and the TEC+ and TEC- connections to the TEC controller.

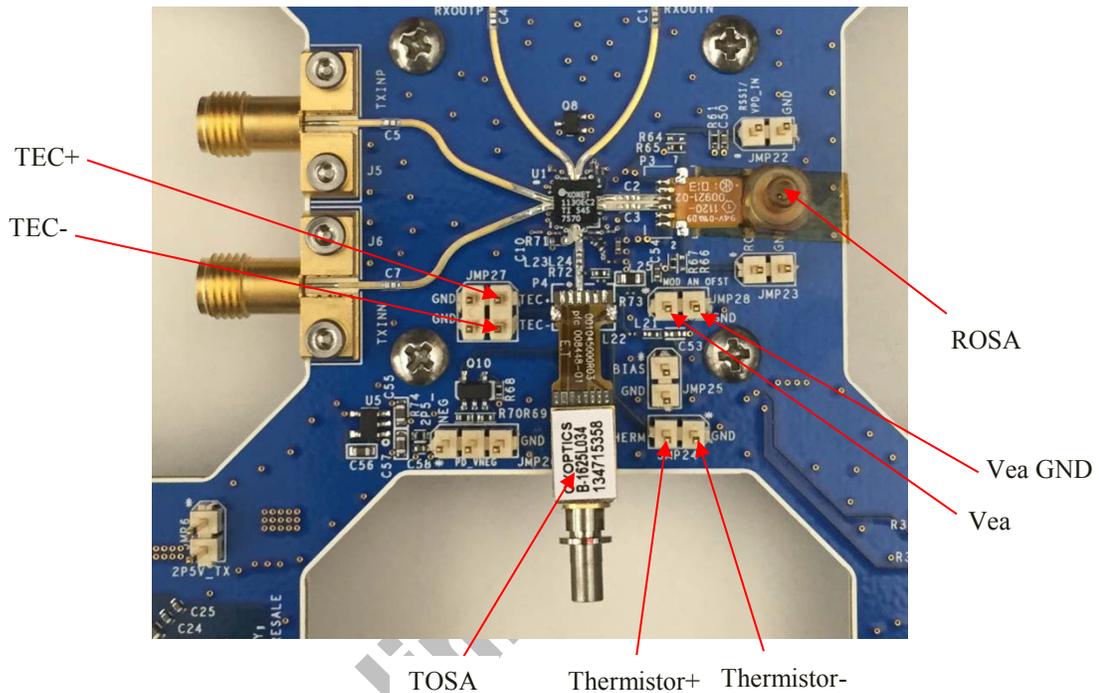


Figure 3. ROSA, TOSA and EML Connections for TEC Controller

Figure 4 shows the main connections except those for the EML bias and TEC controller.

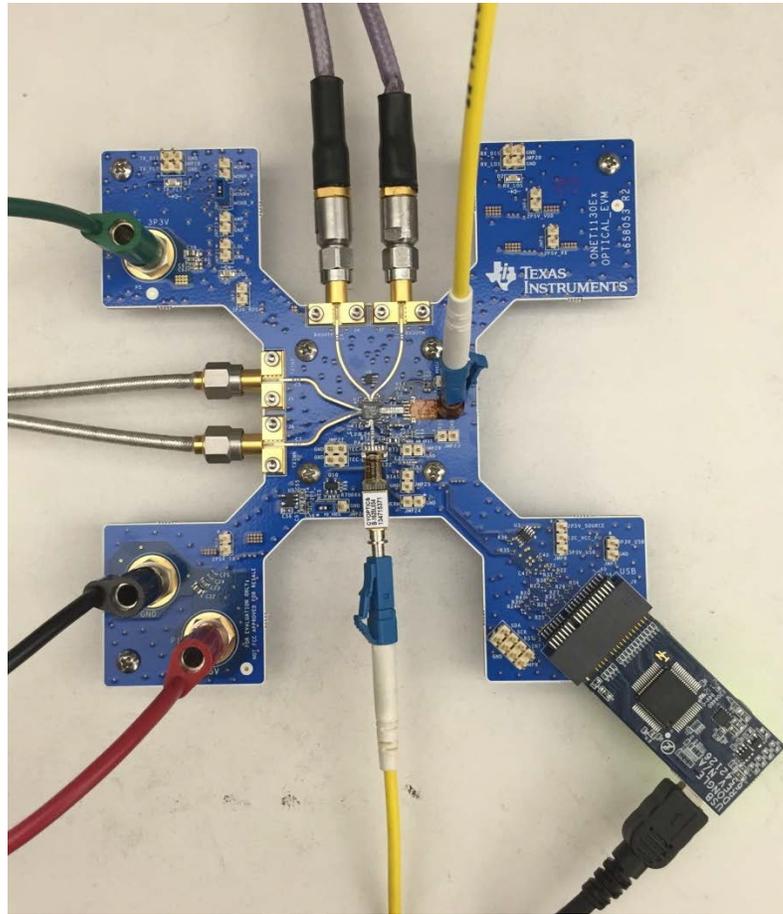


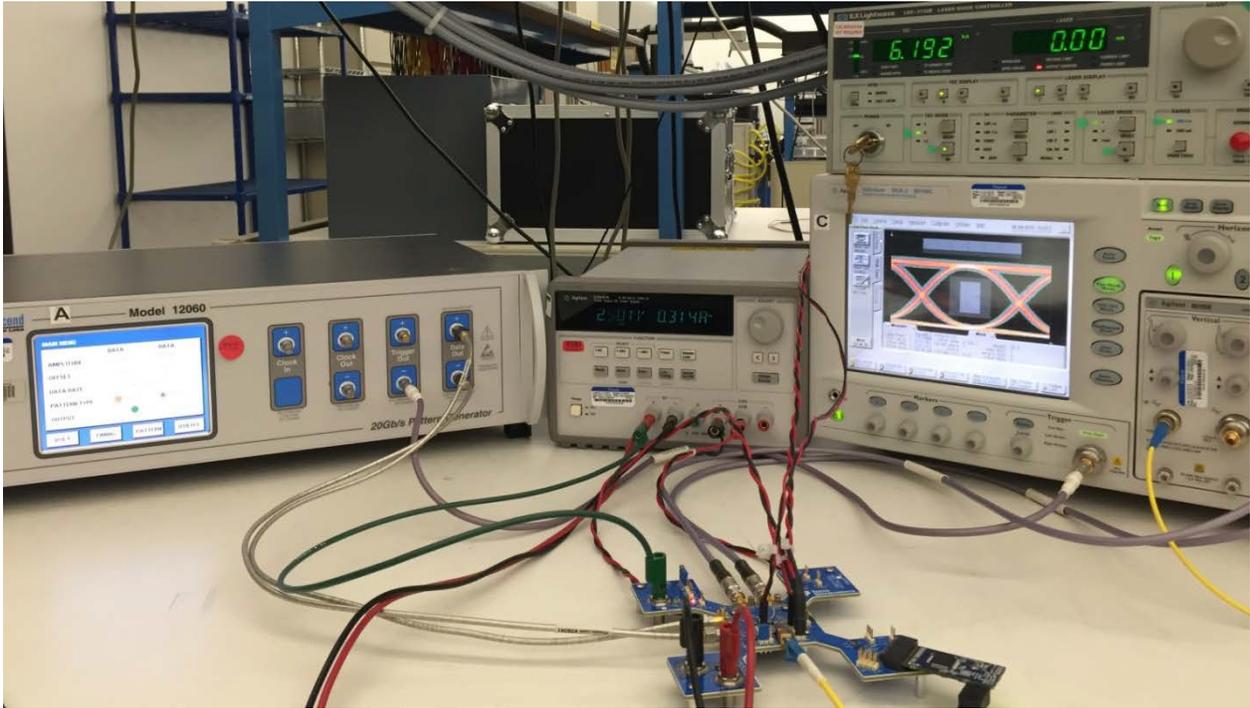
Figure 4. Main EVM Connections

4. Measurement Setup

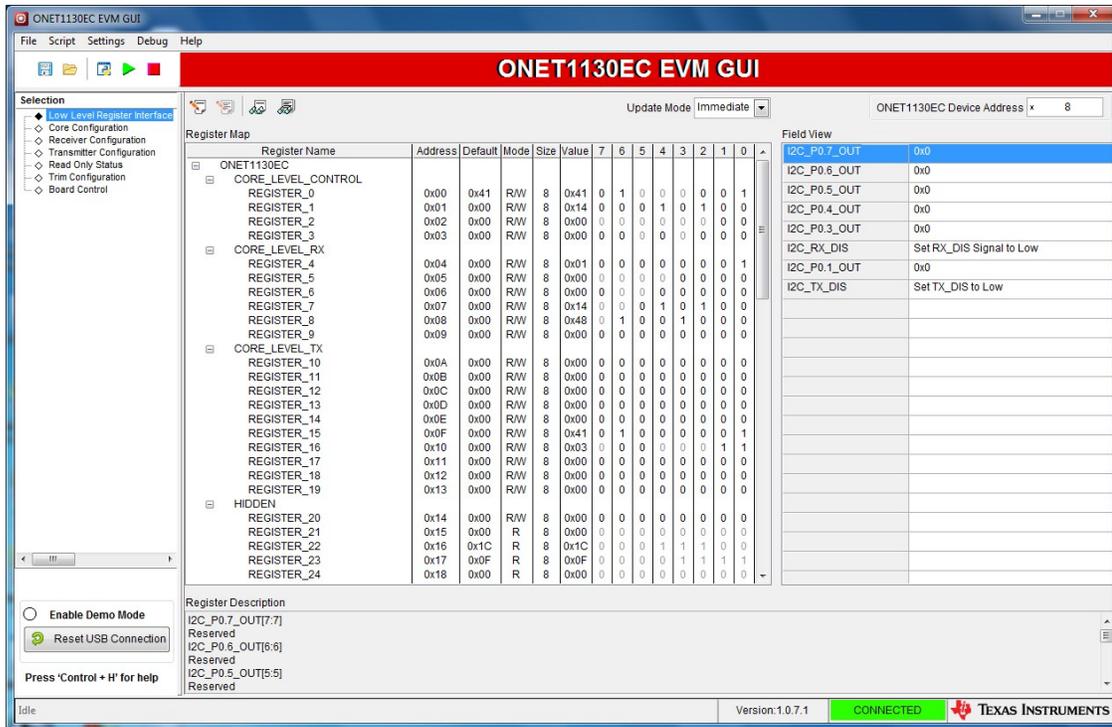
Follow this procedure to quickly setup for measurements.

1. Install the ONET1130EC GUI onto the computer and follow the installation instructions.
2. Solder the ROSA and TOSA onto the EVM.
3. Connect a differential data input signal source to the SMA connectors TXINP/TXINN through 50 Ω matched impedance cables. Set the data rate between 9.8Gbps and 11.7Gbps. Set the amplitude between 100mVp-p differential and 1000mVp-p differential.
4. Connect the output SMA connectors RXOUP/RXOUTN through 50 Ω matched impedance cables to a digital communications analyzer (DCA).
5. Connect the USB dongle to the EVM and attach the interface cable from the USB port of the computer to the mini-B USB port of the dongle.
6. Apply a +2.5V supply to the +2.5V banana jack and the supply ground to the GND banana jack.

7. Apply a negative bias voltage in the range of -0.4V to -1.5V for the EML to JMP28. The required voltage is TOSA dependent.
8. Connect the TOSA thermistor at JMP24 to the TEC controller.
9. Connect the TEC+ and TEC- at JMP27 to the TEC controller.
10. Connect the TOSA to the optical input of a DCA through a single-mode fiber patch cord.
11. Connect an optical input source to the ROSA through a single-mode fiber patch cord.
12. A typical setup will be as shown below

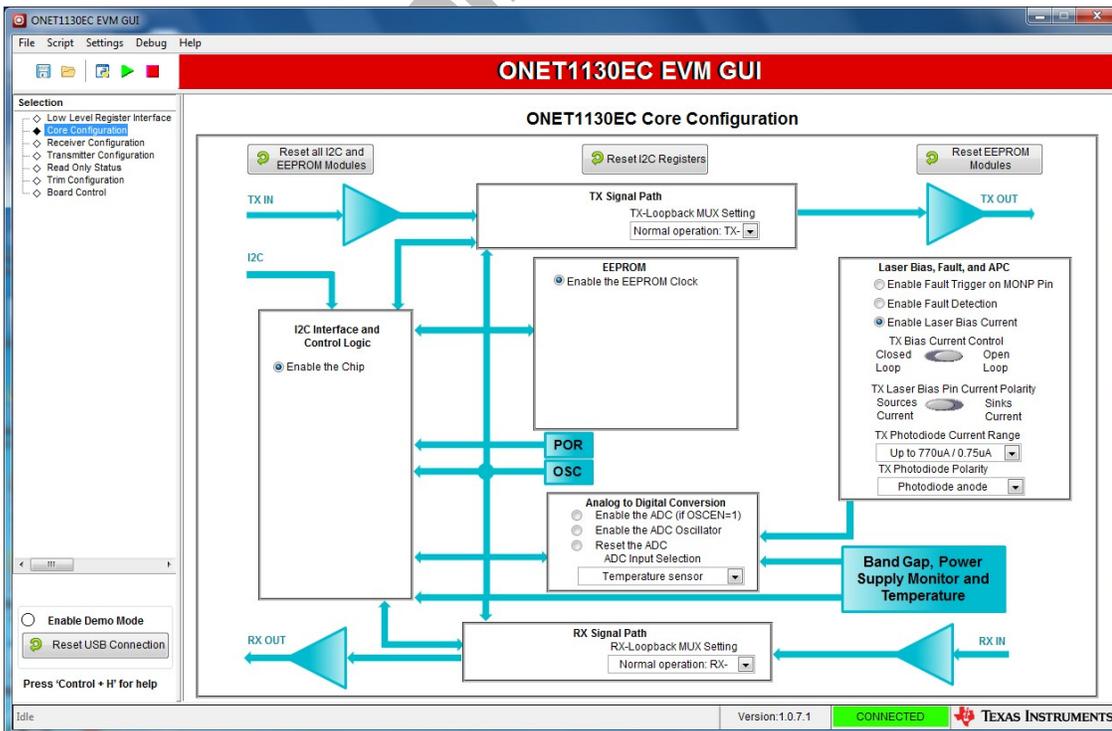


13. Run the GUI by clicking the ONET1130EC GUI shortcut icon on the desktop. The below Low Level Register Interface page will appear.

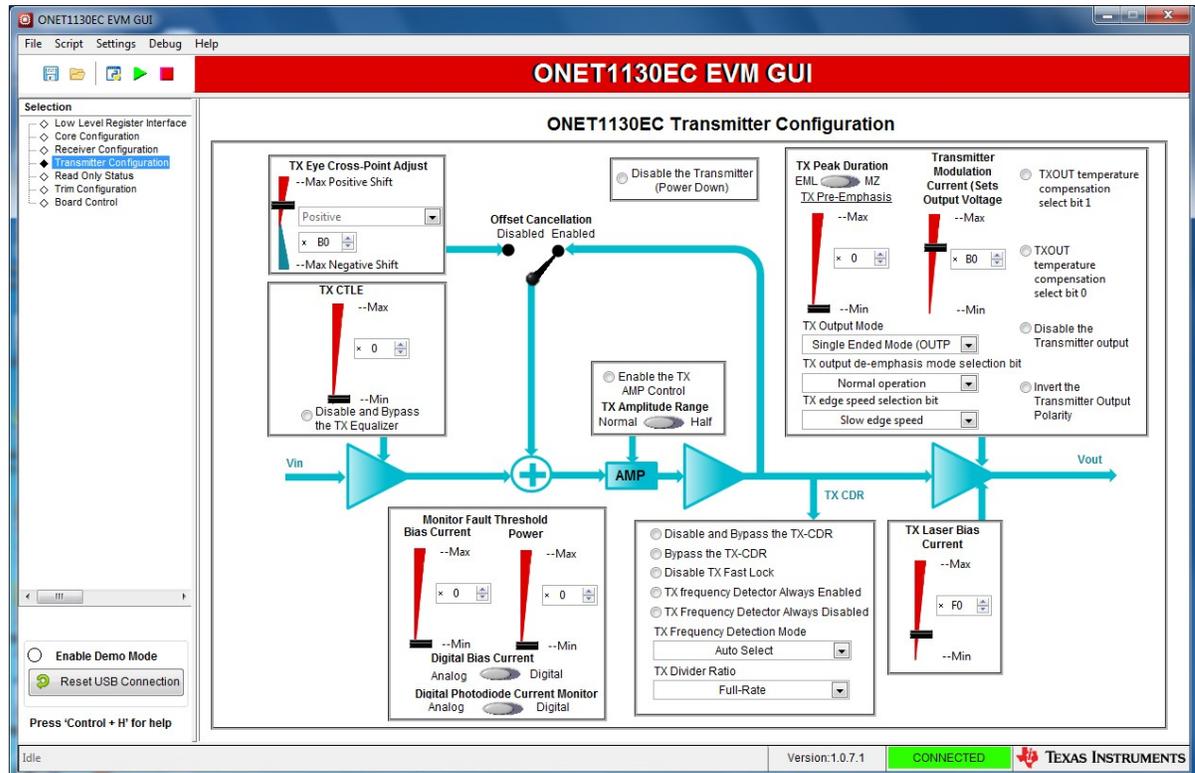


Open Loop Operation without Fault Detection or Digital Monitoring

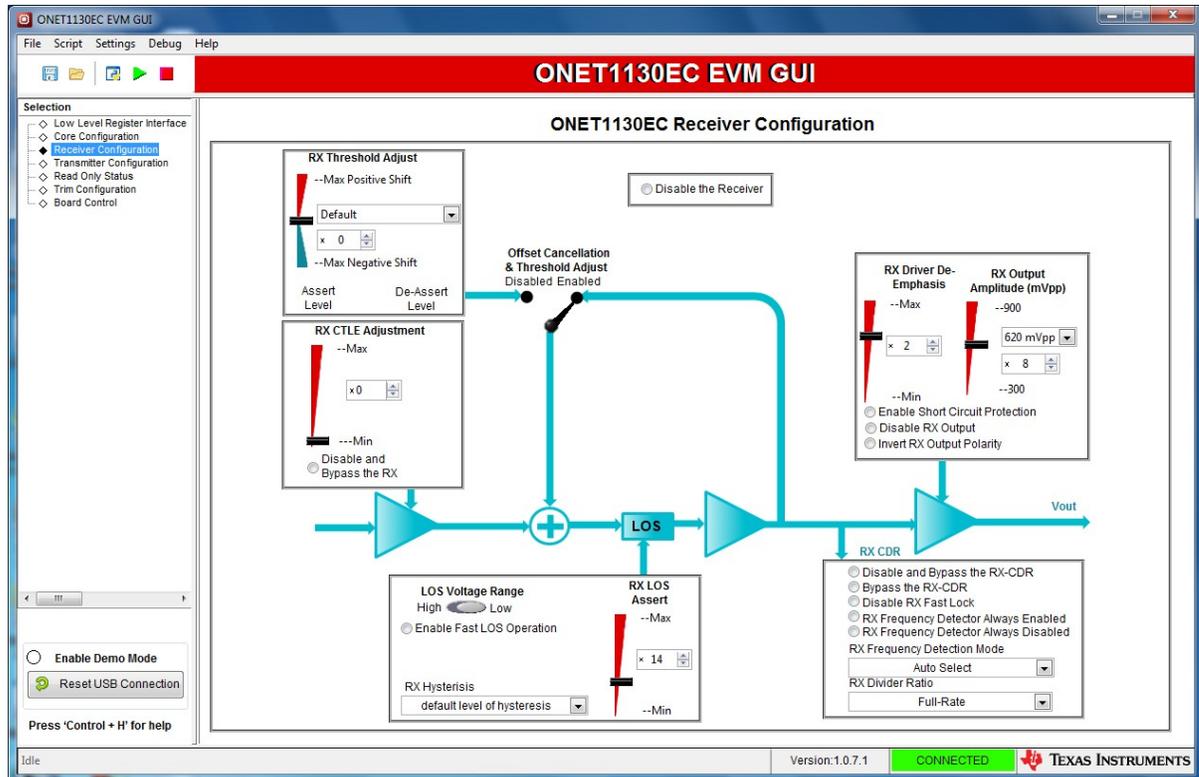
1. It is recommended to initially operate the TOSA in open loop mode with fault detection disabled.
2. Go to the Core Configuration page as shown below.



3. Set the TX Bias Current control to Open Loop.
4. Enable the Laser Bias Current.
5. Go to the Transmitter Configuration page as shown below.



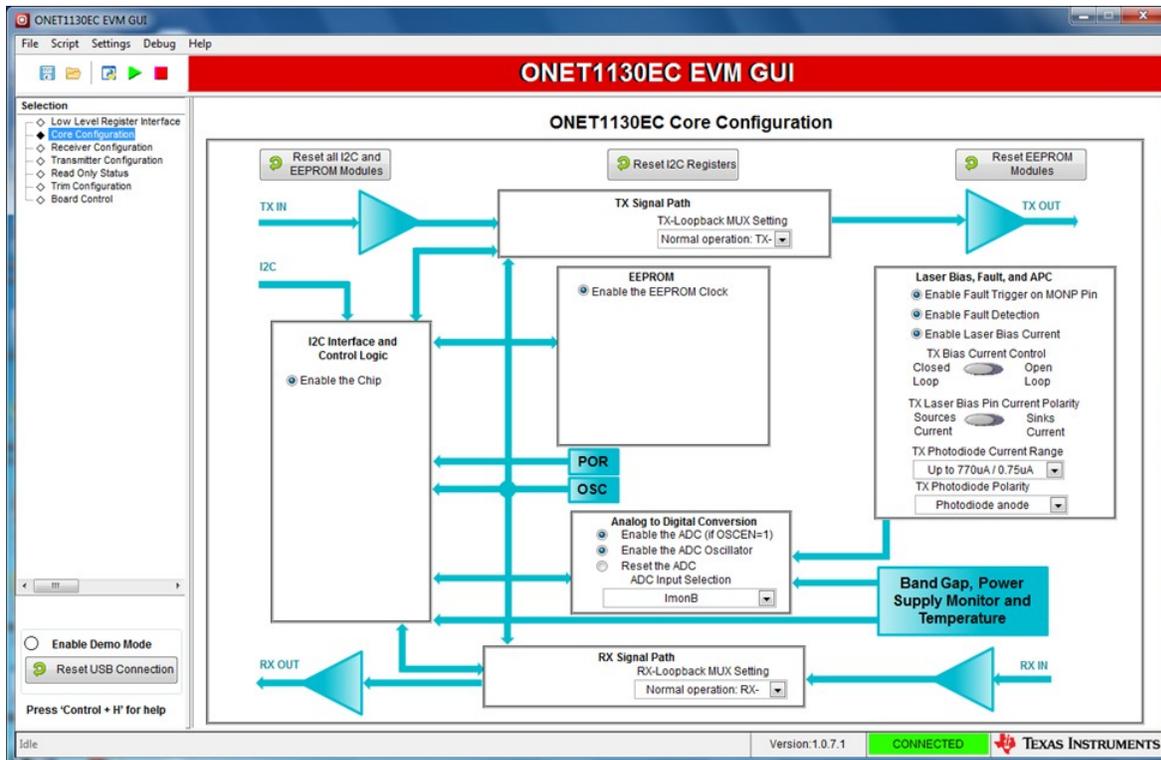
6. Set TX Output Mode to Single Ended Mode (OUTP) using the pull-down box.
7. Set the desired TX Laser Bias Current with the slider.
8. Set the desired Transmitter Modulation Current with the slider.
9. Adjust the TX Cross Point with the slider.
10. If the transmitter modulation current is less than approximately 0xC0 then it is recommended to use the slow edge speed mode as selected with the TX edge speed selection bit. However, the need for using this mode is TOSA dependent.
11. Disable and Bypass the TX-CDR for ONET1130EP evaluation.
12. Go to the Receiver Configuration page as shown below.



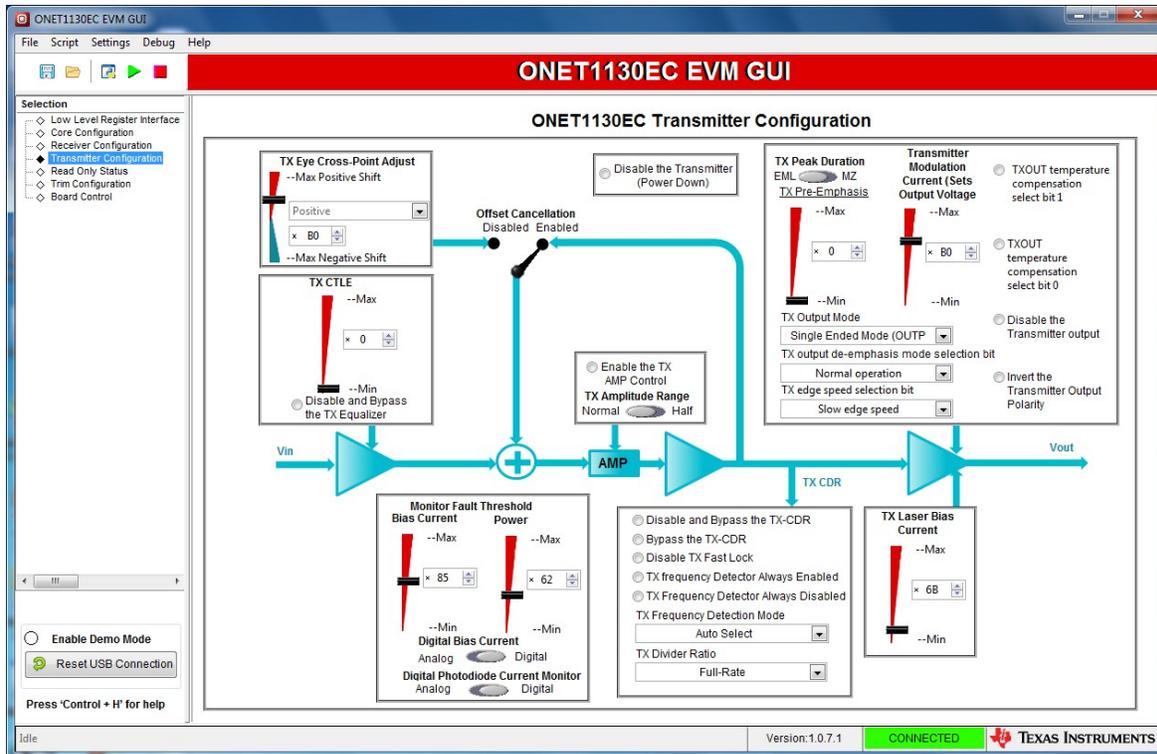
13. Set the Desired RX Output Amplitude.
14. Set the Desired De-Emphasis (2 is recommended).
15. Disable and Bypass the RX-CDR for ONET1130EP evaluation.

Closed Loop Operation with Fault Detection and Digital Monitoring

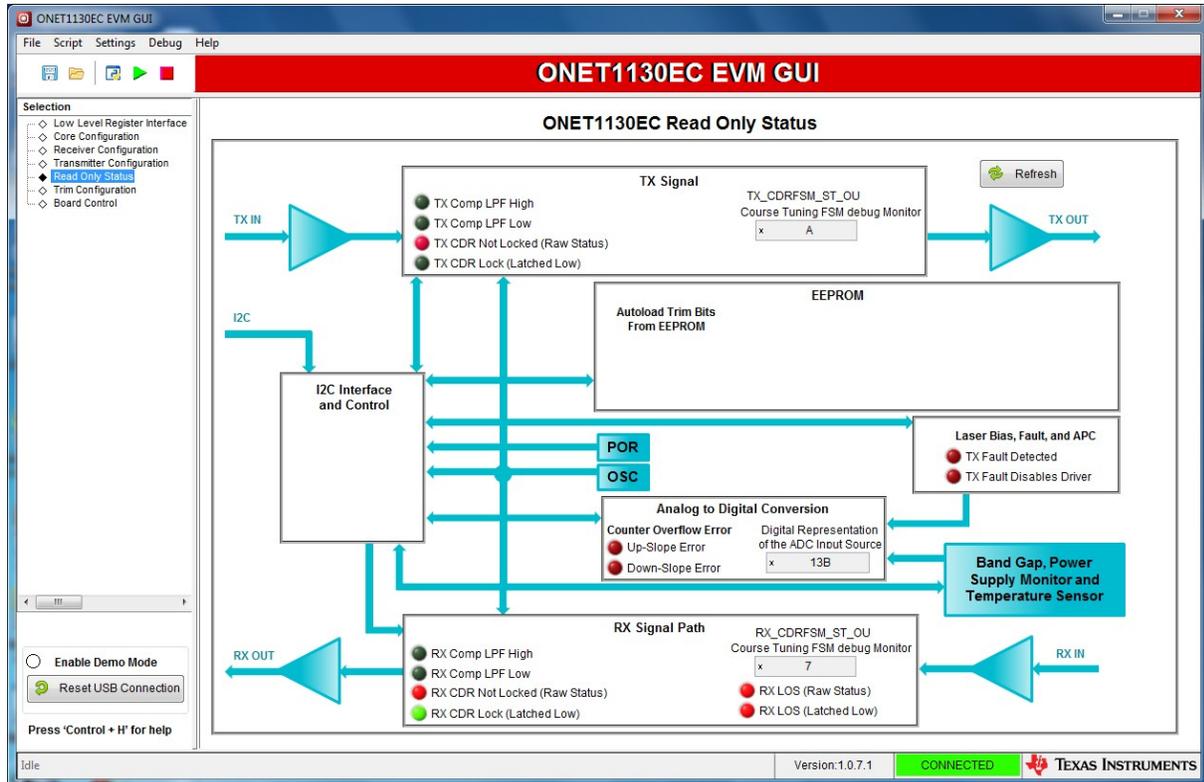
1. To configure the part for automatic power control with digital monitoring and fault detection, place a jumper between pins 1 and 2 of JMP26 to supply -2.5V to the photodiode current mirror.
2. Go to the Core Configuration page as shown below.



3. Disable the laser bias current to prevent a fault from occurring when the Fault Detection is enabled.
4. Enable the Fault Detection and enable the Fault Trigger on MONP Pin.
5. Set the TX Bias Current Control to Closed Loop and initially use the default 770uA photodiode current range.
6. Enable the laser bias current.
7. In the Analog to Digital Conversion box, enable the ADC and ADC oscillator and select the desired parameter to be monitored using the drop down box.
8. Go to the Transmitter Configuration page.



9. In the Monitor Fault Threshold box, set the Bias Current and Power sliders to their maximum value.
10. Select Digital Bias Current and Digital Photodiode current in the Monitor Fault Threshold box.
11. Set the TX Output Mode to Single-Ended (OUTP).
12. Set the desired TX laser bias current, transmitter modulation current and TX cross point.
13. In the Monitor Fault Threshold box, reduce the Bias Current fault threshold slider until a fault occurs. Increase the Bias Current fault threshold level at least by a value of 16.
14. Go to the Core Configuration page and toggle the Enable Laser Bias Current bit. This will disable the fault and restore the transmitter output.
15. In the Monitor Fault Threshold box, reduce the Power fault threshold slider until a fault occurs. Increase the Power fault threshold level at least by a value of 16.
16. Go to the Core Configuration page and toggle the Enable Laser Bias Current bit. This will disable the fault and restore the transmitter output.
17. To read the value from the ADC, go to the Read Only Status page as shown below.



18. Hit the Refresh button and read the value of the Output Representation of the ADC Input Source.

5. LED Indicators

Table 1 shows the meaning of the 2 LEDs on the EVM.

Table 1. LED indicators

LED	LED Color	Default state	Indication when On
D1	Red	On	The transmit CDR or the receive CDR has indicated Loss of Lock (LOL).
D2	Red	On	The receiver input signal level is set below the Loss of Signal (LOS) threshold.

6. Typical Performance Results

A typical set of conditions for operating the EVM are as follows:

- VCC = 2.5V, ICC = 300mA with ROSA
- 10.71Gbps, PRBS31 Pattern
- Transmitter Single-Ended Mode of Operation
- TOSA Temperature = 37°C
- Ve_a = -0.65V
- Laser Bias Current = 0xF0 (~53mA)
- Transmitter Modulation Current = 0xB0
- Transmitter Cross Point = 0xB0
- TX and RX CDRs Enabled
- Receiver amplitude set to 600mVpp
- Receiver de-emphasis set to 0x02

A typical unfiltered eye diagram is shown in Figure 5.

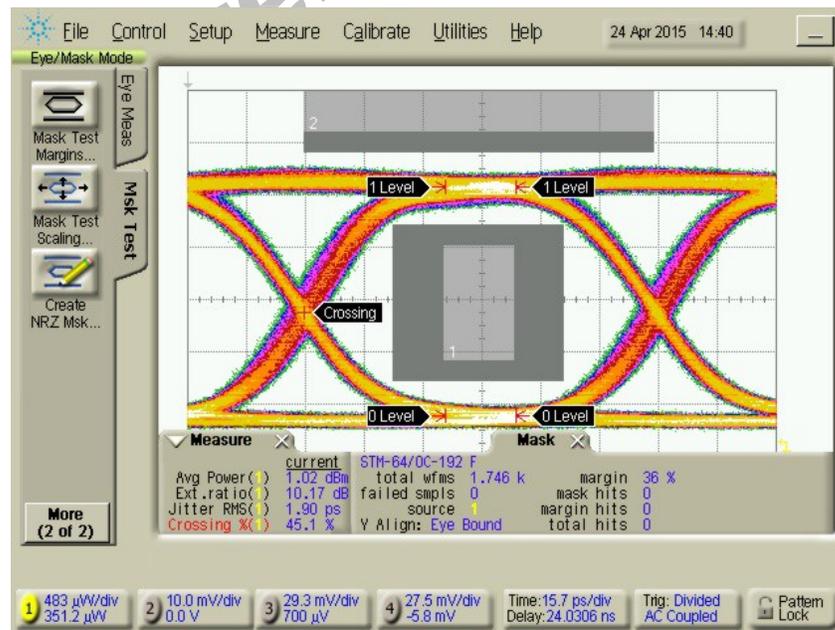


Figure 5. Unfiltered Transmitter Eye Diagram

A typical filtered eye diagram is shown in Figure 6.

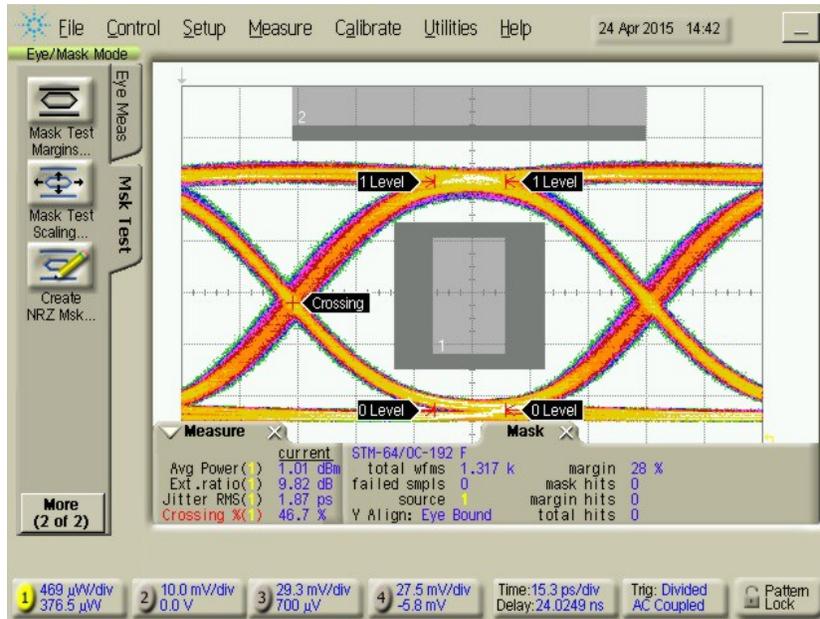


Figure 6. Filtered Transmitter Eye Diagram

A typical receiver eye diagram with the CDR enabled and -20dBm average optical input power to a PIN ROSA is shown in Figure 7.

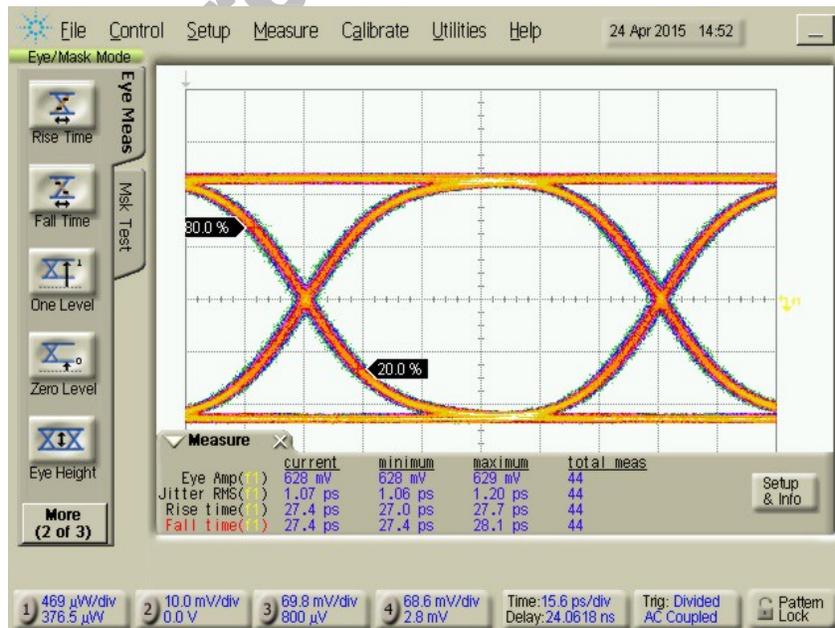


Figure 7. Receiver Eye Diagram with CDR Enabled

A typical receiver eye diagram with the CDR disabled and -20dBm average optical input power to a PIN ROSA is shown in Figure 8.

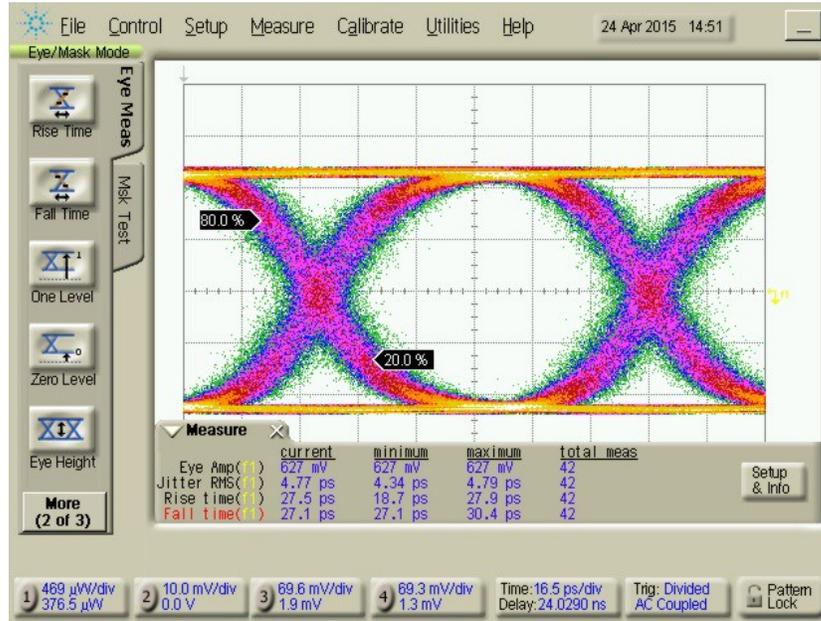
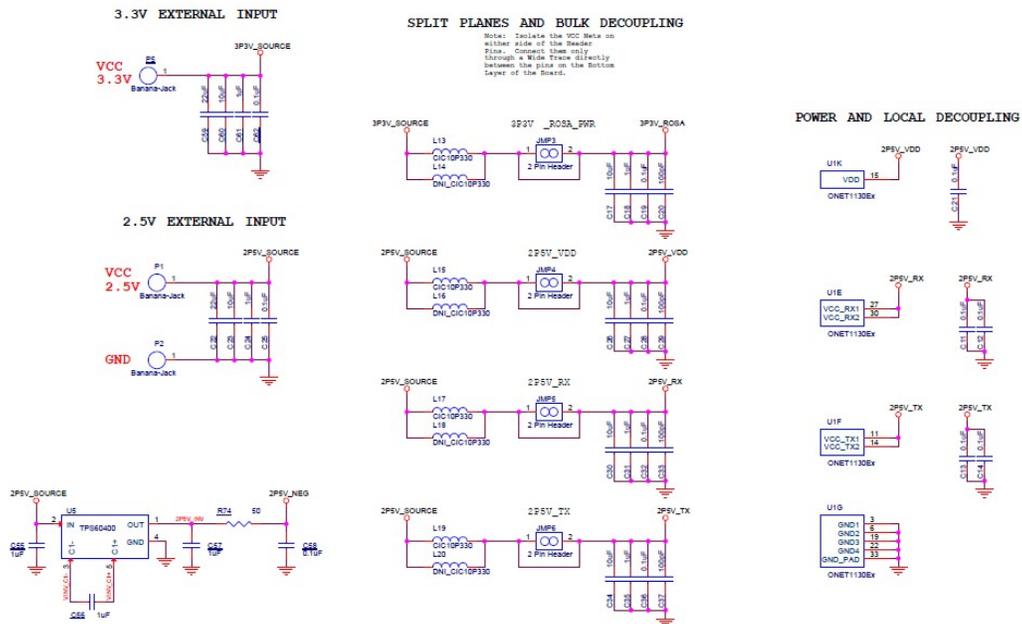


Figure 8. Receiver Eye Diagram with CDR Disabled

7. Schematics and Bill of Materials



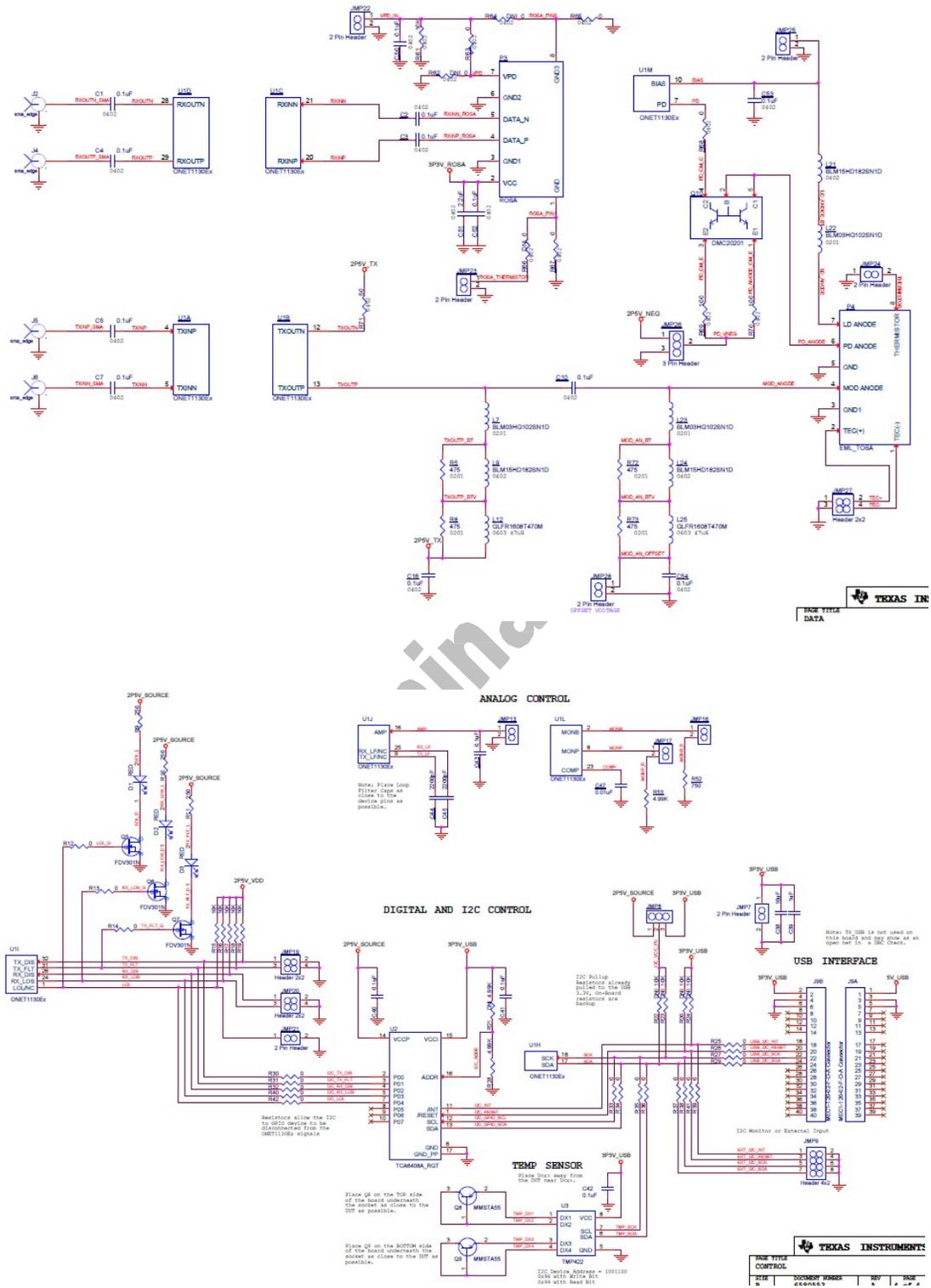


Figure 9. Schematics

Item	Quantity	Reference	Value	Part Number	Manufacturer	PCB Footprint
1	2	C44, C45	2200pF	Any (10V+ Rating)	Any	0201
2	1	C47	0.01uF	Any (10V+ Rating)	Any	0201
3	5	C11, C12, C13, C14, C21	0.1uF	Any (10V+ Rating)	Any	0201
4	4	C20, C29, C33, C37	100pF	Any (10V+ Rating)	Any	0402
5	7	C1, C2, C3, C4, C5, C7, C10	0.1uF	ATC545L104KT10T	ATC	0402
6	16	C16, C19, C25, C28, C32, C36, C40, C41, C42, C43, C50, C52, C53, C54, C58, C62	0.1uF	Any (10V+ Rating)	Any	0402
7	7	C18, C24, C27, C31, C35, C39, C61	1uF	Any (10V+ Rating)	Any	0402
8	1	C51	2.2uF	Any (10V+ Rating)	Any	0402
9	3	C55, C56, C57	1uF	Any (10V+ Rating)	Any	0603
10	7	C17, C23, C26, C30, C34, C38, C60	10uF	Any (10V+ Rating)	Any	0603
11	2	C22, C59	22uF	Any (10V+ Rating)	Any	0805
12	4	R5, R8, R72, R73	475	Any (1% Tolerance)	Any	0201
13	24	R12, R13, R14, R25, R26, R27, R29, R30, R31, R32, R33, R34, R35, R36, R37, R38, R39, R40, R41, R42, R63, R65, R67, R68	0	Any (1% Tolerance)	Any	0402
14	3	R62, R64, R66	DNI_0	Any (1% Tolerance)	Any	0402
15	2	R71, R74	50	Any (1% Tolerance)	Any	0402
16	2	R69, R70	100	Any (1% Tolerance)	Any	0402
17	3	R9, R10, R11	250	Any (1% Tolerance)	Any	0402
18	1	R28	4.99K	Any (1% Tolerance)	Any	0402
19	1	R21	DNI_4.99K	Any (1% Tolerance)	Any	0402
20	6	R15, R16, R17, R18, R19, R61	10K	Any (1% Tolerance)	Any	0402
21	4	R20, R22, R23, R24	DNI_10K	Any (1% Tolerance)	Any	0402
22	1	R52	750	Any (1% Tolerance)	Any	0805
23	1	R53	4.99K	Any (1% Tolerance)	Any	0805
24	3	D1, D2, D3	RED	HSMS-C170	Avago	LED_SM_HSMB_C170
25	14	JMP3, JMP4, JMP5, JMP6, JMP7, JMP13, JMP16, JMP17, JMP21, JMP22, JMP23, JMP24, JMP25, JMP28	0.1" Header (1x2)	HTSW-150-08-G-S	Samtec	HDR_THVT_1x2_100
26	2	JMP8, JMP26	0.1" Header (1x3)	HTSW-150-08-G-S	Samtec	HDR_THVT_1x3_100
27	3	JMP19, JMP20, JMP27	0.1x0.1" Header (2x2)	HTSW-150-08-G-D	Samtec	HDR_THVT_2x2_100
28	1	JMP9	0.1x0.1" Header (2x4)	HTSW-150-08-G-D	Samtec	HDR_THVT_2x4_100
29	4	J2, J4, J5, J6	Edge Launch SMA	32K243-40M	Rosenberger	CON_02K243-40M

30	1	J9	MEC1-120-02-F-D-A Connector	MEC1-120-02-F-D-RA1-SL	Samtec	CON_SMRT_MEC1_40_F
31	2	L12, L25	Inductor 47uH, 35mA	GLFR1608T470M-LR	Taiyo Yuden	0603
32	3	L9, L21, L24	Ferrite Bead 1.8kohm, 200mA	BLM15HD182SN1D	ATC	0402_2020MIL
33	3	L7, L22, L23	Ferrite Bead 1000ohm, 125mA	BLM03HG102SN1D	MuRata	0201
34	4	L13, L15, L17, L19	CIC10P330	CIC10P330	Samsung	0603
35	4	L14, L16, L18, L20	DNI_CIC10P330	CIC10P330	Samsung	0603
36	3	P1, P2, P5	Solderless Banana Jack	108-0740-001	Emerson	JACK_THVT_BANANA_500dia
37	1	P3	DNI_ROSA	DNI	DNI	MSA_XMD_ROSA_INV
38	1	P4	DNI_EML_TOSA	DNI	DNI	162X_TOSA
39	3	Q5, Q6, Q7	FDV301N	FDV301N	Fairchild	SOT23_3
40	2	Q8, Q9	MMSTA55	MMSTA55-7-F	Diodes Inc.	SOT_323-3
41	1	Q10	DMC20201	DMC20201	Panasonic	SOT23_5
42	1	U1	ONET1130Ex	ONET1130Ex	Texas Instruments	ONET1130Ex_QFN32
43	1	U2	TCA6408A_RGT	TCA6408ARGTR	Texas Instruments	QFN_16_3MSQ
44	1	U3	TMP422	TMP422AIDCNT	Texas Instruments	DCN_SOT23-8
45	1	U5	TPS60400	TPS60400	Texas Instruments	SOT23-5
50	8	Standoff	Round Threaded 0.5"	RST3/16-440X.5-A	Keystone Electronics	
51	8	Screws	Round 4-40/0.25"	PMSSS 440 0025 PH	Building Fasteners	

Table 2. Bill of Materials

STANDARD TERMS AND CONDITIONS FOR EVALUATION MODULES

1. *Delivery:* TI delivers TI evaluation boards, kits, or modules, including any accompanying demonstration software, components, or documentation (collectively, an "EVM" or "EVMs") to the User ("User") in accordance with the terms and conditions set forth herein. Acceptance of the EVM is expressly subject to the following terms and conditions.
 - 1.1 EVMs are intended solely for product or software developers for use in a research and development setting to facilitate feasibility evaluation, experimentation, or scientific analysis of TI semiconductor products. EVMs have no direct function and are not finished products. EVMs shall not be directly or indirectly assembled as a part or subassembly in any finished product. For clarification, any software or software tools provided with the EVM ("Software") shall not be subject to the terms and conditions set forth herein but rather shall be subject to the applicable terms and conditions that accompany such Software
 - 1.2 EVMs are not intended for consumer or household use. EVMs may not be sold, sublicensed, leased, rented, loaned, assigned, or otherwise distributed for commercial purposes by Users, in whole or in part, or used in any finished product or production system.
2. *Limited Warranty and Related Remedies/Disclaimers:*
 - 2.1 These terms and conditions do not apply to Software. The warranty, if any, for Software is covered in the applicable Software License Agreement.
 - 2.2 TI warrants that the TI EVM will conform to TI's published specifications for ninety (90) days after the date TI delivers such EVM to User. Notwithstanding the foregoing, TI shall not be liable for any defects that are caused by neglect, misuse or mistreatment by an entity other than TI, including improper installation or testing, or for any EVMs that have been altered or modified in any way by an entity other than TI. Moreover, TI shall not be liable for any defects that result from User's design, specifications or instructions for such EVMs. Testing and other quality control techniques are used to the extent TI deems necessary or as mandated by government requirements. TI does not test all parameters of each EVM.
 - 2.3 If any EVM fails to conform to the warranty set forth above, TI's sole liability shall be at its option to repair or replace such EVM, or credit User's account for such EVM. TI's liability under this warranty shall be limited to EVMs that are returned during the warranty period to the address designated by TI and that are determined by TI not to conform to such warranty. If TI elects to repair or replace such EVM, TI shall have a reasonable time to repair such EVM or provide replacements. Repaired EVMs shall be warranted for the remainder of the original warranty period. Replaced EVMs shall be warranted for a new full ninety (90) day warranty period.
3. *Regulatory Notices:*
 - 3.1 *United States*
 - 3.1.1 *Notice applicable to EVMs not FCC-Approved:*

This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.
 - 3.1.2 *For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:*

CAUTION

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

FCC Interference Statement for Class A EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

FCC Interference Statement for Class B EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

3.2 Canada

3.2.1 For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210

Concerning EVMs Including Radio Transmitters:

This device complies with Industry Canada license-exempt RSS standard(s). Operation is subject to the following two conditions: (1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concernant les EVMs avec appareils radio:

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Concerning EVMs Including Detachable Antennas:

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur.

3.3 Japan

3.3.1 *Notice for EVMs delivered in Japan:* Please see http://www.tij.co.jp/lstds/ti_ja/general/eStore/notice_01.page 日本国内に輸入される評価用キット、ボードについては、次のところをご覧ください。
http://www.tij.co.jp/lstds/ti_ja/general/eStore/notice_01.page

3.3.2 *Notice for Users of EVMs Considered "Radio Frequency Products" in Japan:* EVMs entering Japan may not be certified by TI as conforming to Technical Regulations of Radio Law of Japan.

If User uses EVMs in Japan, not certified to Technical Regulations of Radio Law of Japan, User is required by Radio Law of Japan to follow the instructions below with respect to EVMs:

1. Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

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