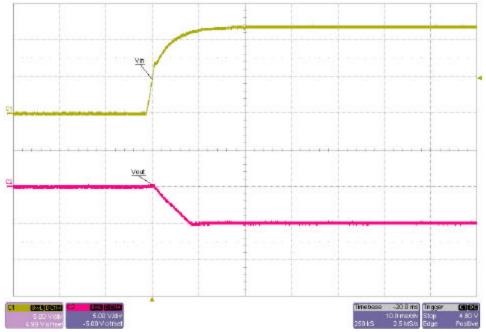


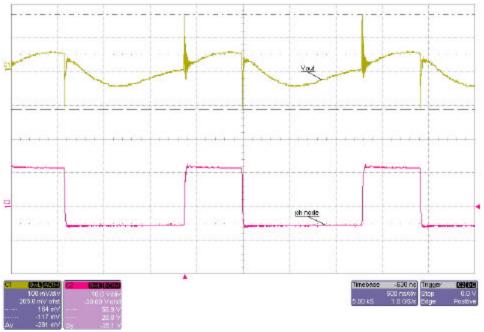
#### 1 Startup

The photo below shows the startup waveform for the -5 V output after the application of 12 Vdc in. The output was loaded to 0A.



#### 2 Output Ripple Voltage

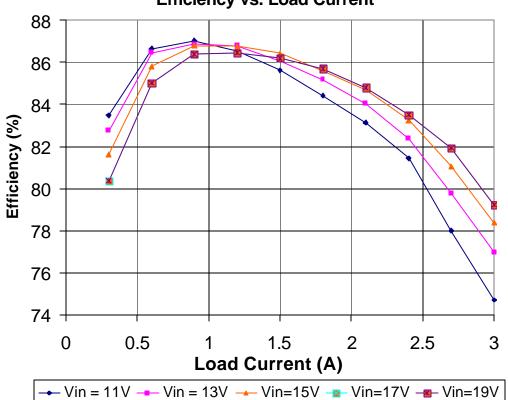
The -5 V output ripple voltage is shown in the figure below. The image was taken with the output loaded to 2A and the input voltage set to 12Vdc. (100mV/DIV for ripple voltage) Switch node was monitored for additional reference.





# 3 Efficiency

The converter efficiency is shown in the figure below.

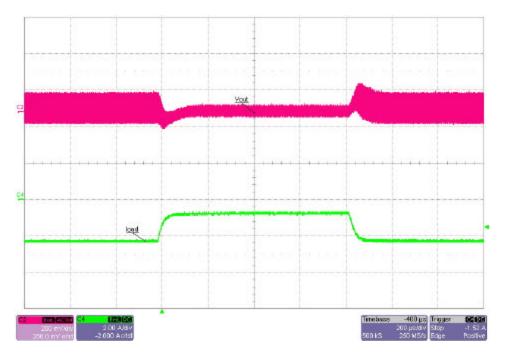


#### Efficiency vs. Load Current



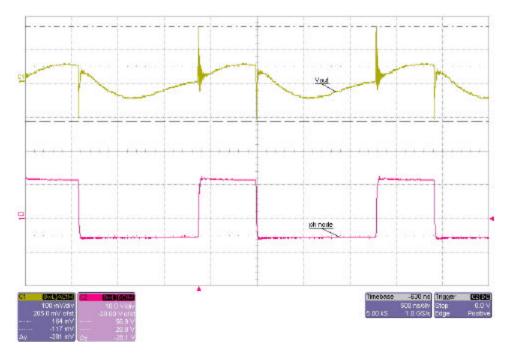
## 4 Load Transients

The photo below shows the AC coupled -5 V output voltage waveform when the load current is pulsed from 75% to 25% load and back (-2.25A to -0.75A). Vin = 12 Vdc.



### 5 Switch Node Waveform

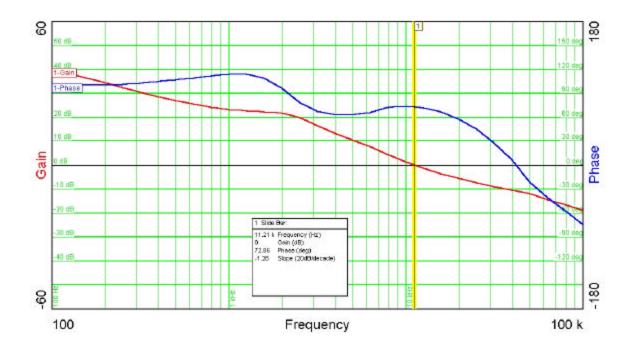
The photo below shows the switch node waveform. The input voltage is 12V and the output is loaded to 2A.





# 6 Control Loop Gain / Stability

The plot below shows the loop gain and phase margin for the -5V output with 2A load. The input voltage was set to 12 V.



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