



Additional input capacitance may be needed depending on U1 and U3 proximity to the input supply.

Additional bulk and bypass capacitance on each rail will be required. See DSP datasheet.

Although not used in this design, two 50mA LDO's and one 20mA LDO/Switch for RTC are available for use.

/LOWBAT and PWRFAIL comparators monitor the 1.2V and 3.3V rails and are tied to HOT\_RST to generate POR. Also they are used to give 1.2V, 3.3V then 1.8V sequencing.

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