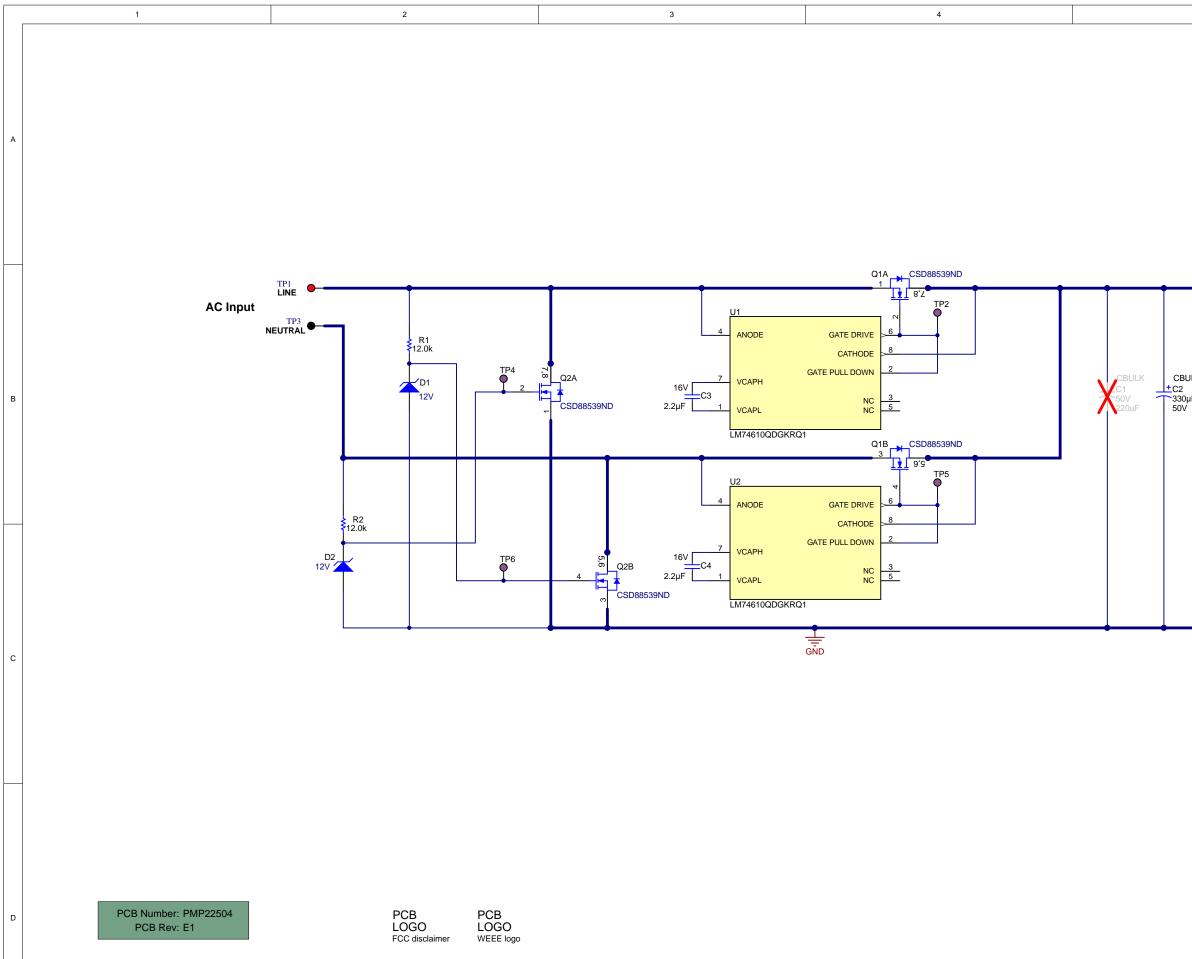
	1	2	3	4		5	6	3
							Revision History	
						Rev ECN # Approved Date	Approved by	Notes
						N/A N/A N/A	N/A	N/A
A								A
в		LM74610-Q1	Q1 LM746		CBULK	RLOAD		В
С		B			↓ ↓	↓ ↓		с
D	1	Texas Instruments and/or its licensors warrant that this design will meet the s licensors do not warrant that the desig 2	do not warrant the accuracy or completeness of this spec pecifications, will be suitable for your application or fit for n is production worthy. You should completely validate an 3	ification or any information contained therein. Texas Instruments any particular purpose, or will operate in an implementation. Texa d test your design implementation to confirm the system function. 4	Orderable: Change TID #: N/A Number: PMP2250 and/or its licensors do not as Instruments and/or its ality for your application. Engineer: Mike Gilt	Me in variant Designed for: Public Re   Project Title: Ideal Diod   4 Rev: E1   Sembly Variant: 001   File: CoverSheet.SchDc   pert Contact: http://www.ti.co   5	ease  Mod. Date: 8/10/2020 e Full Bridge Rectifier c Sheet: 1 of 2 c Size: B pm/support 6	© Texas Instruments 2020





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