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A

A

B

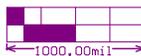
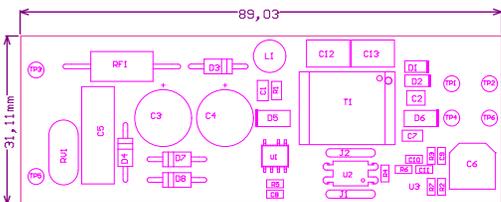
B

C

C

D

D



COMPONENTS MARKED 'DNP' SHOULD NOT BE POPULATED.
ASSEMBLY VARIANT: [No Variations]

| | | | |
|--------------------------|---------------------|-------------------|--------------------------------|
| PCB VIEWED FROM TOP SIDE | BOARD #: PMP10143 | REV: A | SUN REV: Not In VersionControl |
| PLOT NAME = | 16.07.2015 13:39:26 | TEXAS INSTRUMENTS | |

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| DESIGN INFORMATION | |
|--|---|
| MIN. TRACK WIDTH: | <u>8</u> _ML |
| MIN. CLEARANCE: | <u>0,2</u> mm |
| MIN. VIA PAD SIZE: | <u>24</u> _ML |
| MINIMUM ANNULAR RING 0.05mm (2ML) EXTERNAL PER IPC-D-275 CLASS 2 LEVEL C REGISTRATION TOLERANCES: METAL +/- <u>5</u> _ML, HOLES +/- <u>3</u> _ML | |
| MATERIAL: | |
| <input type="checkbox"/> FR-408 | <input checked="" type="checkbox"/> FR-4 High Tg <input type="checkbox"/> OTHER _____ |
| THICKNESS: | <input checked="" type="checkbox"/> 62 ML (1.6mm) +/-10% <input type="checkbox"/> OTHER _____ |
| TOLERANCE: | <input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2 <input type="checkbox"/> OTHER +/- _____ |
| BOW & TWIST: | <input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2 <input type="checkbox"/> OTHER +/- _____ |
| DRILLING: | |
| REFERENCE: | <input checked="" type="checkbox"/> AS SHOWN <input checked="" type="checkbox"/> NC_DRILL FILES |
| PTH MIN COPPER THICKNESS: | <input checked="" type="checkbox"/> 1ML <input type="checkbox"/> OTHER _____ |
| BOARD FINISH: | |
| SILKSCREEN: | <input checked="" type="checkbox"/> TOP <input checked="" type="checkbox"/> BOTTOM |
| SILKSCREEN COLOR: | <input checked="" type="checkbox"/> WHITE <input type="checkbox"/> OTHER _____ |
| SOLDER RESIST COLOR: | |
| <input checked="" type="checkbox"/> GREEN | <input type="checkbox"/> BLUE <input type="checkbox"/> OTHER _____ |
| SURFACE FINISH: | |
| <input checked="" type="checkbox"/> IMMERSION GOLD (ENIG) | <input type="checkbox"/> ENERPIG |
| <input type="checkbox"/> 1MM. TIN/SILVER OR EQUIV | <input type="checkbox"/> OTHER _____ |
| ARRAY/PANEL: | |
| <input type="checkbox"/> CUT AND TRM PER MECH LAYER 1 | <input checked="" type="checkbox"/> V. SCORE |
| CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBs TO MEET OR EXCEED THE REQUIREMENTS OF: | |
| <input checked="" type="checkbox"/> ANSI IPC-A-600F CLASS -> | <input type="checkbox"/> 1 <input checked="" type="checkbox"/> 2 <input type="checkbox"/> 3 |
| <input checked="" type="checkbox"/> UL 94V-0 | <input checked="" type="checkbox"/> RoHS <input type="checkbox"/> OTHER _PER ORDER |
| ADDITIONAL REQUIREMENTS: | |
| MICROSECTION: | <input type="checkbox"/> YES |
| BARE BOARD ELEC. TEST: | <input type="checkbox"/> NONE <input checked="" type="checkbox"/> REQUIRED <input type="checkbox"/> PER ORDER |
| MANUFACTURER'S UL: | <input type="checkbox"/> RAL <input type="checkbox"/> METAL <input checked="" type="checkbox"/> SILK |



| | |
|---|---|
| PROJECT TITLE: Isolated universal line to 3.3V@0.4A & 12V@30mA | |
| DESIGNED FOR: Public Release | |
| FILE NAME: PMP10143 Rev_A PCB.PcbDoc | |
| ENGINEER: R. Scibilia | LAYOUT BY: R. Scibilia |
| SCALE: 0.72 | ALTIUM DESIGNER VERSION: 14.3.18.45973 |

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