	1	2	3	4	5		6	
			I		Layer Name Ma 1 Top Overlay	aterial Thickness	5 Constant Board Layer Stack	
					2 Top Solder Sol	older Resist 0.40mil opper 1.40mil	3.5	
					4 Dielectric1 FR	?-4 High Tg 59.20mil	4.8	
					6 Bottom Solder So	opper 1.40mil older Resist 0.40mil	3.5	
A					7 Bottom Overlay		A	
						DESIGN INFORMATION MIN. TRACK WIDTH: <u>8</u> MIL MIN. CLEARANCE: 7 <u>.874</u> MIL		
-	-					MIN. CLEARANCE: MIN. VIA PAD SIZE:	1 <u>9.6</u> 9 ML —	
						MINIMUM ANNULAR RING 0.15mm (5.9055 MIL) EXTERNAL PER IPC-D-275 CLASS 2 LEVEL C		
						REGISTRATION TOLERANCES	: METAL +/- 5 ML, HOLES +/- 3 ML ILESS OTHERWISE SPECIFIED): +/- 3 ML	
						MATERIAL:		
					FR-408 X FR-4			
_						THICKNESS: X 63 MIL (1.6 TOLERANCE: X AN	imm) +/-10% 0THER	
В							HSI PC-6012 TYPE 3 CLASS 2 B	
						BOW & TWIST: X AN	ISI PC-6012 TYPE 3 CLASS 2	
					DRILLING:			
					PTH COPPER THICKNESS:	REFERENCE: X AS SHOWN X NC_DRILL FILES PTH COPPER THICKNESS: X 20-30 um OTHER		
						BOARD FINISH:		
			Symbol Quantity Finished H	Hole Size Platad Hole Type 200mm) PTH Round		SILKSCREEN: X SILKSCREEN COLOR: X	TOP X BOTTOM	
	L 191 1 122datal (3.580m) PTH Royad X 0 1772mil (3.680m) PTH Royad				SOLDER RESIST COLOR: X GREEN OTHER			
	C						X MATTE SEMI-GLOSS	
						MM. TIN/SILVER OR E	EQUIV OTHER	
				Reund PTH Round		ARRAY/PANEL: CUT AND TRIM PER MI BOARD OUTLINE 		
С								
						X ANSI IPC-A-600	F CLASS -> 1 X 2 3	
				- +0/-7.87MIL		_		
				12MIL MIL		PCB MUST BEAR THE UL94	R EXCEED UL94-VO REQUIREMENTS. /-O UL REGISTERED MATERIAL ID NUMBER	
						ADDITIONAL REQUIREMENTS: V	MA TENTING: YES X NO MPEDANCE CONTROL: YES NO X	
						BARE BOARD ELEC. TEST:		
	65. 00mm					TEXAS INSTRUMENTS		
	I I I I I I I I I I I I I I I I I I I					PROJECT TITLE:	PROJECT TITLE: TIDA-00807	
						DESIGNED FOR:		
D						Public Release		
						TIDA-00807_E2.PcbDoc		
	THE TREAK VIEWED TROTTOP SIDE DOIND #: TIDITOGOOF REV. E2 SOUR REV. NOV IT VETSIDICONTOL			Texas Instruments (TI) and/or its licensors do not uarrant the accuracy or completeness of this specification or any information contained therein. TI and/or its licensors do not uarrant that this design uill meet		ENGNEER: Srinivasa	LAYOUT BY: Manjunath M	
	LAYER NAME = Drill Drawing			the specifications, will be suitable for your application or fit for any an implementation. II and/or its licensors do not warrant that the design completely will due and test war design implementations to require the set	n is production worthy. You should	SCALE: 1.00	ALTUM DESIGNER VERSION: 16.0.9.368	
1	PLOT NAME = TIDA-00807E2.GD1	GENERATED : 6/6/2016 5:14:36 PM 2	TEXAS INSTRUMENTS 3	completely validate and test your design implementation to confirm the s	ystem functionality for your application. 5		6	
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