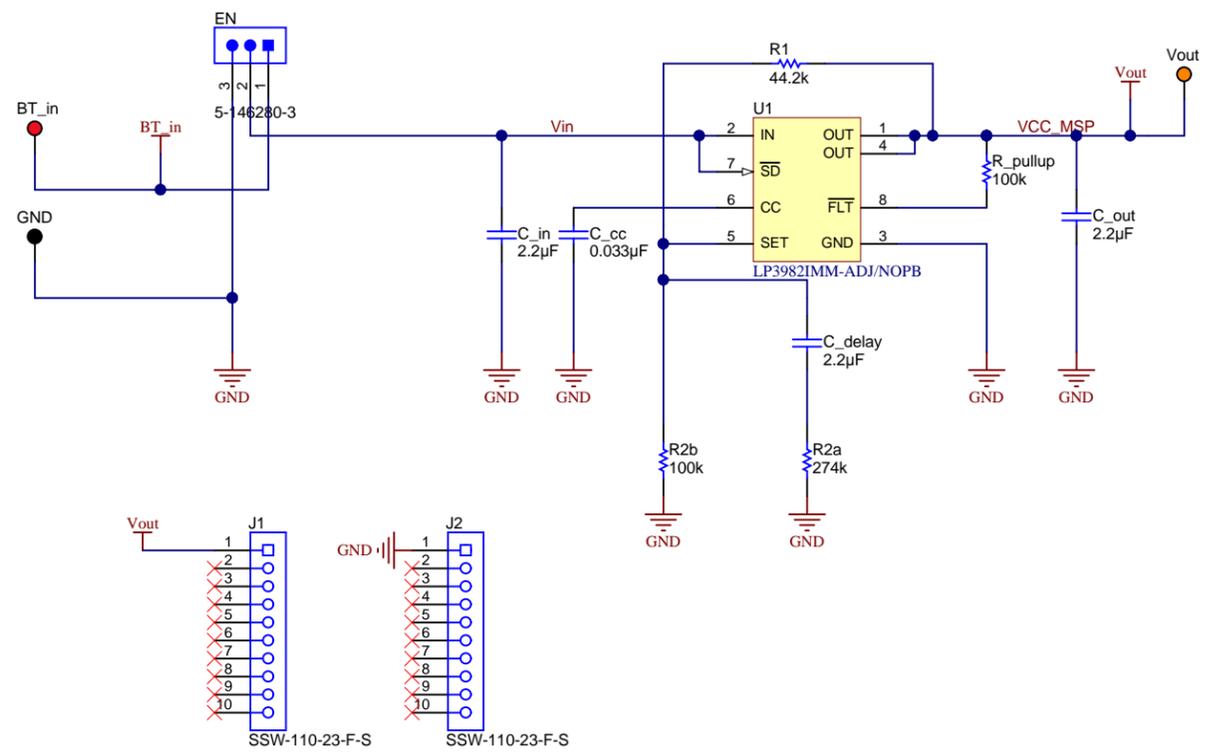


Revision History	
Revision	Notes

Cannot open file C:\Users\{a0225153}\Desktop\DVS LDO.png

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TID #: TIDA001172	Project Title: MSP430_DVS_TIDA		
Number: TIDA-01172	Rev: E1	Sheet Title:	
SVN Rev: Version control disabled	Assembly Variant: [No Variations]	Sheet: 1 of 3	
Drawn By:	File: PCB_Project_CoverSheet.SchDoc	Size: B	
Engineer: Jerry Leung	Contact: http://www.ti.com/support		

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TID #: TIDA001172	Project Title: MSP430_DVS_TIDA	Sheet: 2 of 3	
Number: TIDA-01172	Rev: E1	Assembly Variant: [No Variations]	Size: B
SVN Rev: Version control disabled	File: TIDA_DVS.SchDoc	Contact: http://www.ti.com/support	© Texas Instruments 2016
Drawn By:	Engineer: Jerry Leung		



PCB Number: TIDA-01172
PCB Rev: E1

PCB
LOGO
Texas Instruments

PCB
LOGO
Pb-Free Symbol

Label Table	
Variant	Label Text
001	TIDA-00531

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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Number: TIDA-01172	Rev: E1	Sheet Title:	
SVN Rev: Version control disabled	Assembly Variant: [No Variations]	Sheet: 3 of 3	
Drawn By:	File: PCB_Project_TID_Hardware.SchDoc	Size: B	
Engineer: Jerry Leung	Contact: http://www.ti.com/support		

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