

**Test Data
For PMP9486
08/22/2014**



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1. Design Specifications

Vin Minimum	20VDC
Vin Maximum	40VDC
Vout1	14.5VDC
Iout 1	0.100A
Vout2	9VDC
Iout 2	0.100A
Vout3	14.5DC
Iout 3	0.100A
Vout4	9VDC
Iout4	0.100A
Vout5	10.5VDC
Iout 5	3mA (Just a Reference Supply)
Approximate Switching Frequency	300KHz Approx

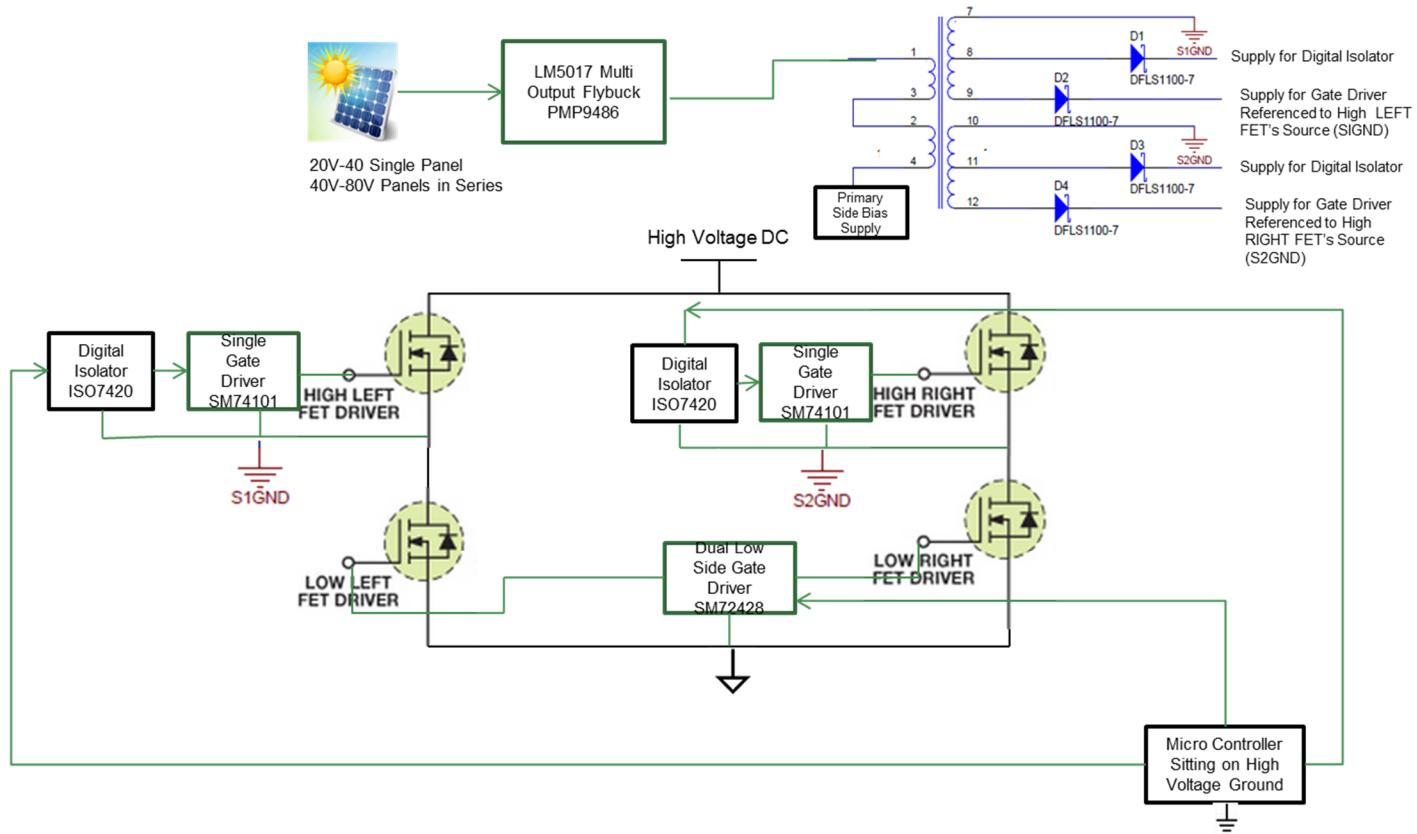
Vout 1 and Vout 2 are referenced to same Ground (GND1), Vout3 and Vout4 are referenced to same ground (GND2) . Vout5 is reference to Input Supply's Ground

2. Circuit Description and PCB details

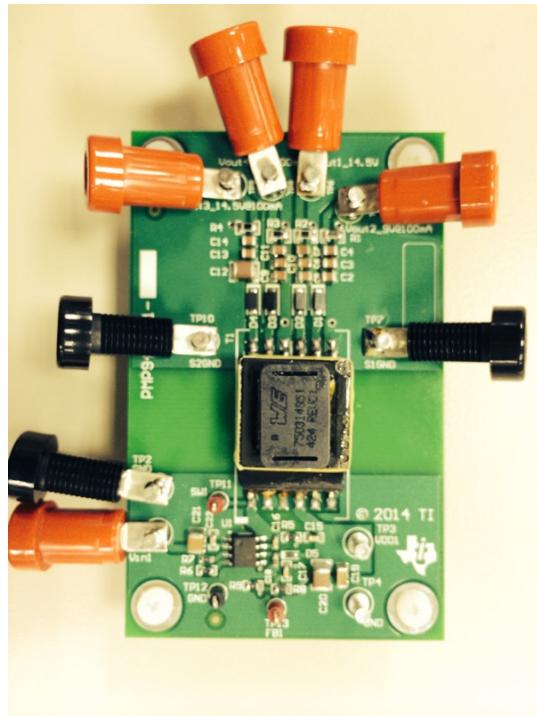
PMP9486 is a 5 output Flybuck Converter for driving Isolated Driver stages(GND wrt High Side Sources) in DC/AC section of High Frequency Commercial or residential Inverters(Ex-Solar Micro inverters) using the LM5017 regulator IC. The design accepts an input voltage of 20Vin to 40Vin DC(from Single Panel in Microinverters or 12V/24V batteries) and provides Isolated outputs of 14.5V,9V @100mA(referenced to same ground) and 14.5V,9V@100mA(referenced to same Ground). It features a small size and is an inexpensive and more efficient solution to using Flyback or Pushpull converters

The design is made by modifying the isolated Flybuck Evaluation module of LM5017 using Wruth Customized Transformer.

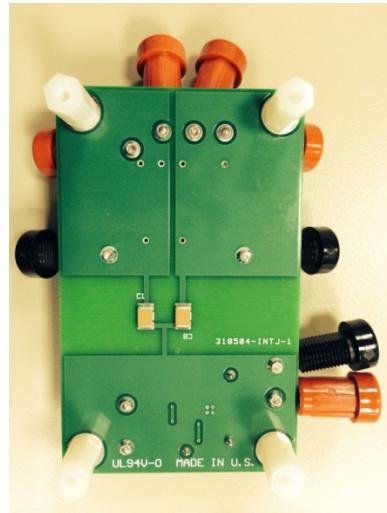
3. PMP9486 Block Diagram –Application in MicroInverters



4. PMP9486 Board Photos

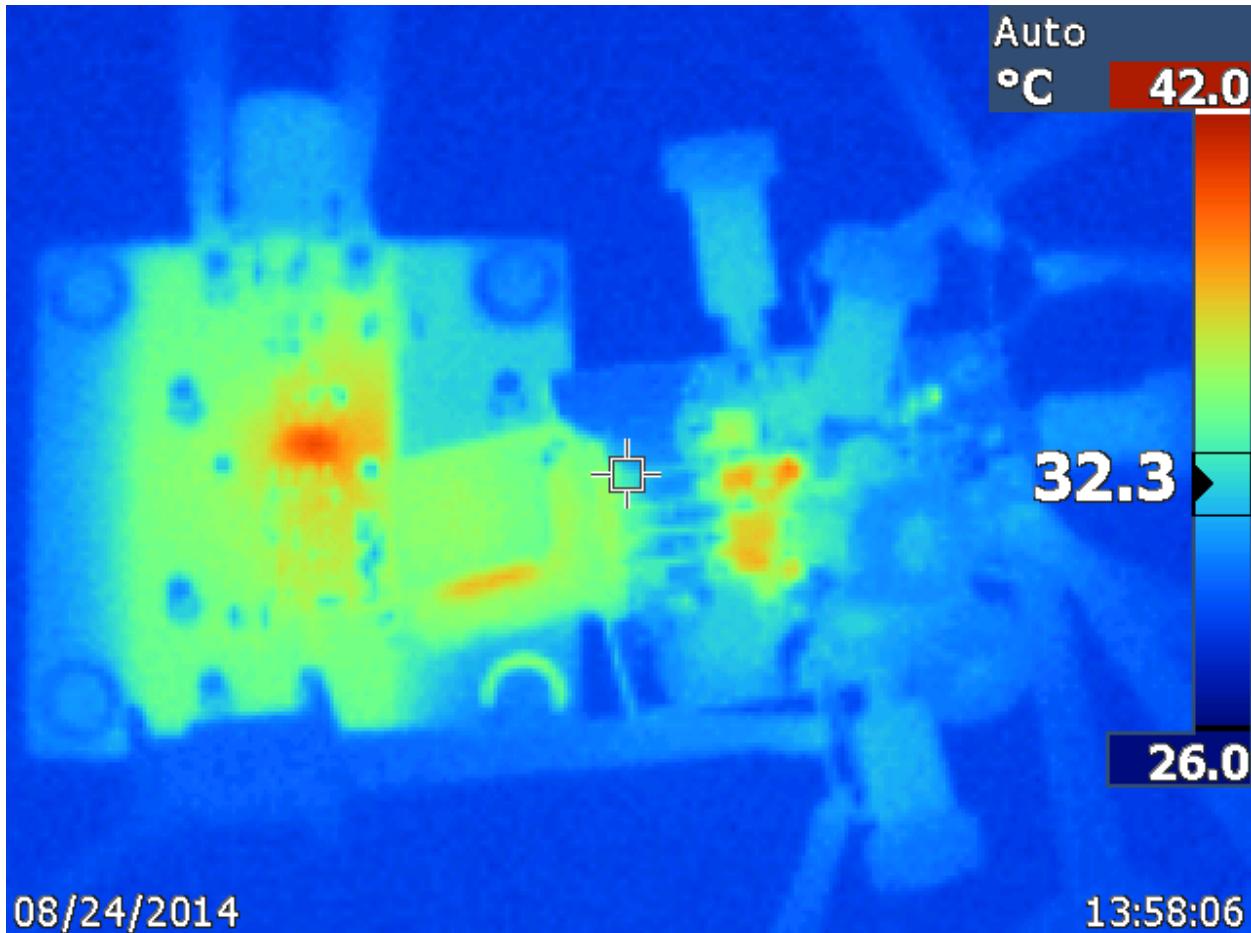


Board Photo (Top)



Board Photo (Bottom)

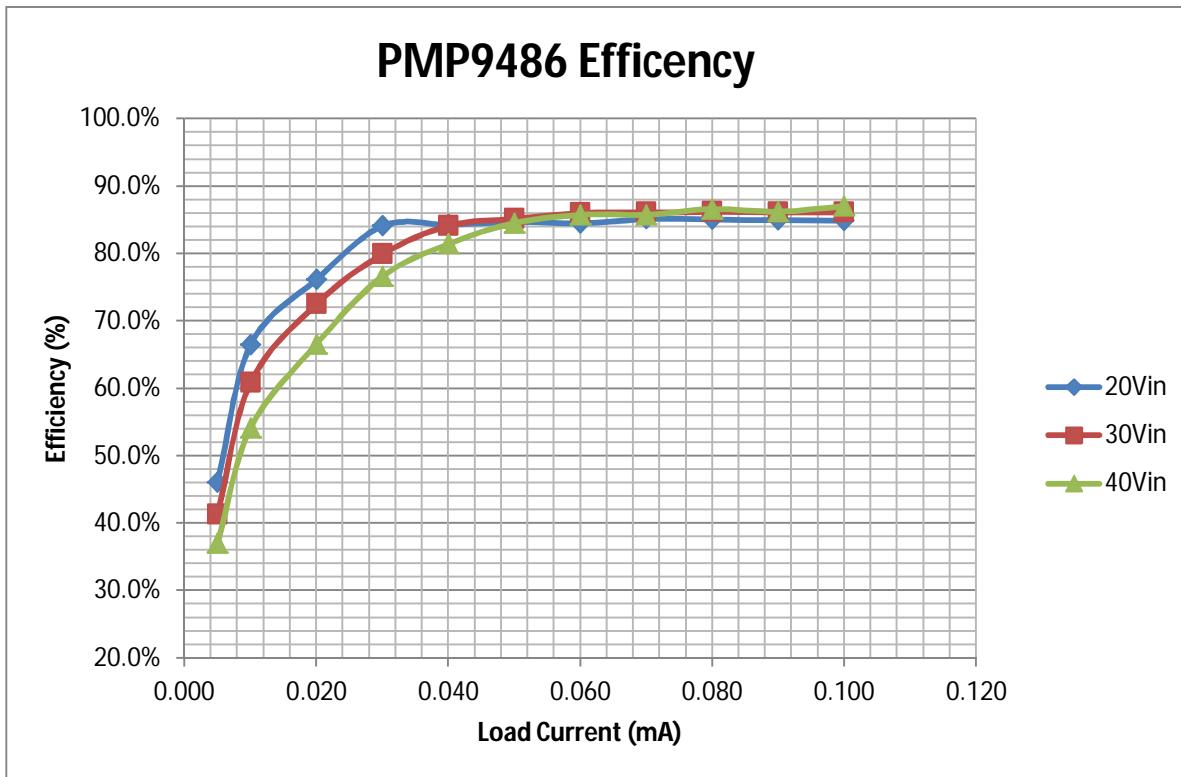
5. Thermal Data



IR thermal image taken at steady state with 30Vin and 150mA load (no airflow)

6. Efficiency

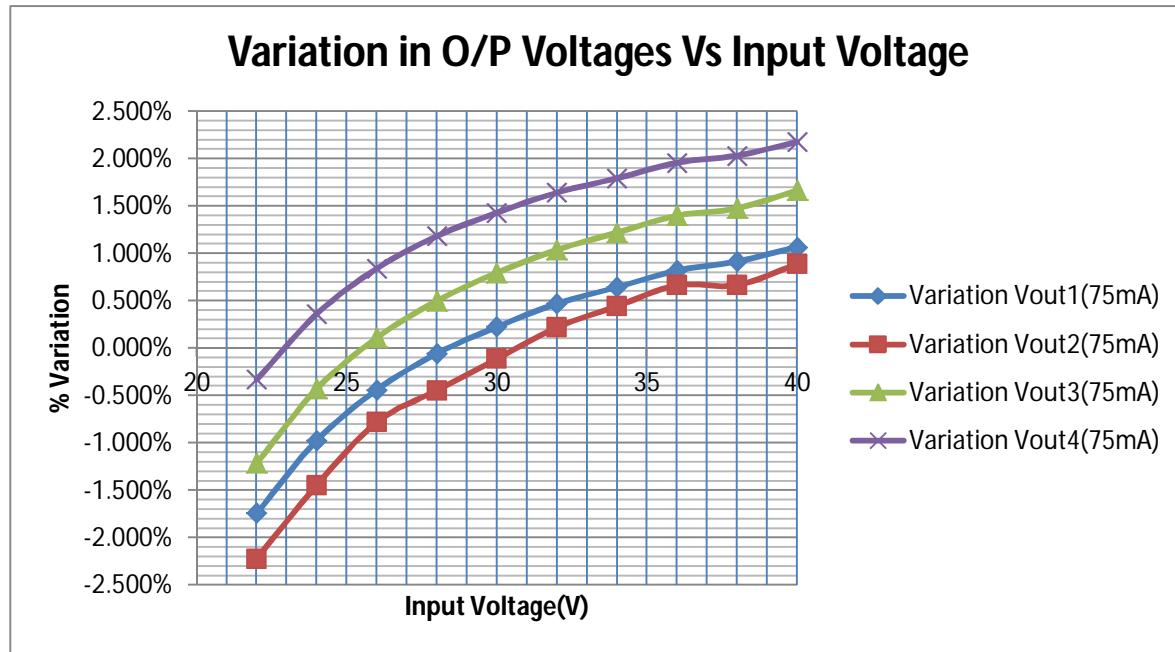
6.1 Efficiency Chart



7. Cross Regulation

The Cross regulation was tested by Sweeping Vin(keeping Load Constant) or Output Load(Keeping Vin Constant)

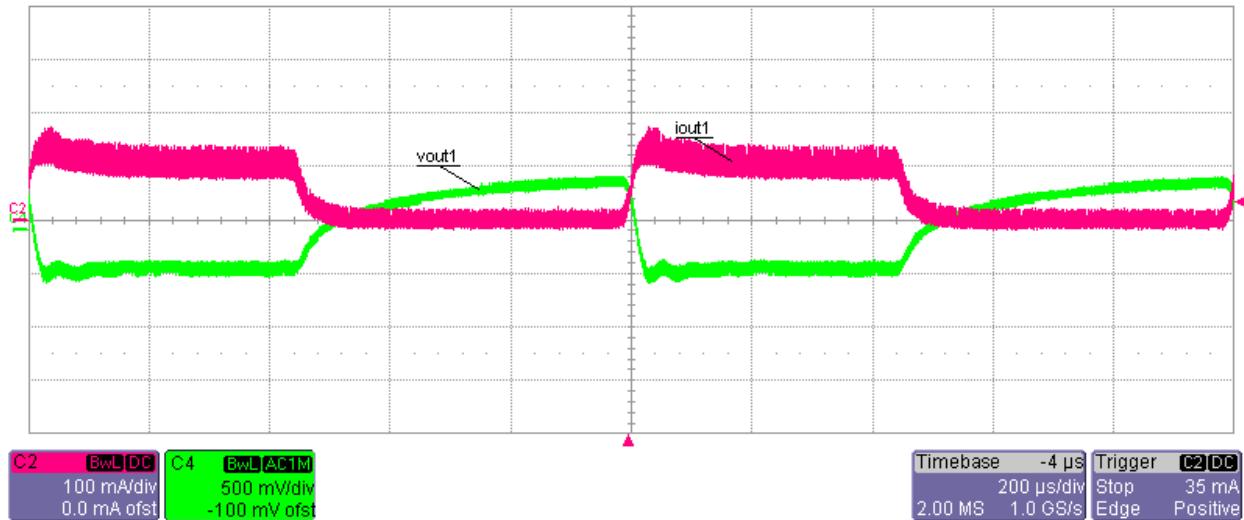
7.1 Vin Sweep Response



Vin	Vout1	Vout2	Vout3	Vout4	Iout	Variation Vout 1	Variation Vout2	Variation Vout 3	Variation Vout 4
40	14.756	9.08	14.918	9.15	70mA	1.068%	0.889%	1.667%	2.178%
38	14.734	9.06	14.897	9.133	70mA	0.918%	0.667%	1.478%	2.034%
36	14.72	9.06	14.886	9.126	70mA	0.822%	0.667%	1.400%	1.959%
34	14.694	9.04	14.862	9.11	70mA	0.644%	0.444%	1.222%	1.795%
32	14.669	9.02	14.84	9.0934	70mA	0.473%	0.222%	1.038%	1.644%
30	14.633	8.99	14.809	9.0718	70mA	0.226%	-0.111%	0.798%	1.432%
28	14.592	8.96	14.773	9.045	70mA	-0.055%	-0.444%	0.500%	1.185%
26	14.536	8.93	14.723	9.01	70mA	-0.438%	-0.778%	0.111%	0.842%
24	14.458	8.87	14.653	8.9615	70mA	-0.973%	-1.444%	-0.428%	0.363%
22	14.346	8.8	14.552	8.891	70mA	-1.740%	-2.222%	-1.211%	-0.329%

8 Waveforms

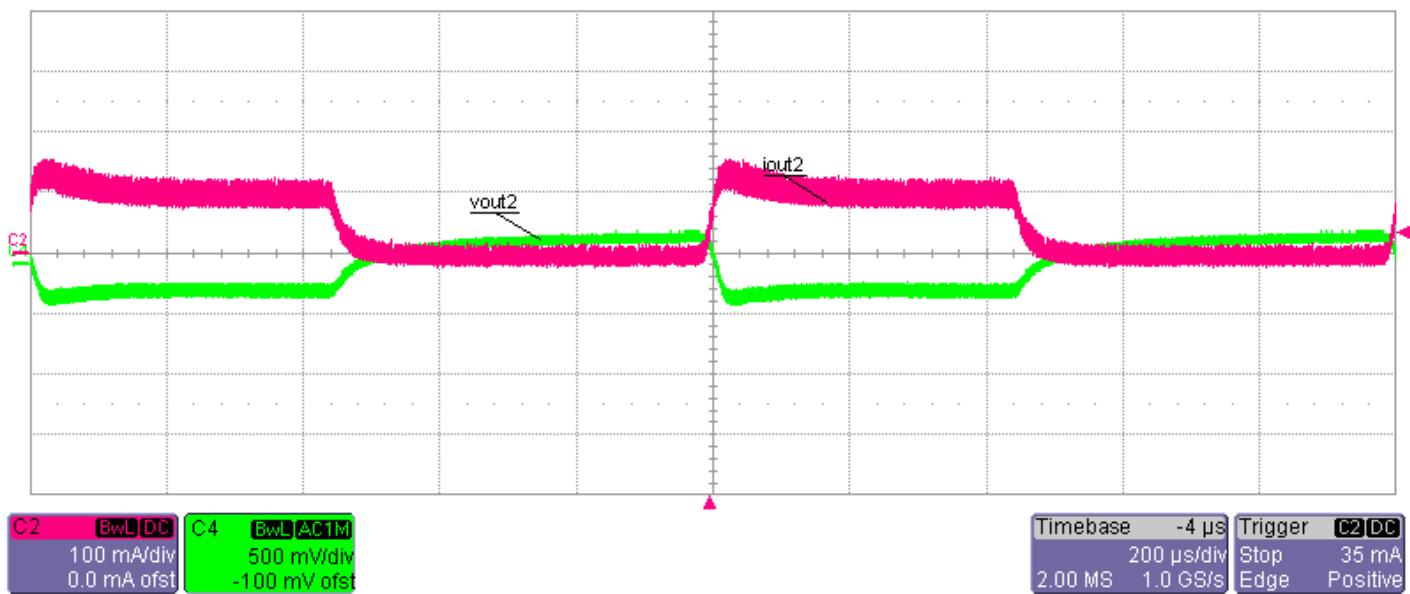
8.1 Load Transient Response



**Load Transient Response at 20Vin and 0%-to-100% (0mA-to-100mA) Load Step on 14.5V Output
Vout1 (Load were no connected to any other outputs)**

Ch4 – Vout1 (AC coupled)

Ch2- Iout 1



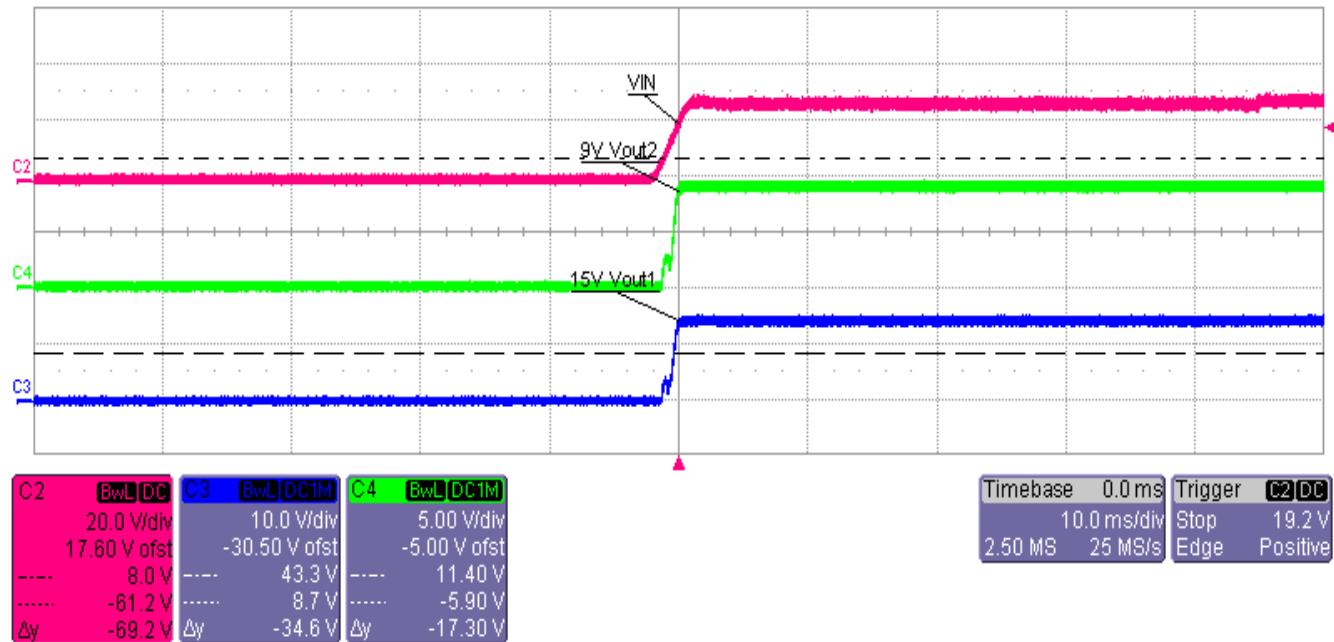
Load Transient Response at 20Vin and 0%-to-100% (0mA-to-100mA) Load Step on 9V Output

Vout2 (Load were no connected to any other outputs)

Ch4 – Vout2 (AC coupled)

Ch2- Iout 2

8.2 Startup



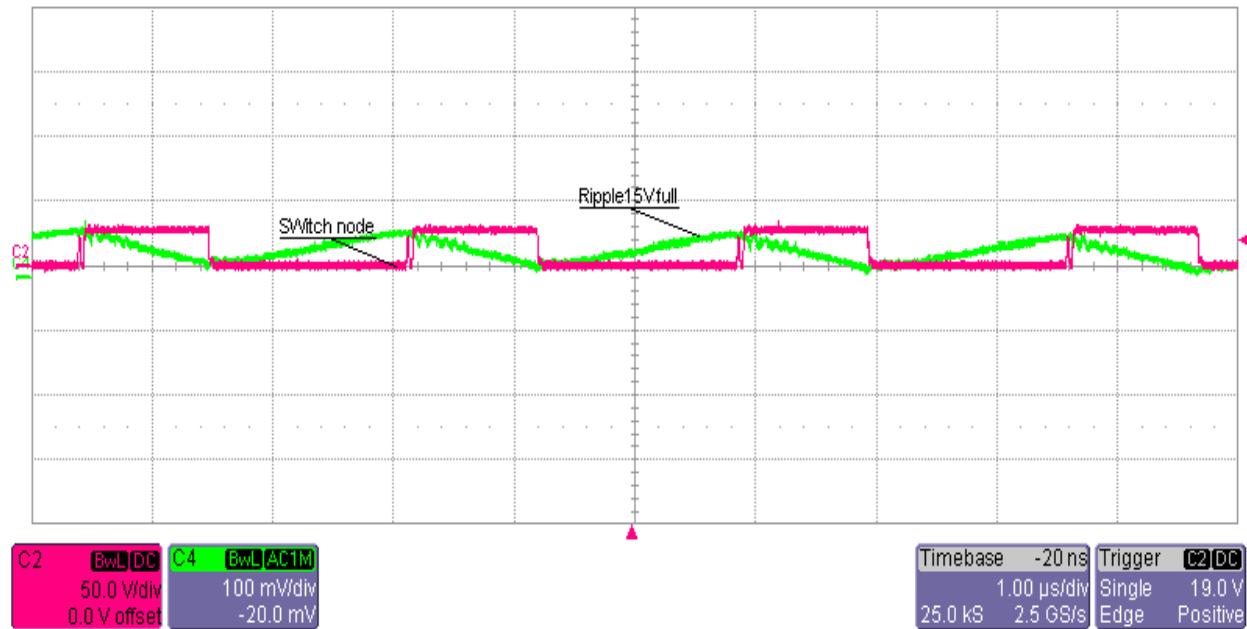
Startup into full Load (all the output was connected to 100mA) at 20 Vin

Ch2-Vin

Ch3-Vout 1

Ch4-Vout 2

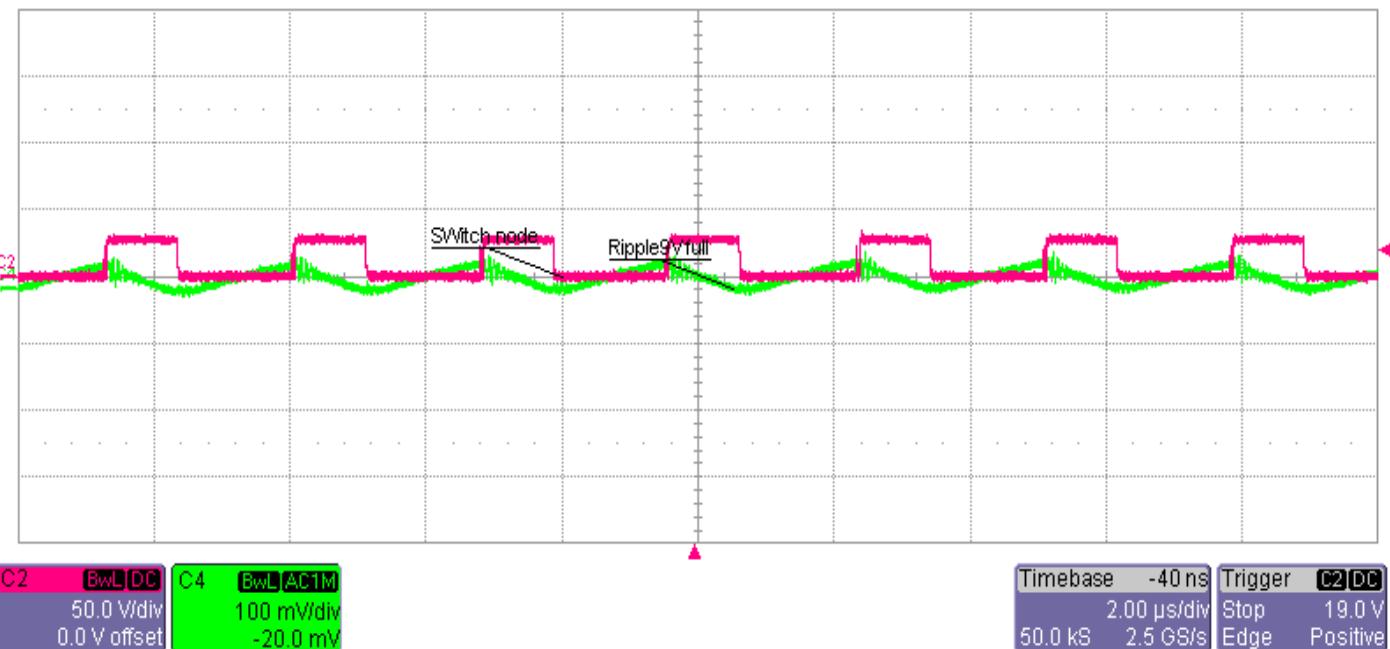
8.3 Output Voltage Ripple and Switch Node Voltage



**Switch Node Voltage and Output Voltage Ripple at 20 Vin and Full (100mA) Load on all the outputs
(Vripple < 60mVp-p)**

Ch4-Vout1 (AC Coupled)

Ch2-Switching Waveform

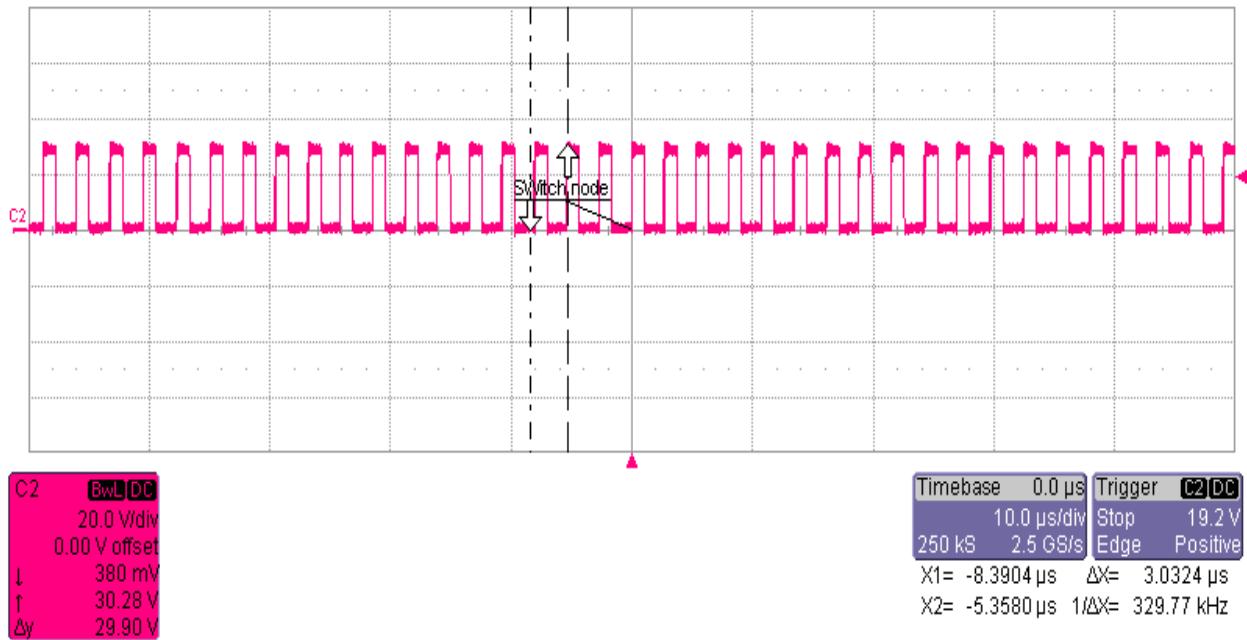


**Switch Node Voltage and Output Voltage Ripple at 20 Vin and Full (100mA) Load on all the outputs
($V_{ripple} < 60mV_{p-p}$)**

Ch4-Vout2 (AC Coupled)

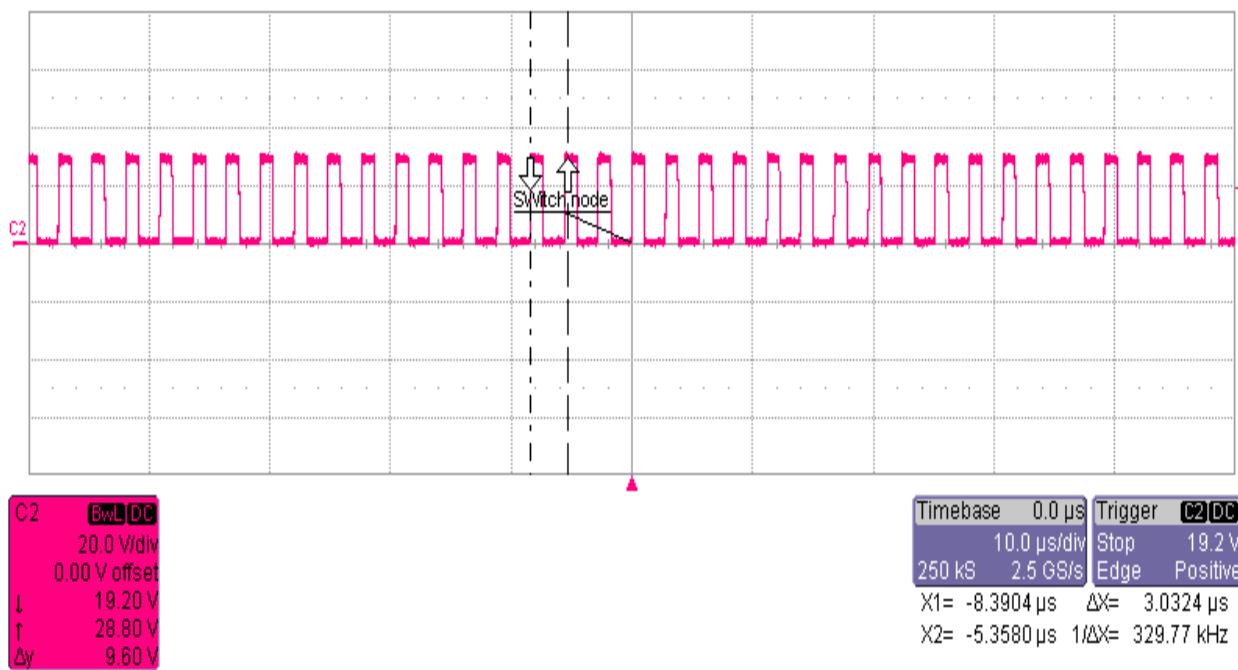
Ch2-Switching Waveform

8.4 Primary Side Switching Waveform



Switch Node Voltage(Primary side) at 30 Vin and Full (100mA) Load on all the outputs

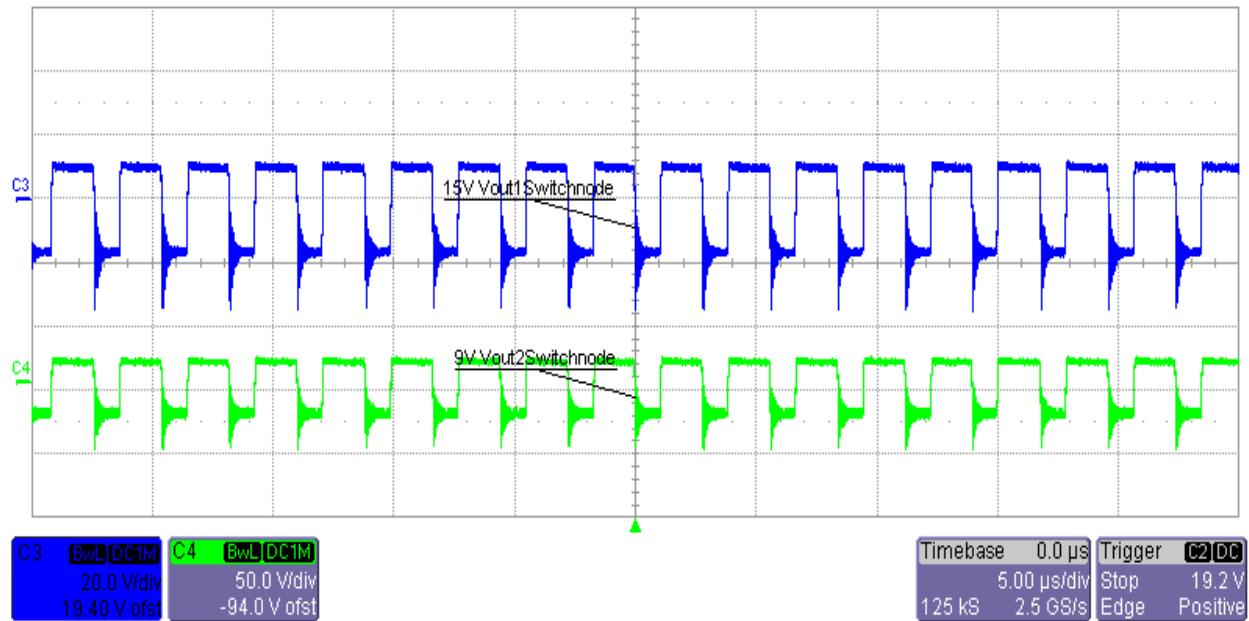
Ch2-Switching Waveform



Switch Node Voltage(Primary side) at 30 Vin and No Load on all the outputs

Ch2-Switching Waveform

8.5 Secondary Side Switching Waveform



All the outputs were loaded with 100mA and waveform was taken at Secondary switch node for Vout1 14.5V as well as Vout2 9V.

Ch3-Switching Waveform from Anode of Diode to Secondary Ground – 9V

Ch4- Switching Waveform from Anode of Diode to Secondary Ground – 14.5V

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