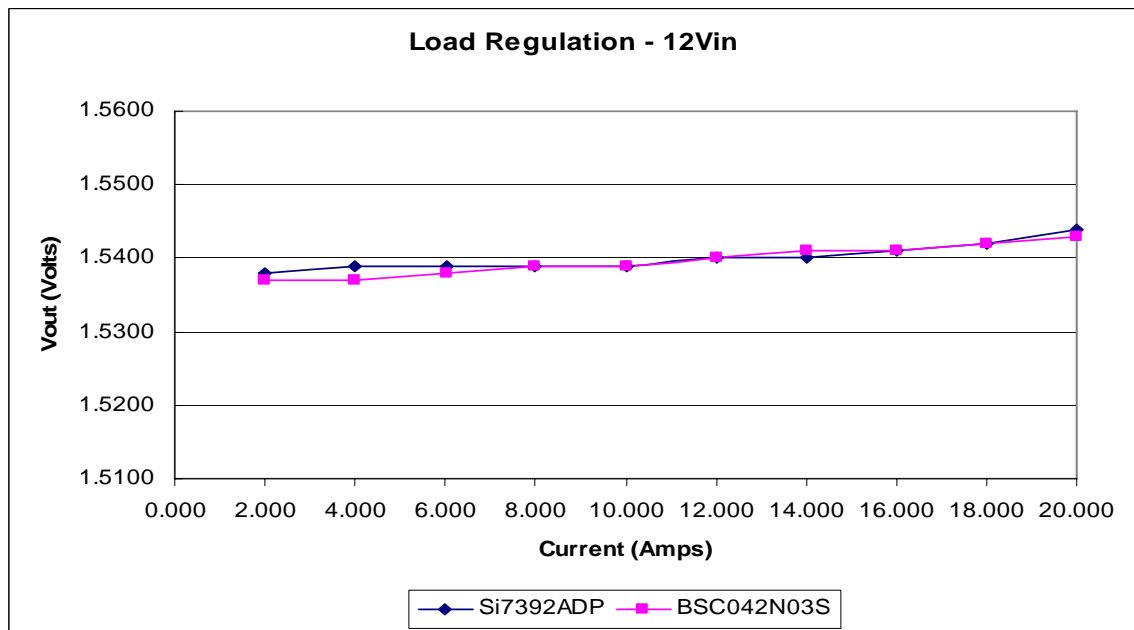
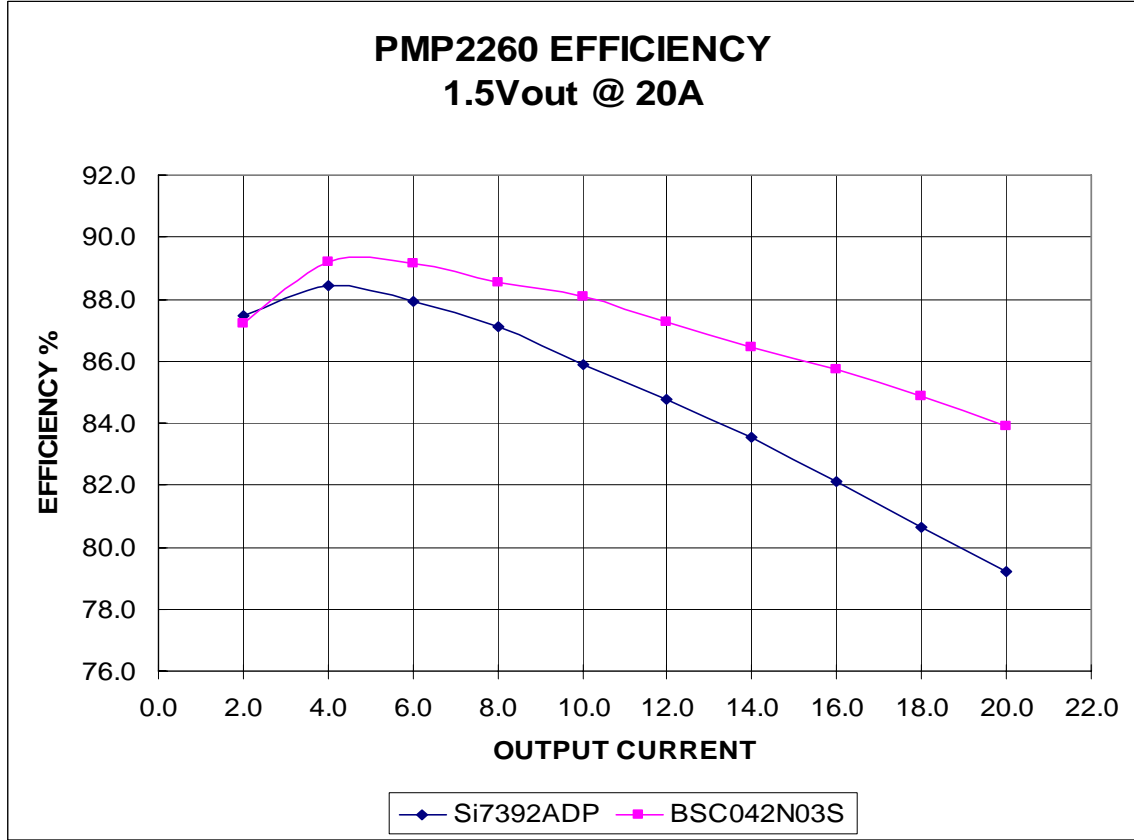


1 Efficiency/Line Regulation (1.5V output)

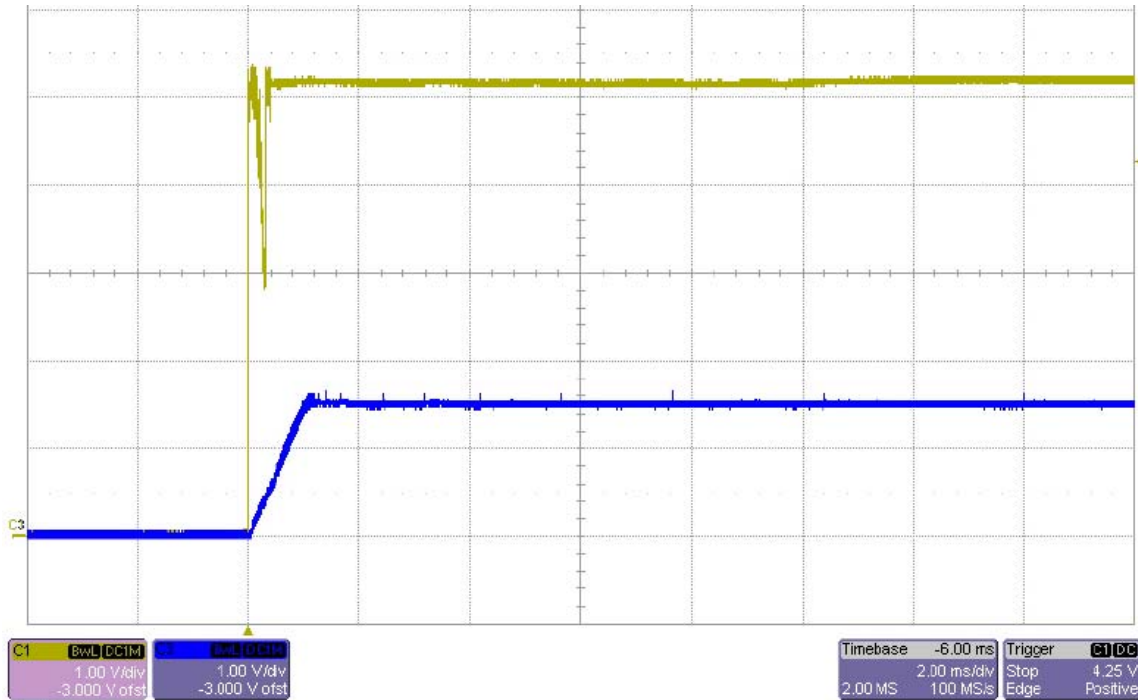
The converter efficiency and regulation over load are shown in the figures below.



1.5V@20A

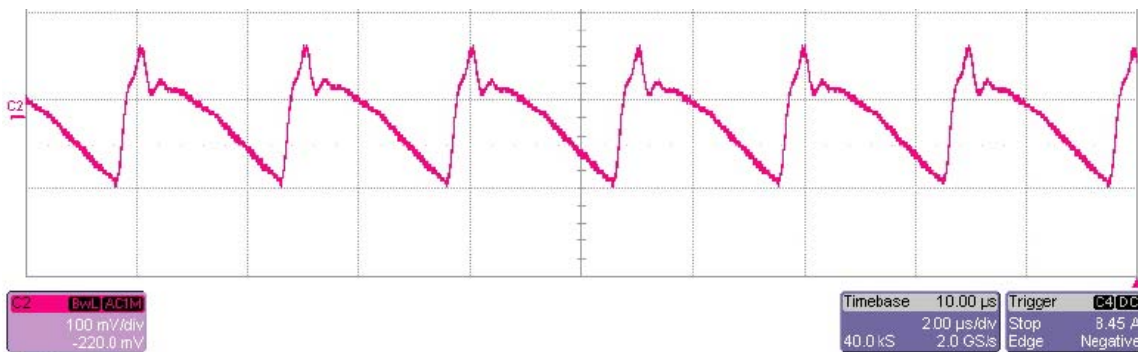
2 Startup (1.5V)

The photo below shows the 1.5V output voltage (Ch3) startup waveform after the application of 5Vdc in (Ch1). The output was loaded to 0A.



3 Output Ripple Voltage (1.5V)

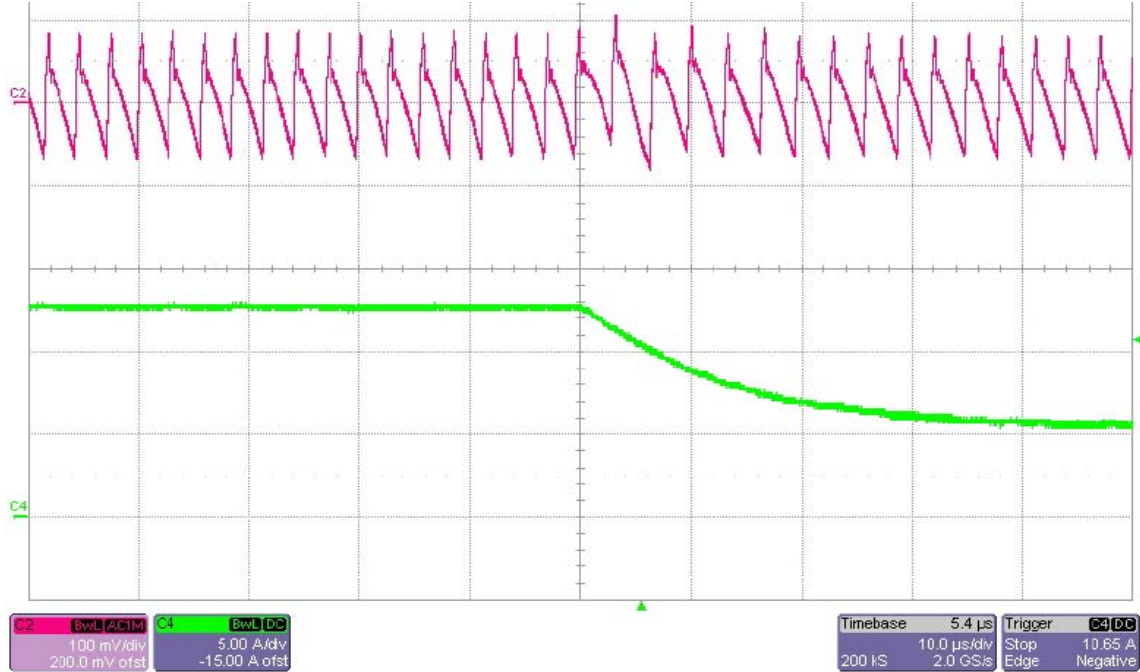
The 1.5V output ripple voltage is shown in the figure below. The image was taken with the output loaded to 20A and the input voltage set to 5Vdc. The Vpp was approx. 160mV



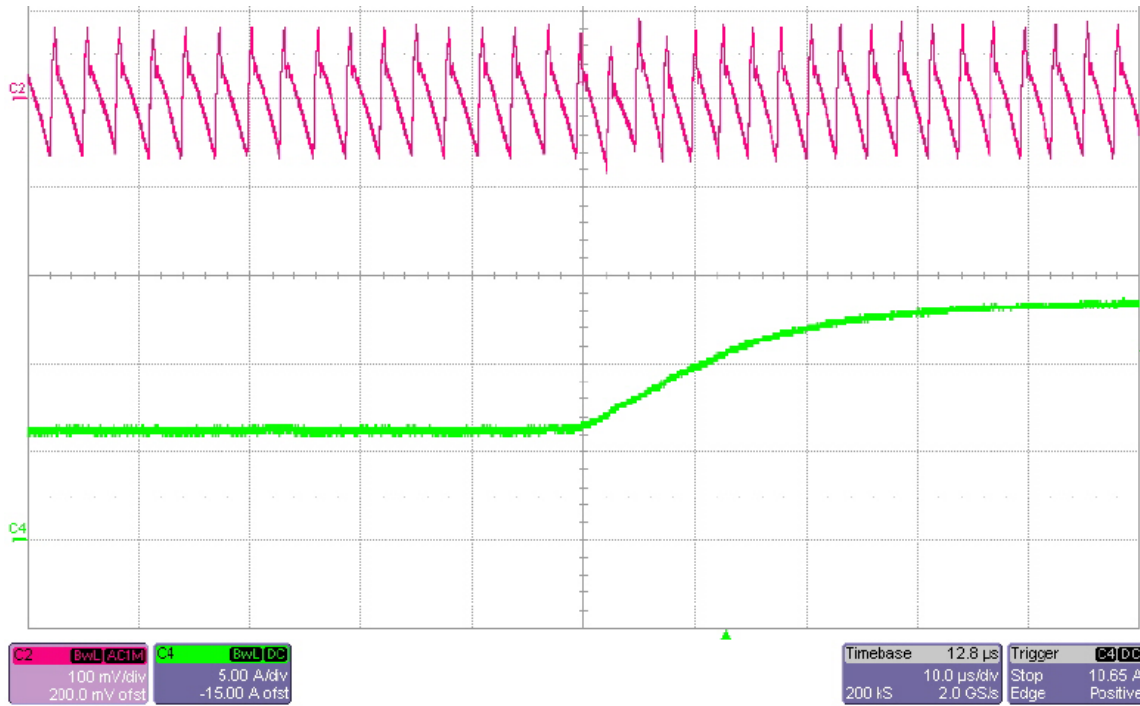
1.5V@20A

4 Load Transient (1.5V)

The photo below shows the 1.5V output voltage when the load current is pulsed between 20A and 10A.
Vin = 5Vdc.



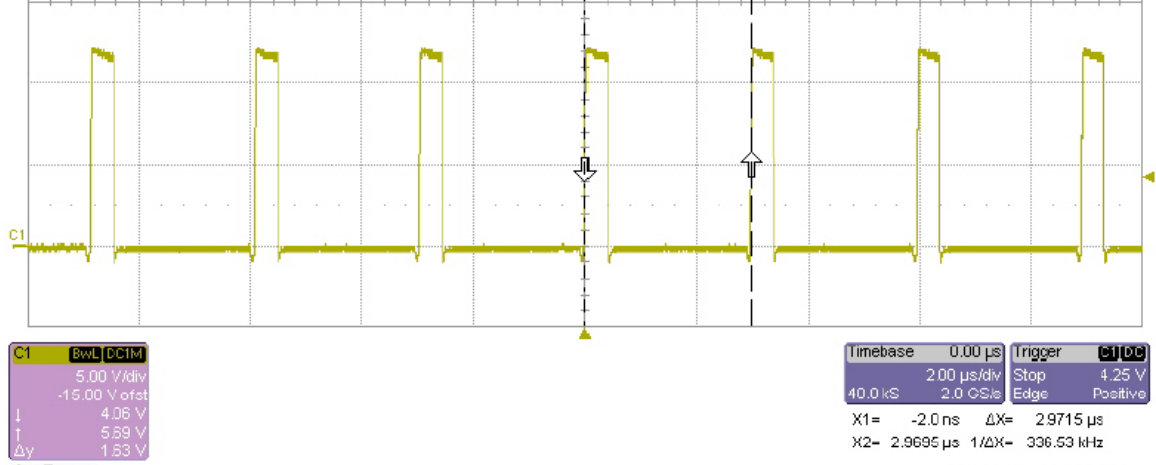
Negative Edge



Positive Edge

5 Switch Node Waveform (1.5V)

The photo below is of the switching node waveform. The input voltage is 5V and the output is loaded to 20A.



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