

**Buttons, Switches,
LEDs**

SHEET 2

DDR III

SHEET 3

FMC CONN

SHEET 4

ARRIA V I/O

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FPGA Configuration

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ARRIA High Speed Bank

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ARRIA V POWER

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SHEETS 16

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SHEET 17

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POWER SEQUENCER

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POWER SUPPLIES

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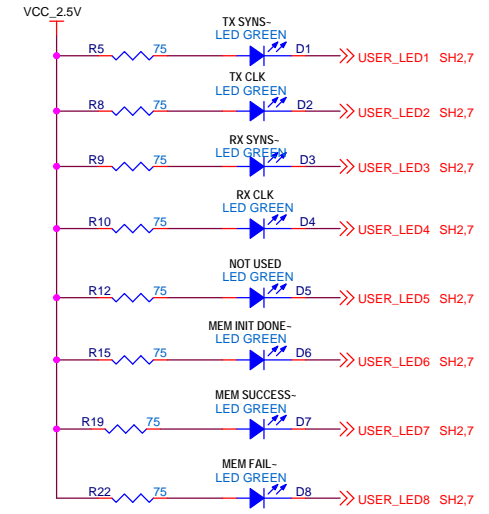
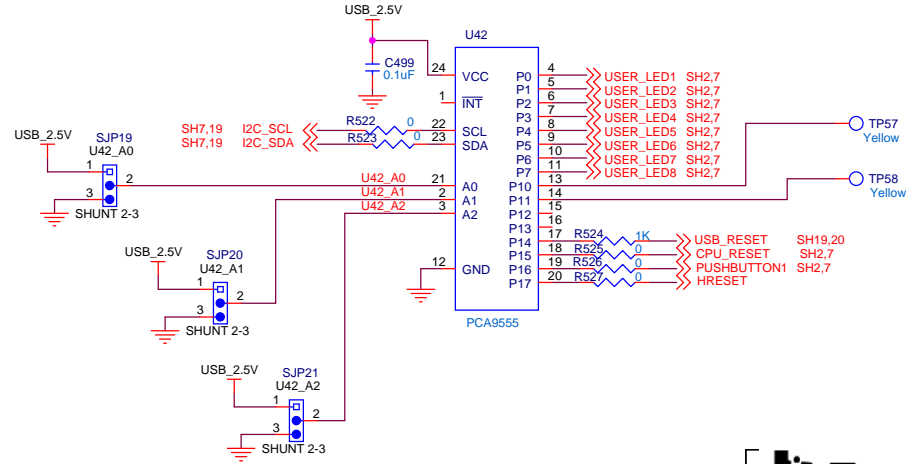
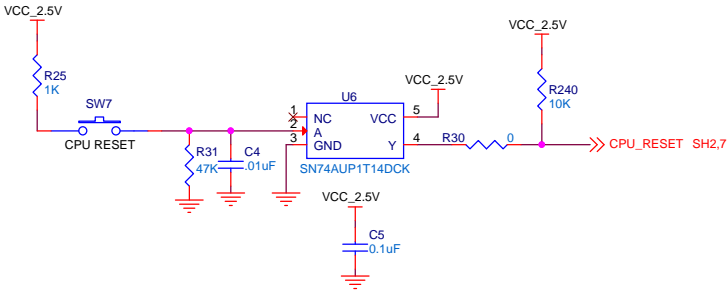
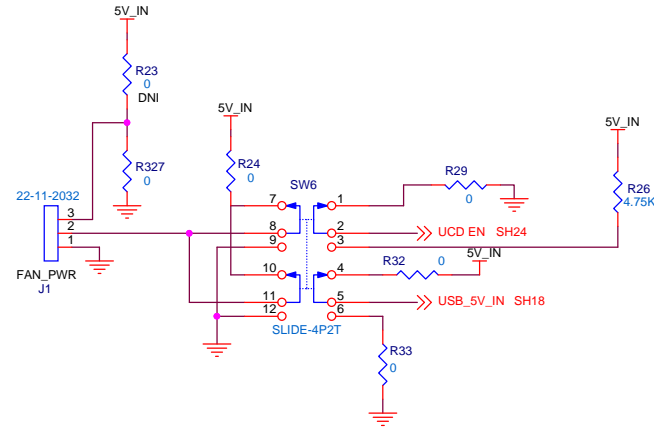
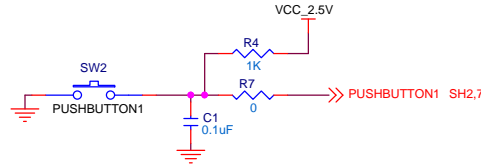
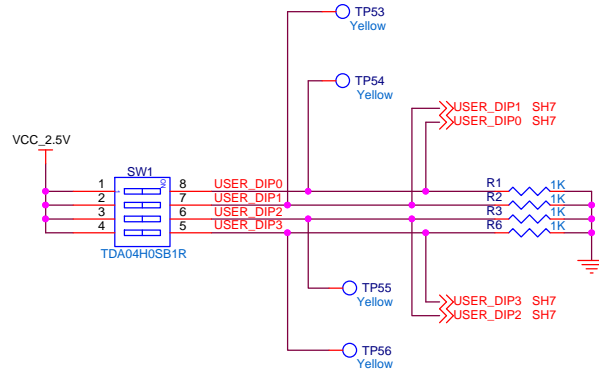
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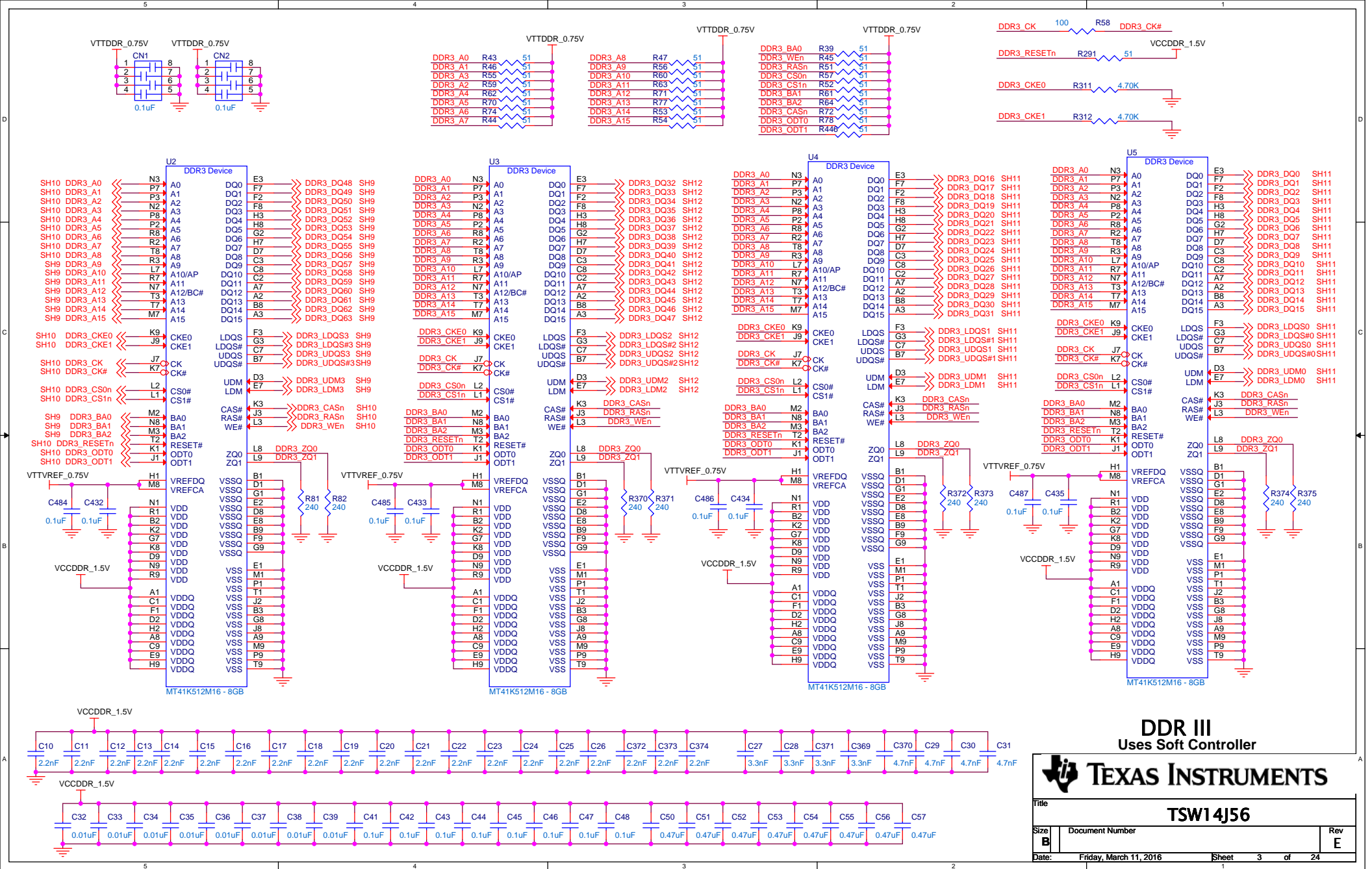
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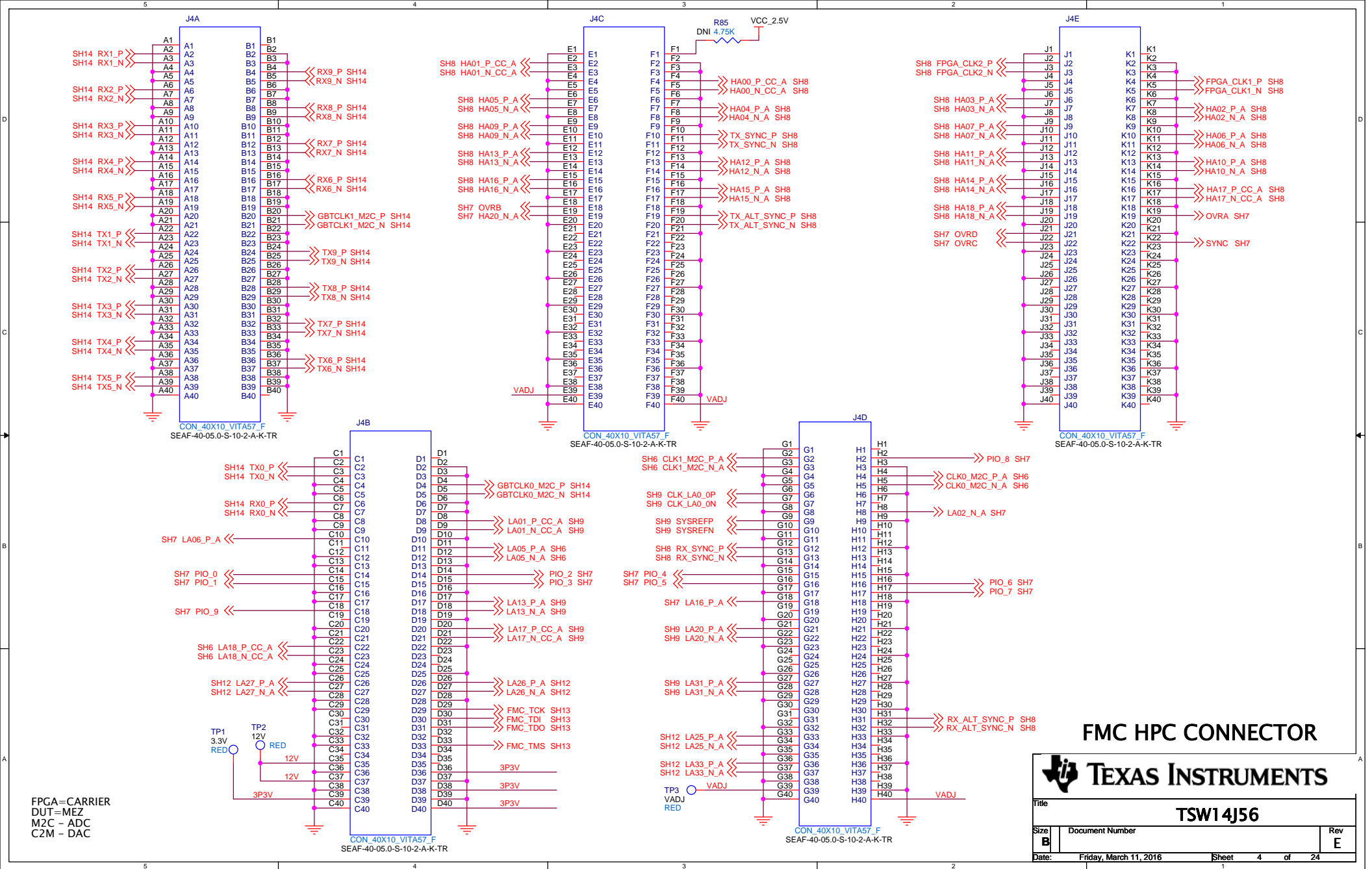
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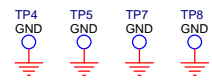
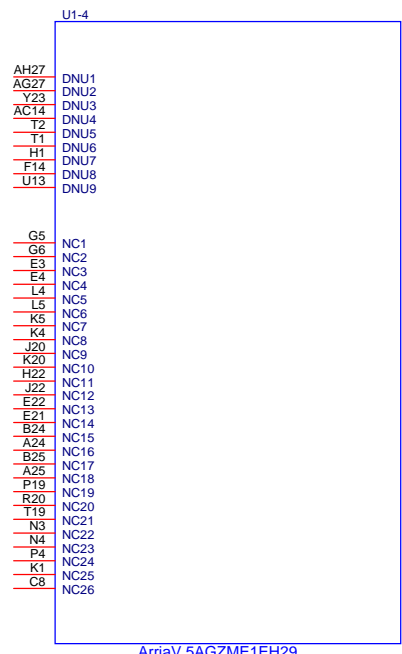
Buttons, Switches, LEDs



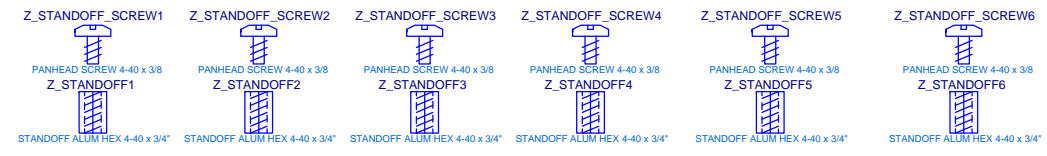
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MECHANICAL PARTS



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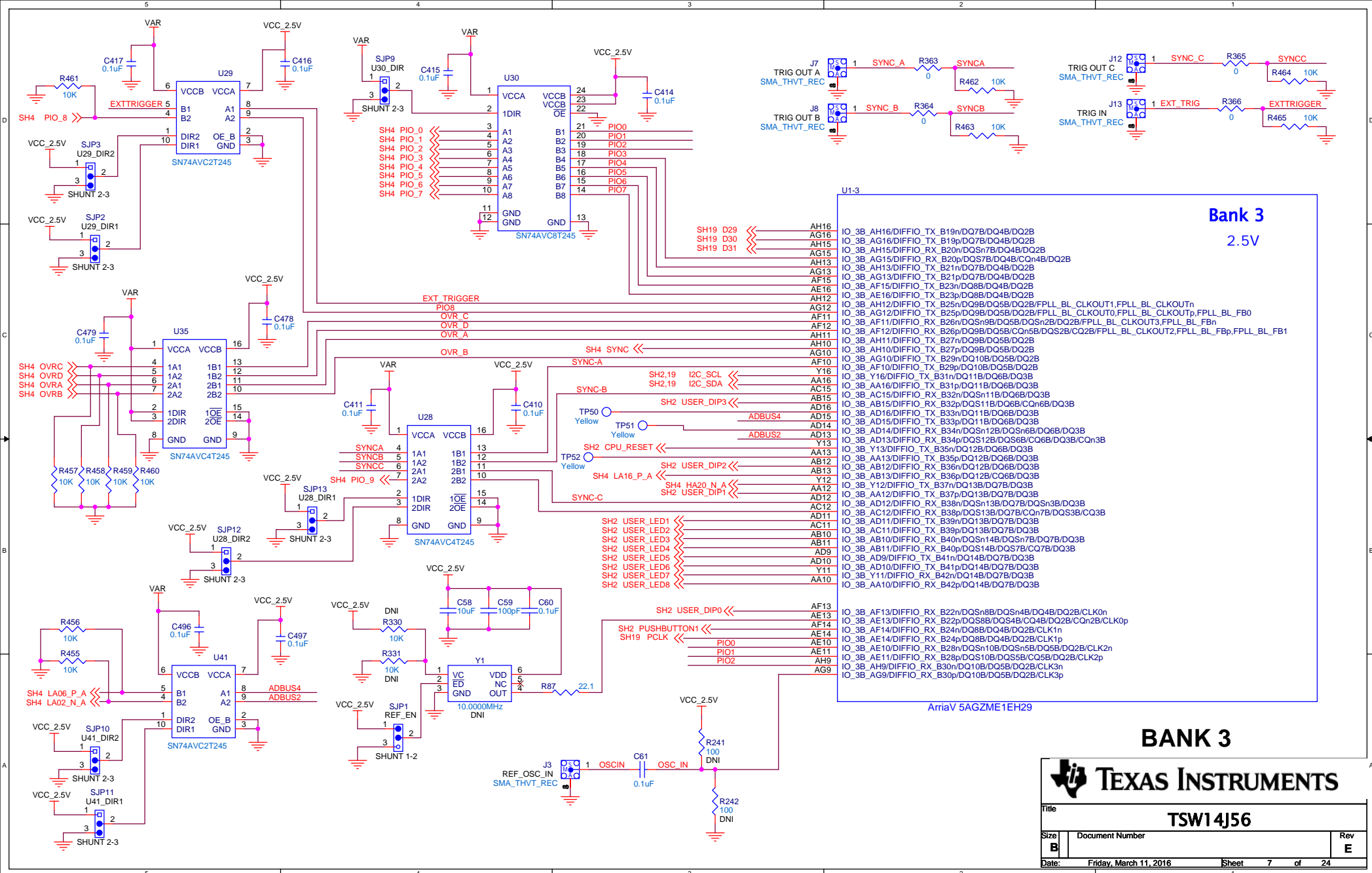


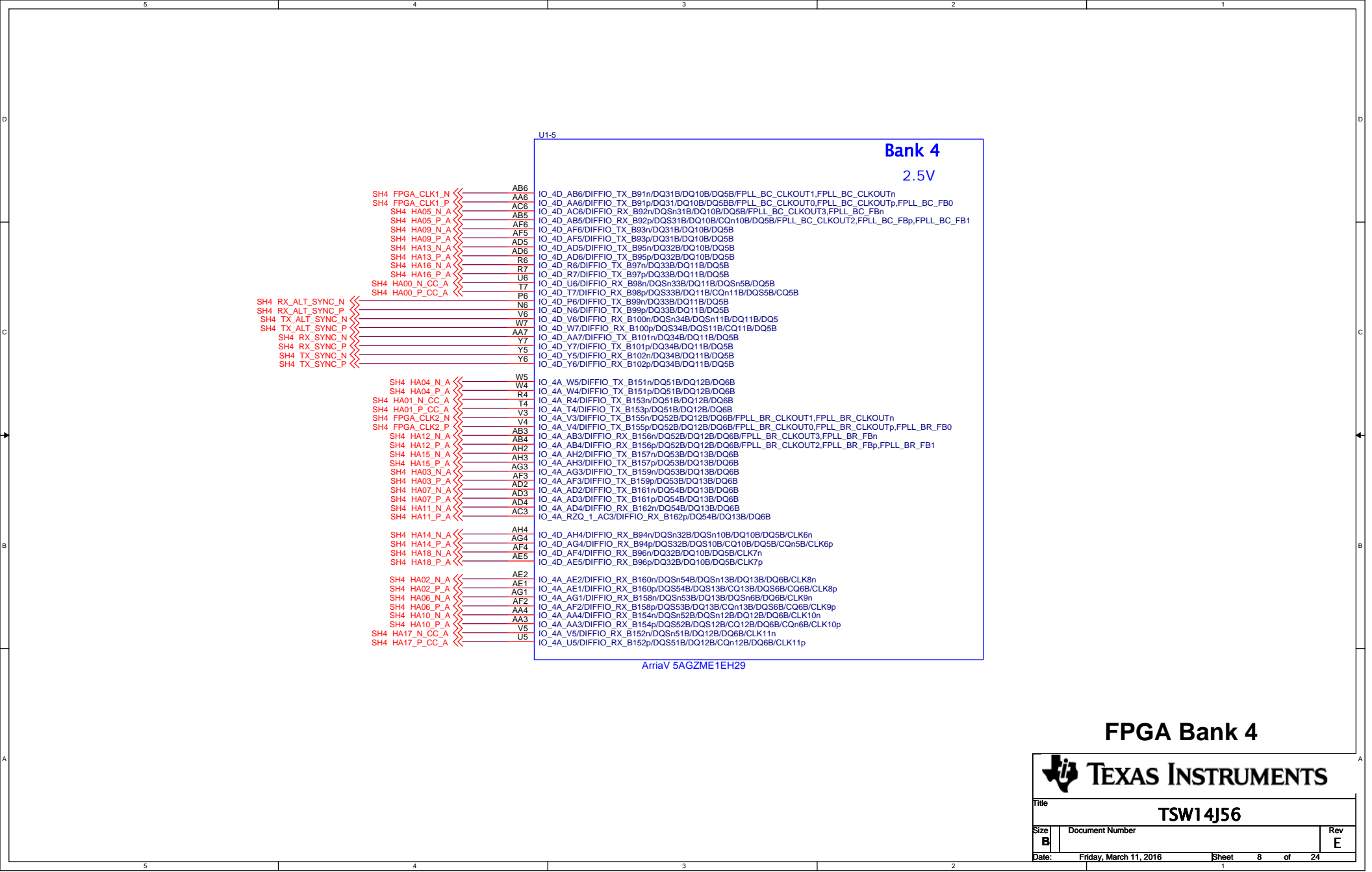
BANK 3




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FPGA Bank 4



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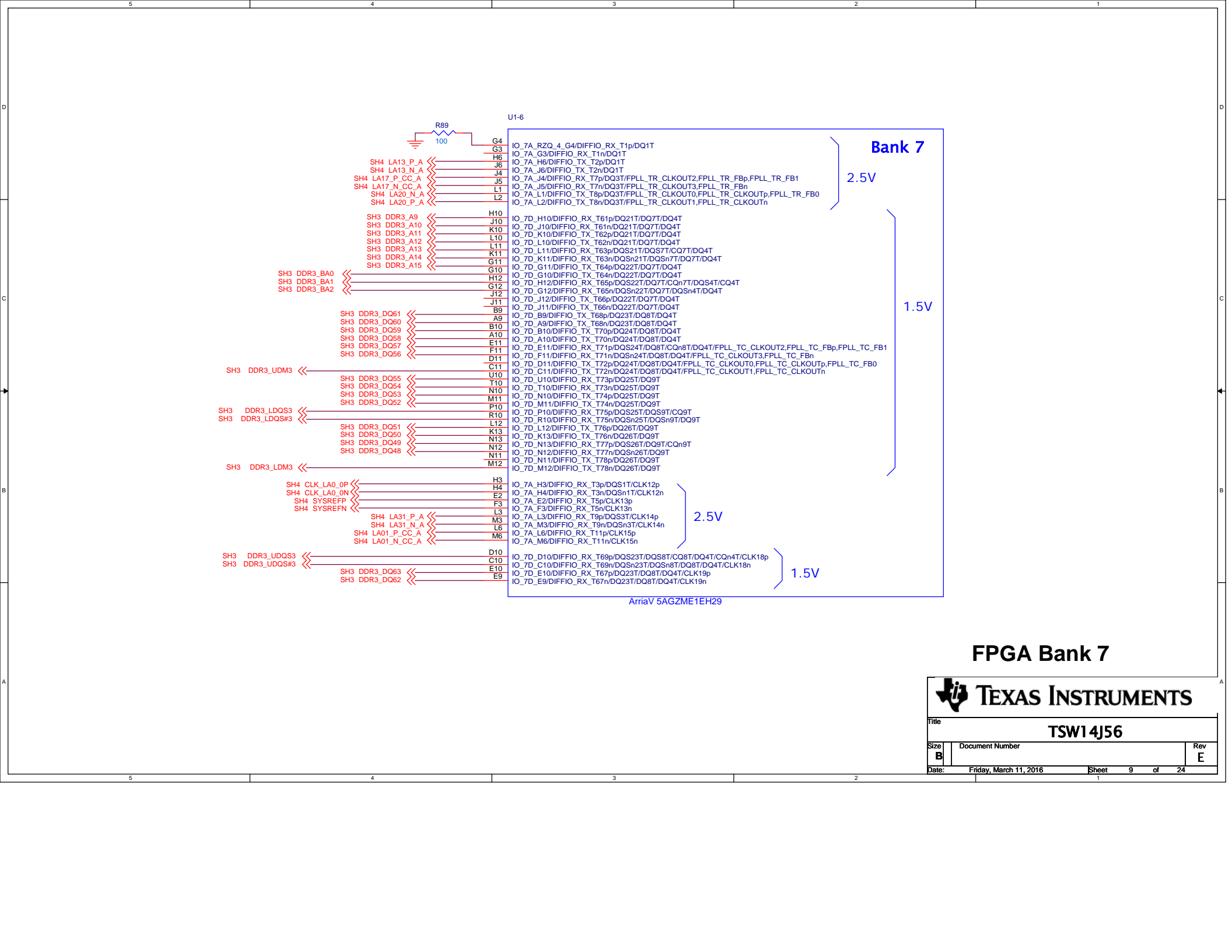
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U1-7

Bank 7

1.5V

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SH3 DDR3_RASn <<
SH3 DDR3_CASn <<
SH3 DDR3_ODT0 <<
SH3 DDR3_ODT1 <<
SH3 DDR3_CKE0 <<
SH3 DDR3_CKE1 <<
SH3 DDR3_CK <<
SH3 DDR3_CK# <<
SH3 DDR3_CS0n <<
SH3 DDR3_CS1n <<
SH3 DDR3_RESETh <<
SH3 DDR3_A0 <<
SH3 DDR3_A1 <<
SH3 DDR3_A2 <<
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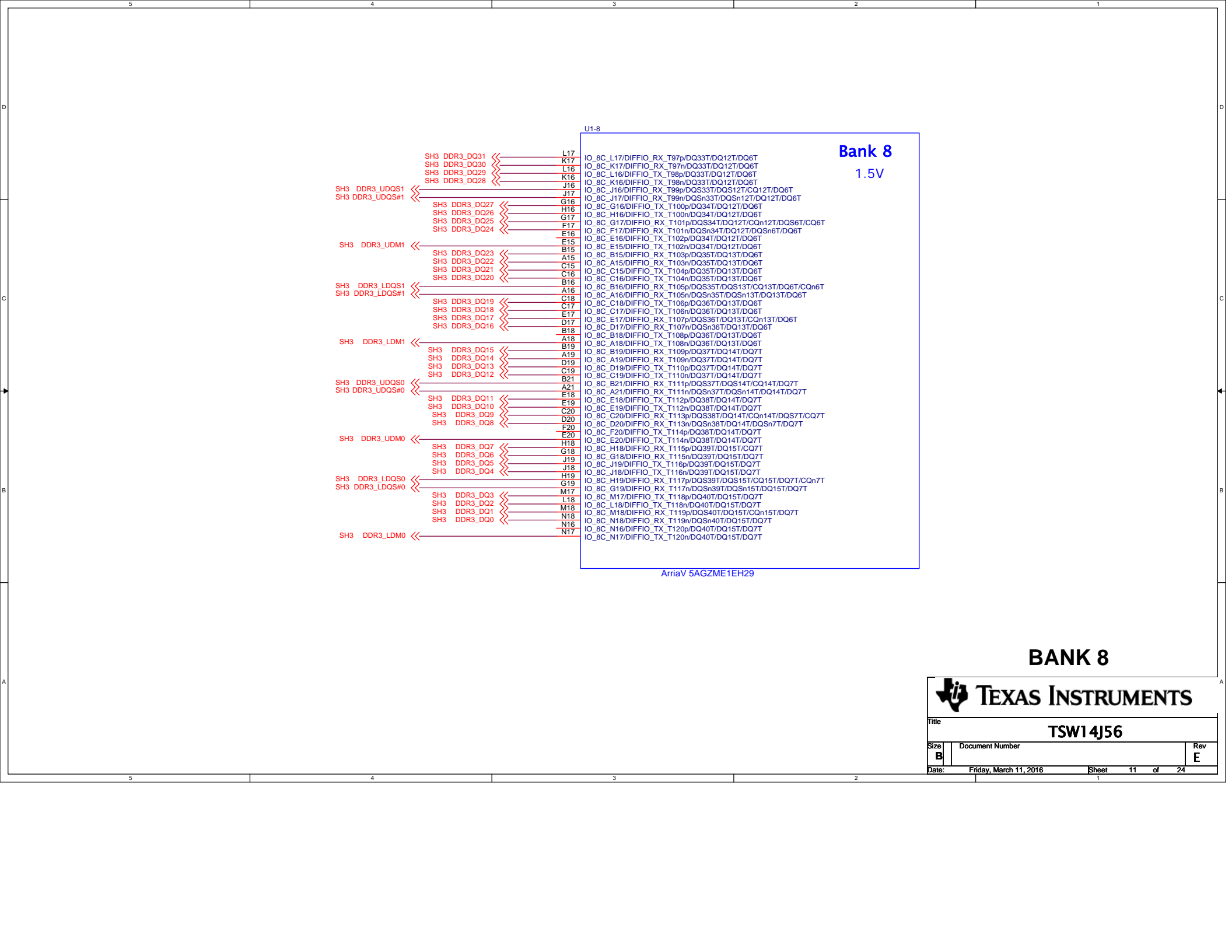
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H9 IO_7C_H9/DIFFIO_TX_T38p/DQ13T/DQ3T/DQ2T
G9 IO_7C_G9/DIFFIO_TX_T38n/DQ13T/DQ3T/DQ2T
G7 IO_7C_G7/DIFFIO_RX_T39p/DQS13T/DQS3T/CQ3T/DQ2T
G8 IO_7C_G8/DIFFIO_RX_T39n/DQS13T/DQS3T/DQ3T/DQ2T
H7 IO_7C_H7/DIFFIO_TX_T40p/DQ14T/DQ3T/DQ2T
J7 IO_7C_J7/DIFFIO_TX_T40n/DQ14T/DQ3T/DQ2T
K8 IO_7C_K8/DIFFIO_RX_T41p/DQS14T/DQ3T/CQn3T/DQS2T/CQ2T
J9 IO_7C_J9/DIFFIO_RX_T41n/DQS14T/DQ3T/DQS2T/DQ2T
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C2 IO_7C_C2/DIFFIO_RX_T43n/DQ15T/DQ4T/DQ2T
C1 IO_7C_C1/DIFFIO_TX_T44p/DQ15T/DQ4T/DQ2T
B1 IO_7C_B1/DIFFIO_TX_T44n/DQ15T/DQ4T/DQ2T
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P7 IO_7C_P7/DIFFIO_TX_T58n/DQ20T/DQ6T/DQ3T
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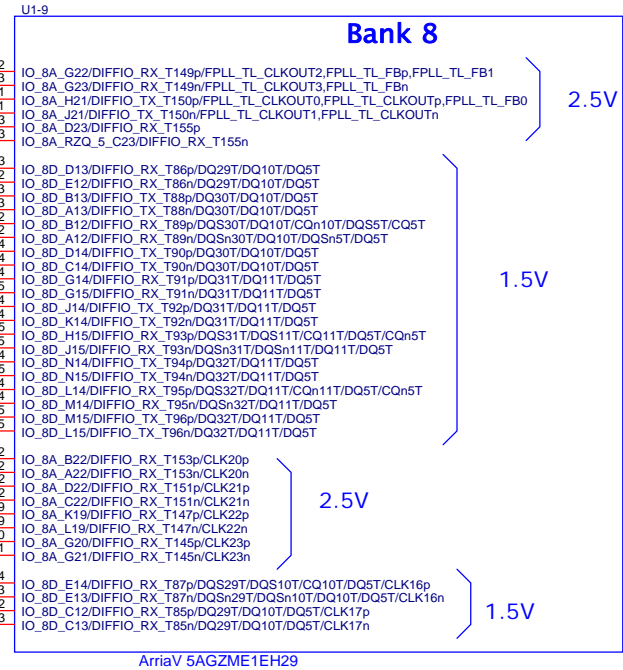
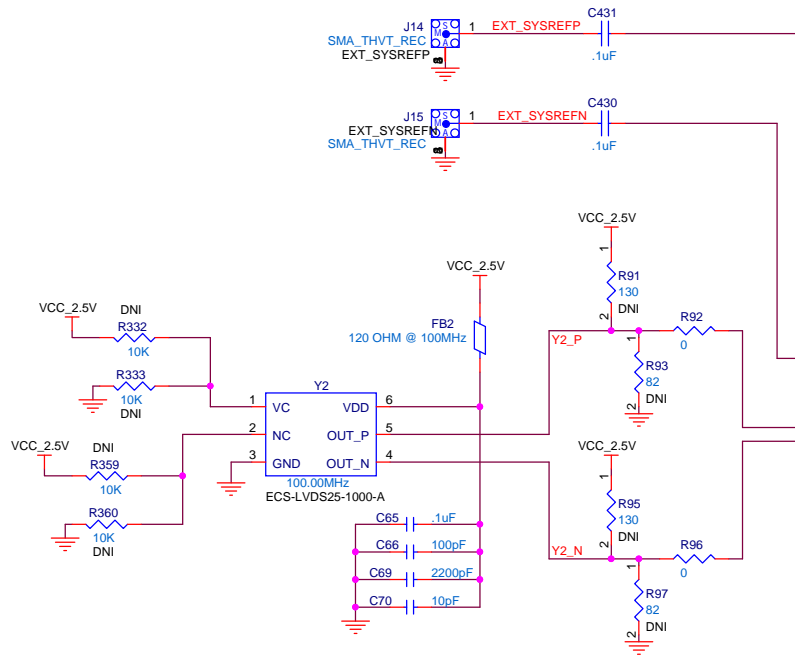
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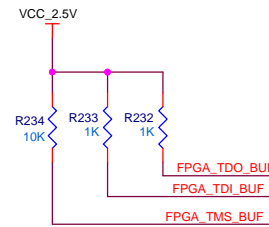
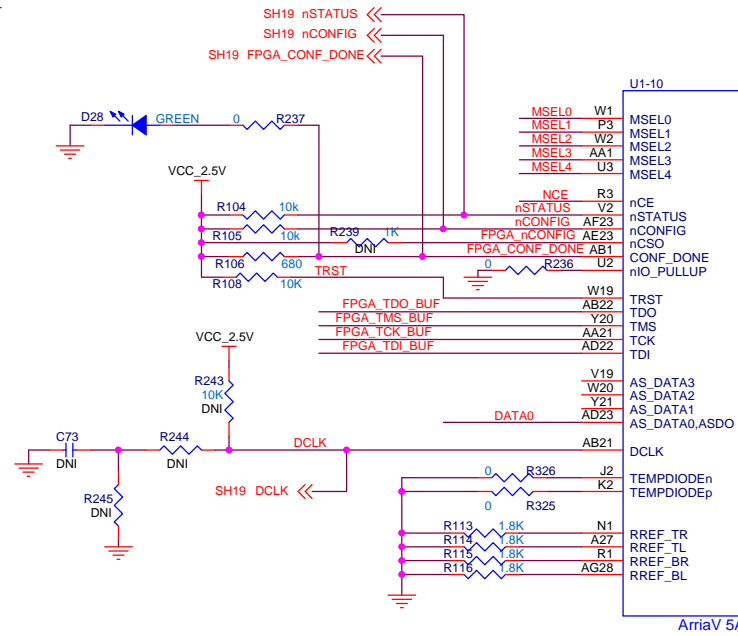
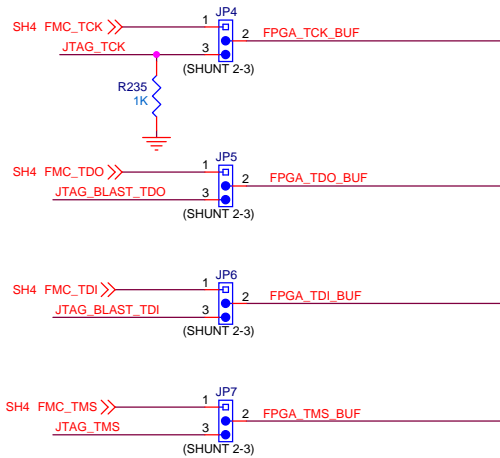
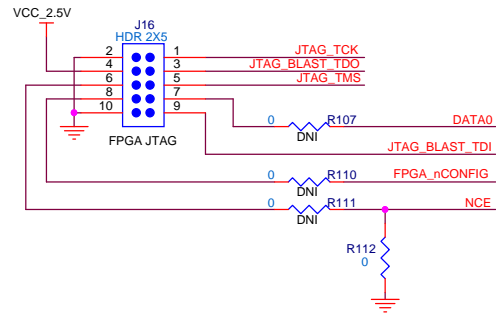
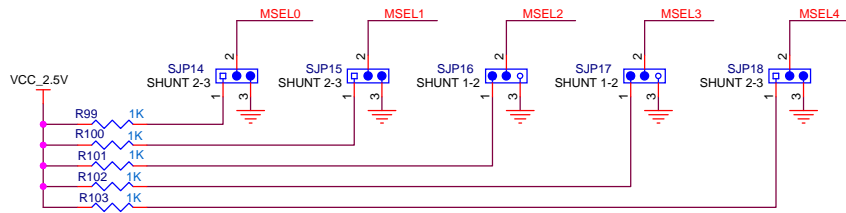
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FPGA CONFIGURATION

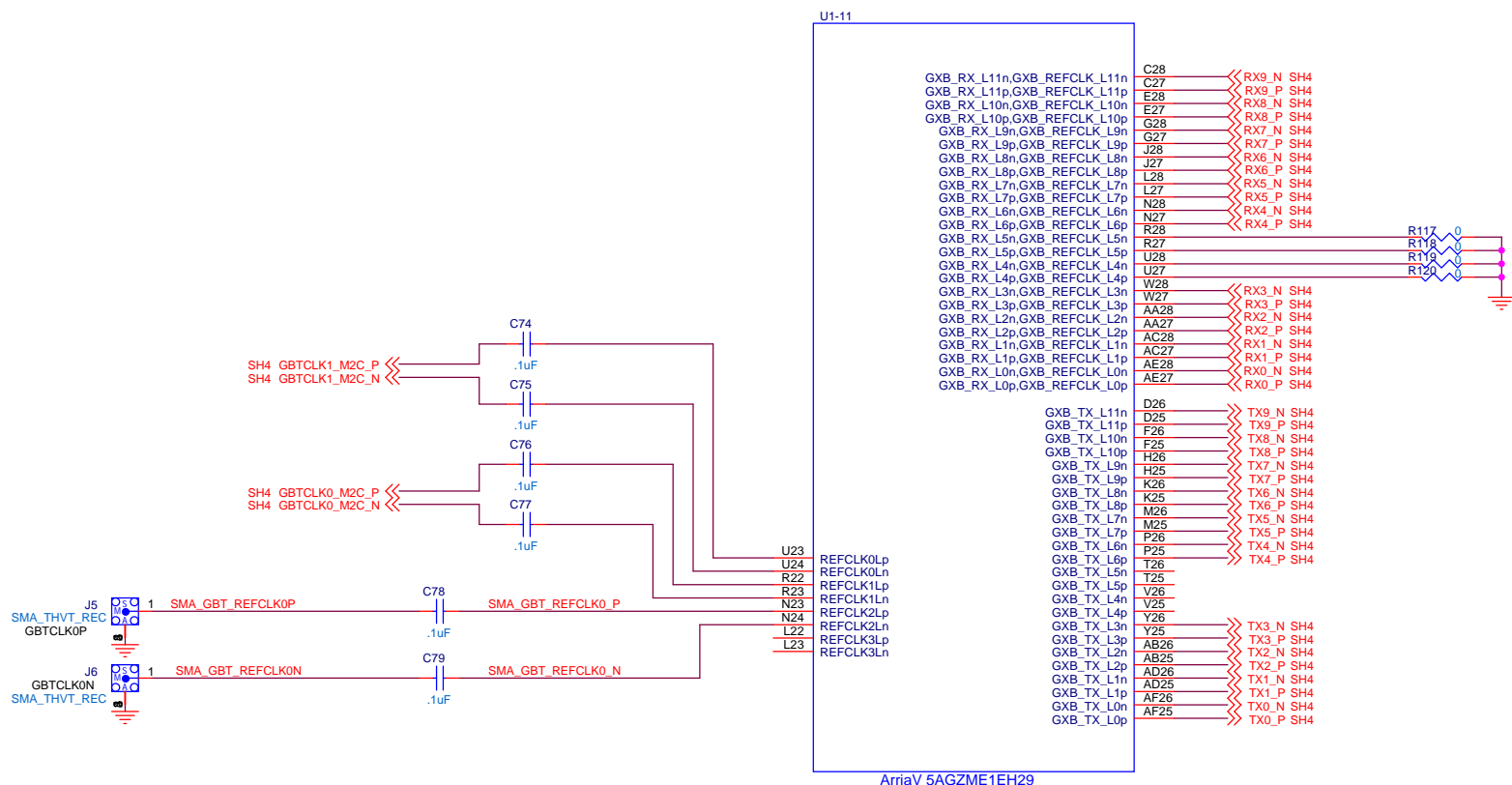


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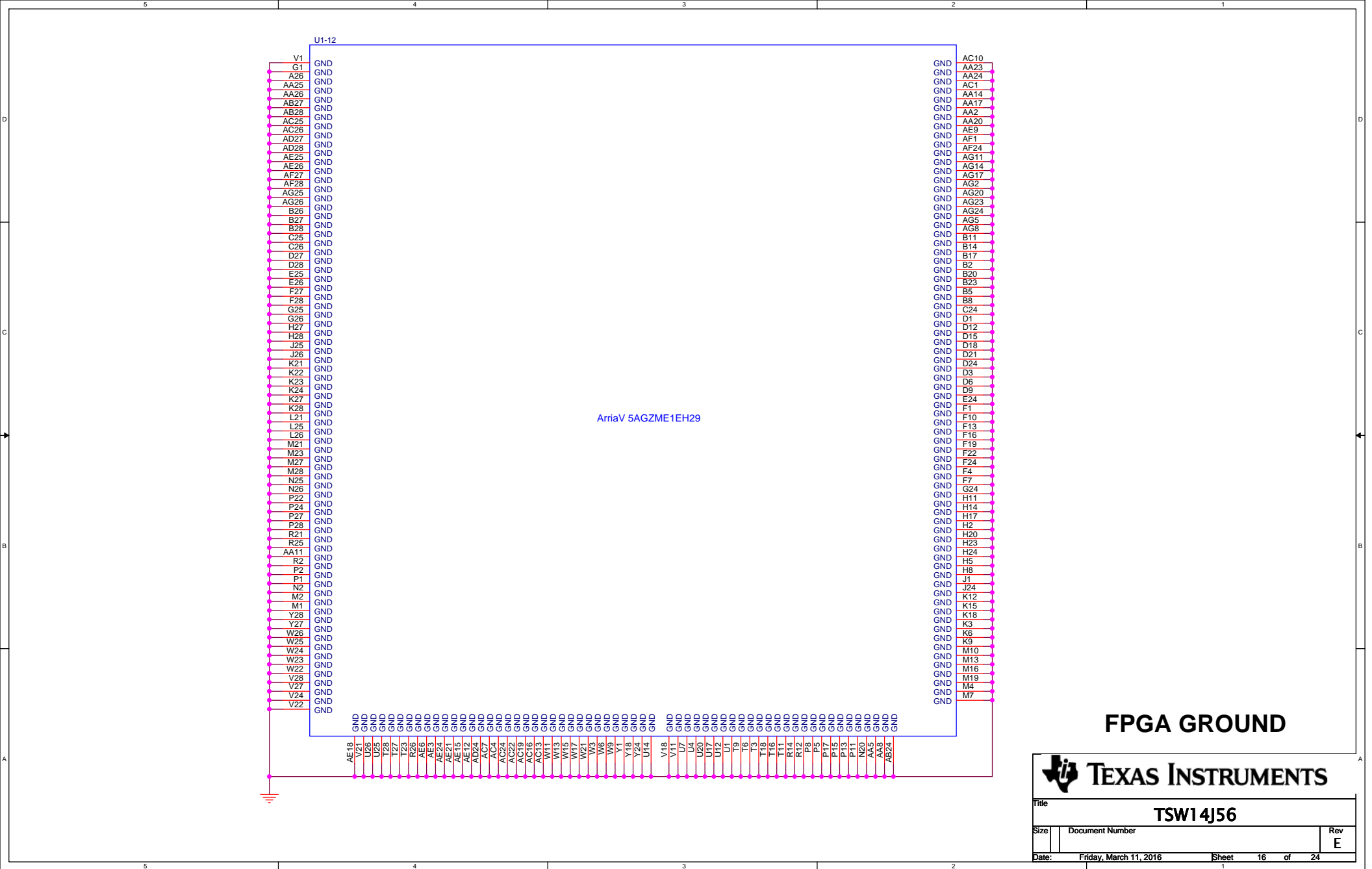
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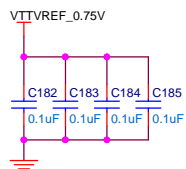
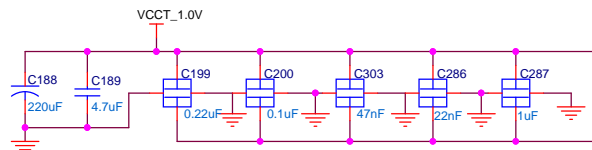
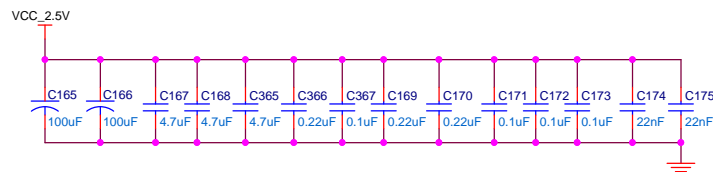
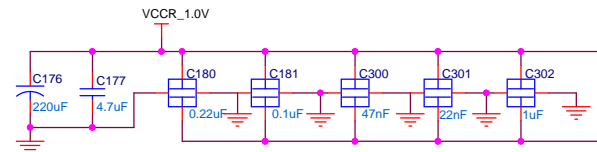
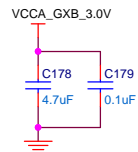
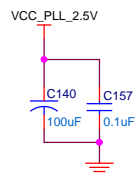
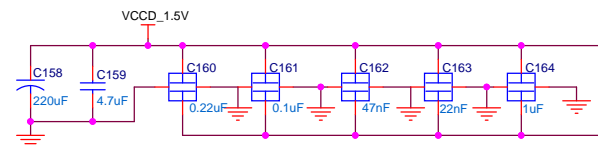
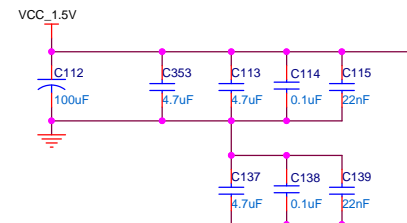
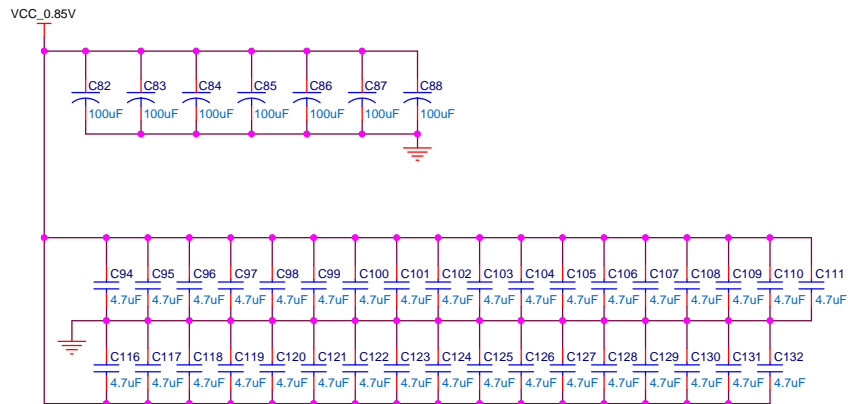
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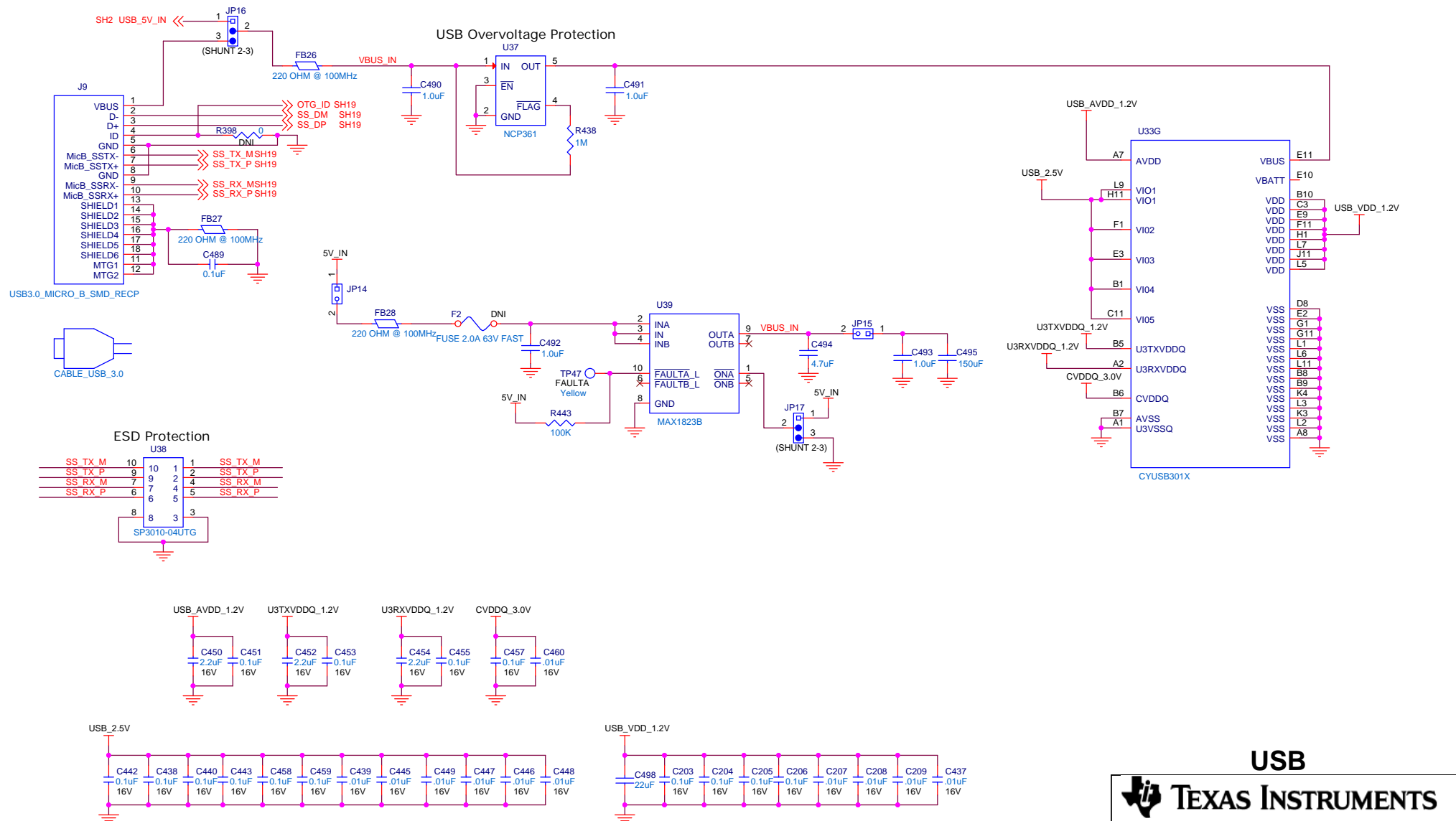
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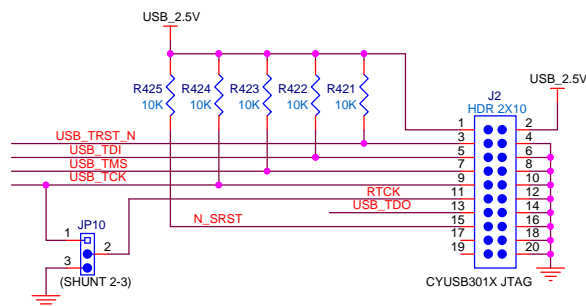
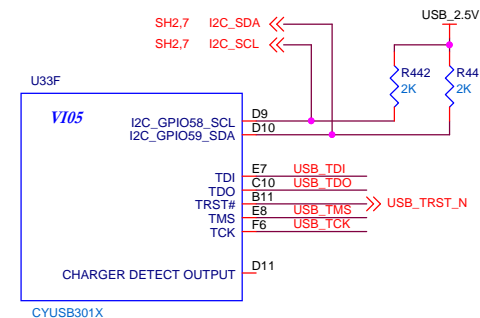
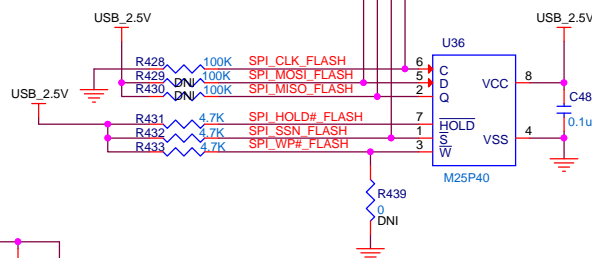
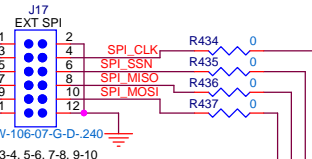
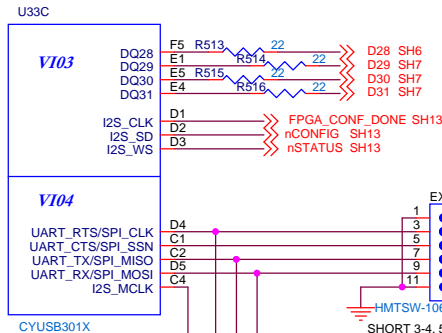
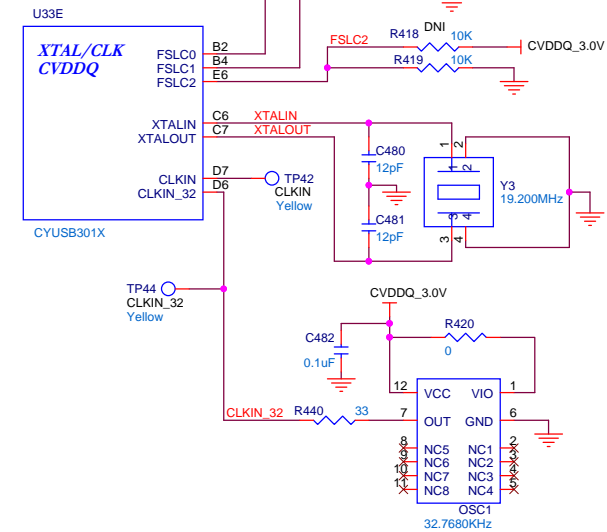
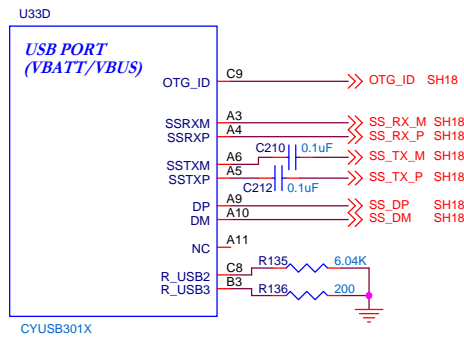
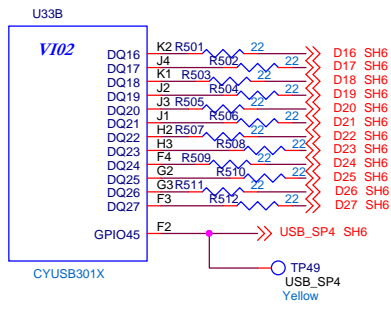
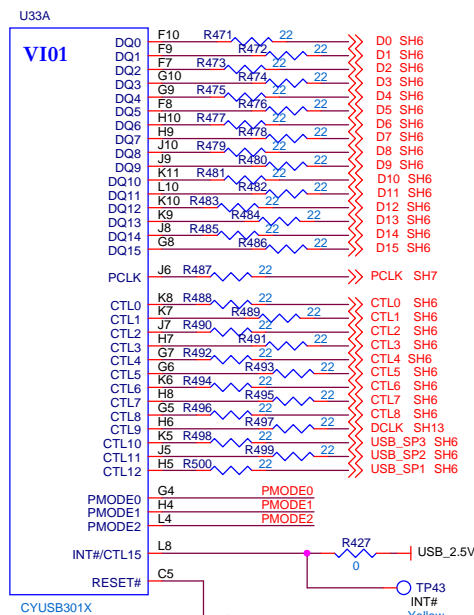
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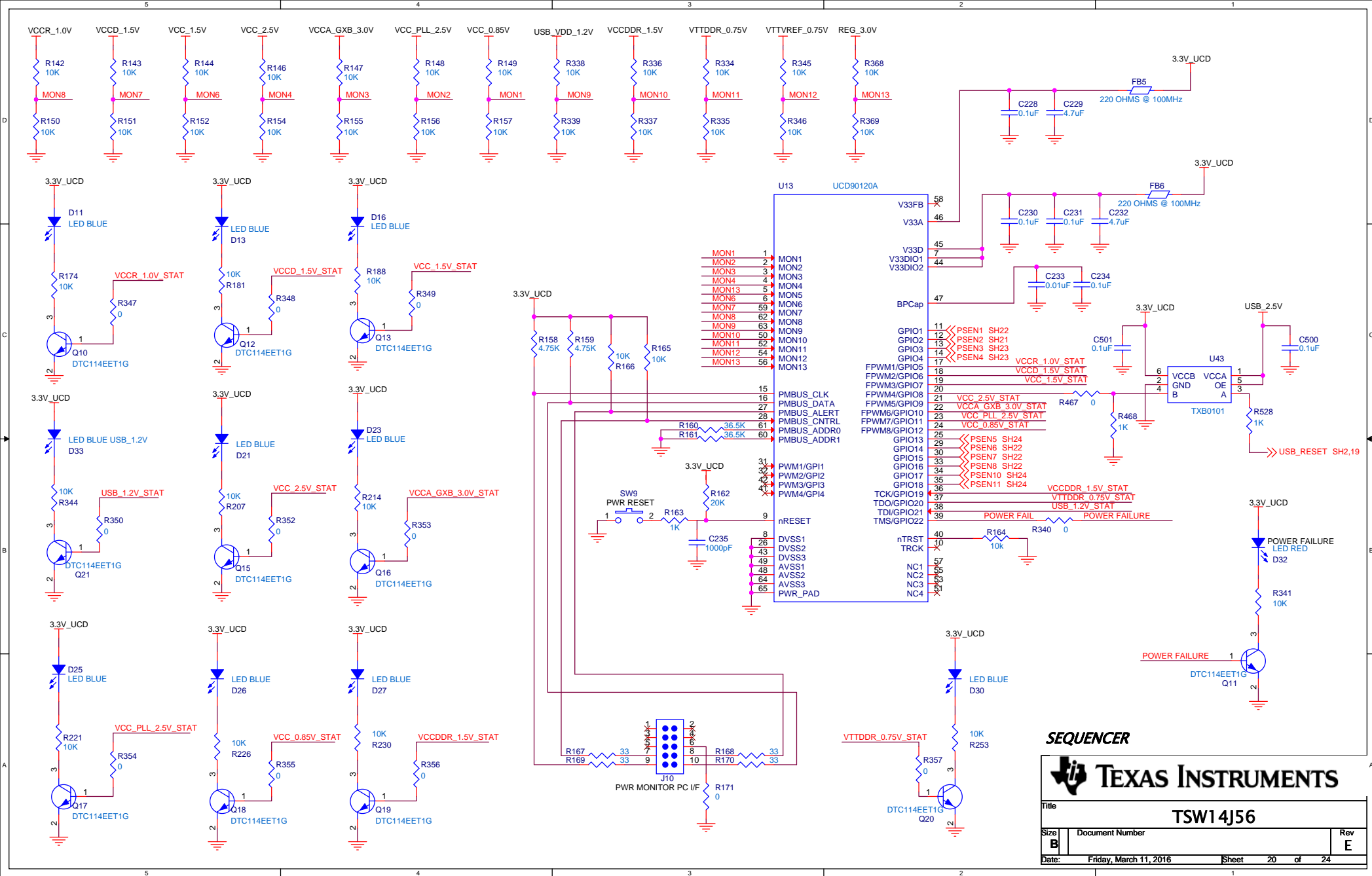
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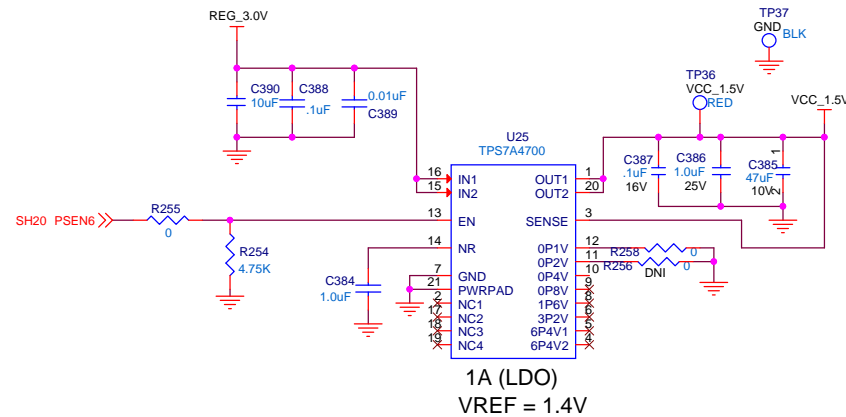
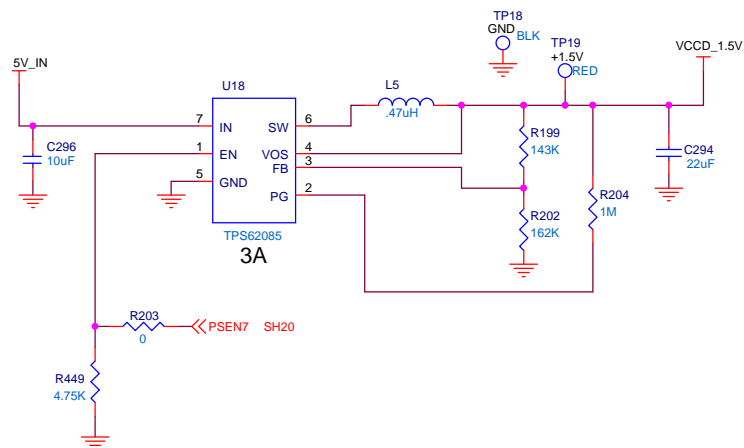
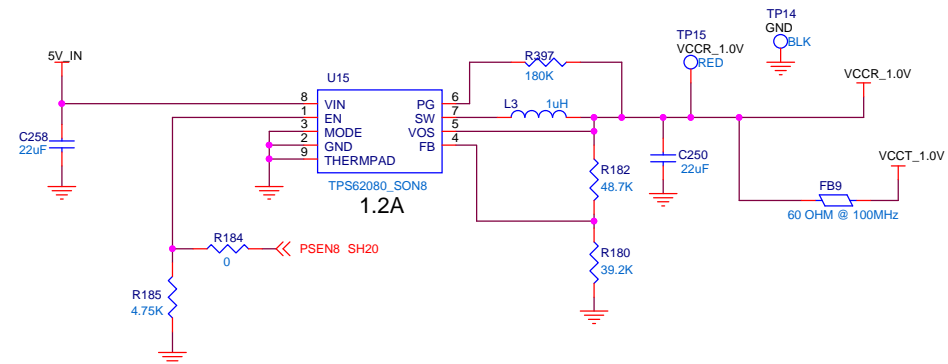
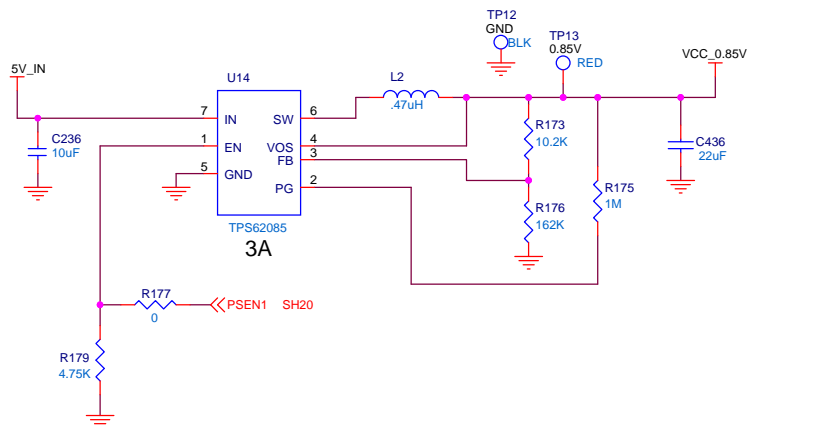


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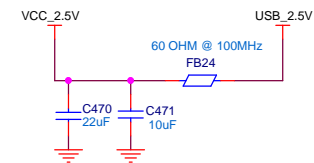
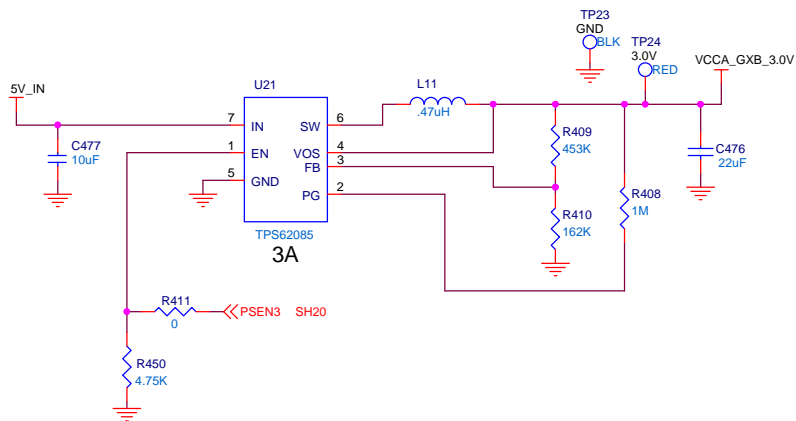
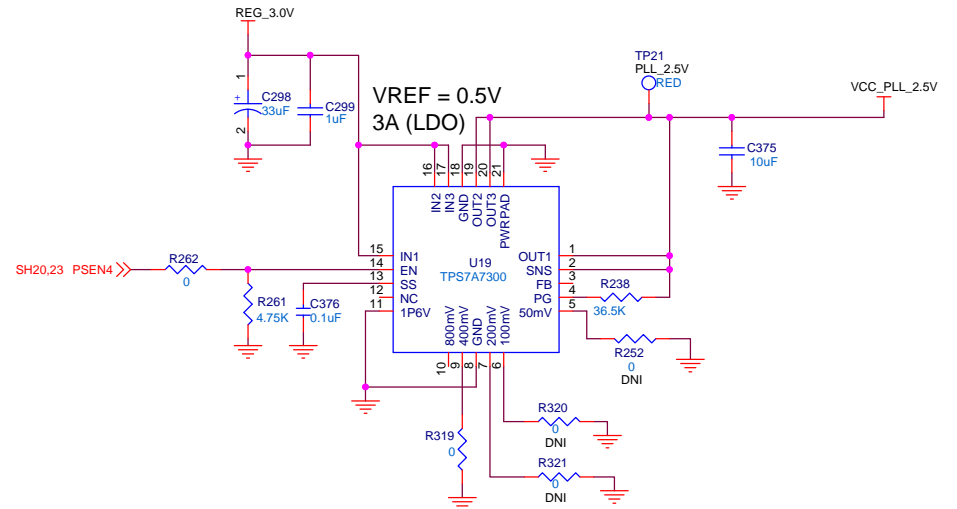
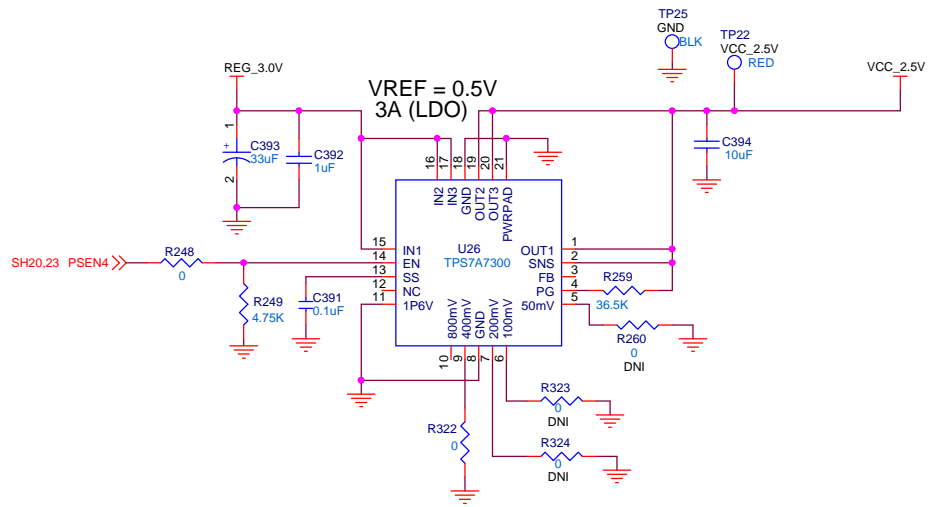




FPGA SUPPLIES



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