

* Model Usage Notes:

* A. Features have been modelled

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- * 1. Output Voltage Setting
- * 2. Internal Fixed Soft-Start
- * 3. EN On/off
- * 4. UVLO Protection
- * 5. Low-side FET Zero-Crossing(ZC)
- * 6. Negative Over current Protection(NOC)
- * 7. Overcurrent Protection(OCP)
- * 8. 16 Cycles of Consecutive Negative OCL Results to Device Operating in PSM
- * 9. Large Duty cycle operation

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* B. Features have not been modelled

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- * 1. Operating Quiescent Current
- * 2. Shutdown Current
- * 3. Temperature dependent characteristics

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* C. Application Notes

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- * 1. The parameter STEADY_STATE has been used to reach the steady state faster
 - * Keep STEADY_STATE = 0 to observe startup behaviour.
 - * Keep STEADY_STATE = 1 and appropriate IC on Inductor and capacitor to observe for faster Steady state.
- * 2. Ground Pins have been tied to 0V internally and hence model does not support Inverting topologies.

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