


	REVISIONS		
REV	DESCRIPTION	APPRVD	DATE
A	INITIAL RELEASE		03-11-08
G	CORRECTED NOTES, CORRECTED ASSY VARIANT	J.JONES	08-01-08

NOTES: _____ UNLESS OTHERWISE SPECIFIED

1. SCHEMATIC DIAGRAM FOR THIS PRODUCT IS 8706000090-001.
2. ELECTRICAL COMPONENTS ARE IDENTIFIED BY REFERENCE DESIGNATOR ON ASSEMBLY DRAWING, BILL OF MATERIALS, AND SCHEMATIC DIAGRAM.
3. APPLICABLE BILL(S) OF MATERIAL FOR THIS ASSEMBLY DRAWING: 9806000090-000.
4. SOLDER PASTE GERBER PATTERN 810600090-000 MAY BE MODIFIED PER ASSEMBLY REQUIREMENTS.

5.  DENOTES ORIGIN 0x AND 0y FOR COMPONENT POSITIONS PER PICK PLACE FOR 600090.TXT FILE.

6. FID1 THRU FID5 ARE THE OPTICAL ALIGNMENT TARGETS FOR SURFACE MOUNTED COMPONENTS. FOR 0x AND 0y LOCATION REFER TO THE PICK PLACE FOR 600090<ASSY VARIANT>.TXT FILE.
7. ALL VENDOR IN-PROCESS MARKING, QA STAMPS, ETC., MUST BE PLACED ON THE BOTTOM SIDE OF BOARD.
8. EACH ASSEMBLED BOARD MUST BE PACKAGED IN AN ESD BAG AND SEALED WITH AN ESD LABEL.

		NSC CONFIDENTIAL DO NOT COPY, DISPLAY, OR USE DRAWING WITHOUT NSC AUTHORIZATION			
DO NOT SCALE DRAWING		<div> National Semiconductor™ INTERFACE PRODUCTS GROUP</div>			
SIGNATURES	DATE				
DRAWN BY: VIRGIL HUTCHINS	6-25-08	PCB ASSEMBLY DRAWING SD346 EVK			
CHECKED BY: JOHN JONES	08-01-08				
APPRVD BY: JOHN JONES	08-01-08	B	DWG 980600090-000	G	
APPRVD BY:					
		SCALE : NONE		SHEET 1 OF 2	
				REV	

