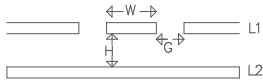


Symbol	Quantity	Finished Hole Size	Plated	Hole Type	Drill Layer Pair	Hole Tolerance
D	3	39.09mil (0.991mm)	NPTH	Round	Top Layer - Bottom Layer	
G	2	39.37mil (1.000mm)	NPTH	Round	Top Layer - Bottom Layer	
S	280	7.87mil (0.200mm)	PTH	Round	Top Layer - Bottom Layer	
*	6	10.00mil (0.254mm)	PTH	Round	Top Layer - Bottom Layer	
□	19	20.60mil (0.508mm)	PTH	Round	Top Layer - Bottom Layer	
▽	6	27.56mil (0.700mm)	PTH	Round	Top Layer - Bottom Layer	
M	2	33.47mil (0.850mm)	PTH	Round	Top Layer - Bottom Layer	
E	4	39.37mil (1.000mm)	PTH	Round	Top Layer - Bottom Layer	
A	3	40.00mil (1.016mm)	PTH	Round	Top Layer - Bottom Layer	
J	4	40.16mil (1.020mm)	PTH	Round	Top Layer - Bottom Layer	
R	2	43.31mil (1.100mm)	PTH	Round	Top Layer - Bottom Layer	
K	40	45.28mil (1.150mm)	PTH	Round	Top Layer - Bottom Layer	
F	4	51.18mil (1.300mm)	PTH	Round	Top Layer - Bottom Layer	
L	2	23.62mil (0.600mm)	PTH	Slot	Top Layer - Bottom Layer	
	377 Total					

Net definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.  
Hole Length = Routed Path Length - Tool size + Slot length as defined in the PCB layout

CONTROLLED IMPEDANCE USING CPW with GND

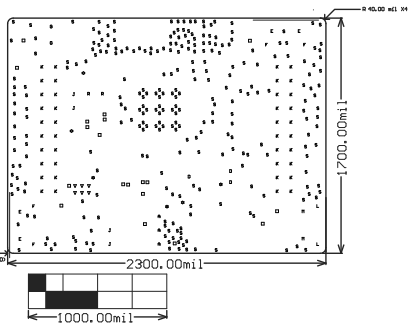
	GAP	IMPEDANCE +/- 10%	REFERENCE GND
	6.5 mil	50 OHMS	L2



Layer	Name	Material	Thickness	Constant	Board Layer Stack
1	Top Overlay				
2	Top Solder	Solder Resist	0.60mil	3.5	
3	Top Layer	Copper	1.80mil		
4	Dielectric1	FR-4	42.11mil	4.8	
5	Bottom Layer	Copper	1.80mil		
6	Bottom Solder	Solder Resist	0.60mil	3.5	
7	Bottom Overlay				

ADDITIONAL FAB NOTES

ALL OTHER DRILL TOLERANCE FOR PLATED +/-3 MIL AND NPTH +/-2 MIL  
(UNLESS OTHERWISE SPECIFIED)



DESIGN INFORMATION	
MIN. TRACK WIDTH:	6 MIL
MIN. CLEARANCE:	3 MIL
MIN. VIA PAD SIZE:	MIL
MINIMUM ANNULAR RING 0.05mm (2MIL) EXTERNAL PER IPC-D-275 CLASS 2 LEVEL C REGISTRATION TOLERANCES: METAL +/- .5 MIL, HOLES +/- .3 MIL	
MATERIAL:	
<input type="checkbox"/> FR-408	<input checked="" type="checkbox"/> FR-4 High Tg <input type="checkbox"/> OTHER
THICKNESS:	<input type="checkbox"/> 62 MIL (1.6mm) +/-10% <input checked="" type="checkbox"/> OTHER1.2 mm +/-10%
TOLERANCE:	<input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2 <input type="checkbox"/> OTHER +/-
BOW & TWIST:	<input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2 <input type="checkbox"/> OTHER +/-
DRILLING:	
REFERENCE:	<input checked="" type="checkbox"/> AS SHOWN <input checked="" type="checkbox"/> NC_DRILL FILES
PTH MIN COPPER THICKNESS:	<input checked="" type="checkbox"/> 1MIL <input type="checkbox"/> OTHER
BOARD FINISH:	
SILKSCREEN:	<input checked="" type="checkbox"/> TOP <input checked="" type="checkbox"/> BOTTOM
SILKSCREEN COLOR:	<input checked="" type="checkbox"/> WHITE <input type="checkbox"/> OTHER
SOLDER RESIST COLOR:	
<input type="checkbox"/> GREEN	<input type="checkbox"/> BLUE <input checked="" type="checkbox"/> OTHER <input type="checkbox"/> RED
SURFACE FINISH:	
<input checked="" type="checkbox"/> IMMERSION GOLD (ENG)	<input type="checkbox"/> ENERP
<input type="checkbox"/> IMM. TIN/SILVER OR EQUIV	<input type="checkbox"/> OTHER
ARRAY/PANEL:	
<input type="checkbox"/> CUT AND TRM PER MECH LAYER 1	<input checked="" type="checkbox"/> V. SCORE
<input type="checkbox"/> N.C. ROUTE	
CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBs TO MEET OR EXCEED THE REQUIREMENTS OF:	
<input checked="" type="checkbox"/> ANSI IPC-A-600F CLASS ->	<input type="checkbox"/> 1 <input checked="" type="checkbox"/> 2 <input type="checkbox"/> 3
<input checked="" type="checkbox"/> UL 94V-0	<input checked="" type="checkbox"/> RoHS <input type="checkbox"/> OTHER PER ORDER
ADDITIONAL REQUIREMENTS:	
MICROSECTION:	<input type="checkbox"/> YES
BARE BOARD ELEC. TEST:	<input type="checkbox"/> NONE <input checked="" type="checkbox"/> REQUIRED <input type="checkbox"/> PER ORDER
MANUFACTURER'S UL:	<input type="checkbox"/> RAL <input type="checkbox"/> METAL <input checked="" type="checkbox"/> SILK



PROJECT TITLE: BOOSTXL-CC3135MOD	
DESIGNED FOR: Public Release	
FILE NAME: BOOSTXL-CC3135MOD_PCB.PcbDoc	
ENGINEER: PMovva	LAYOUT BY: PMovva
SCALE: 1.00	ALTUM DESIGNER VERSION: 18.1.9.240

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: MCU064	REV: A	SUN REV: Not In VersionControl
LAYER NAME = FAB			
PLOT NAME = Drill Drawing For (Bottom Layer) : 7/26/2019 12:48:20 PM	TEXAS INSTRUMENTS		

Texas Instruments (TI) and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. TI and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. TI and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.