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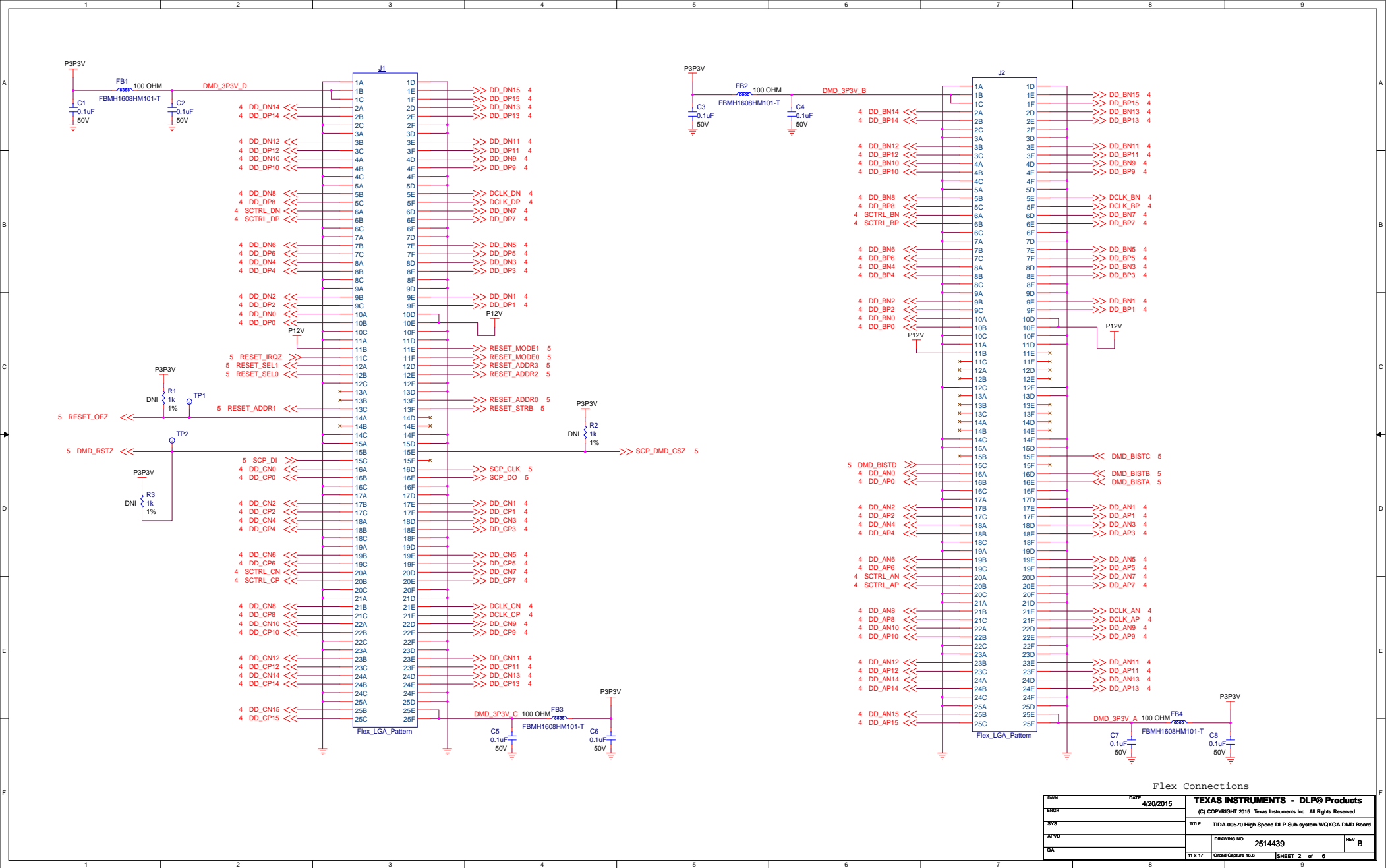
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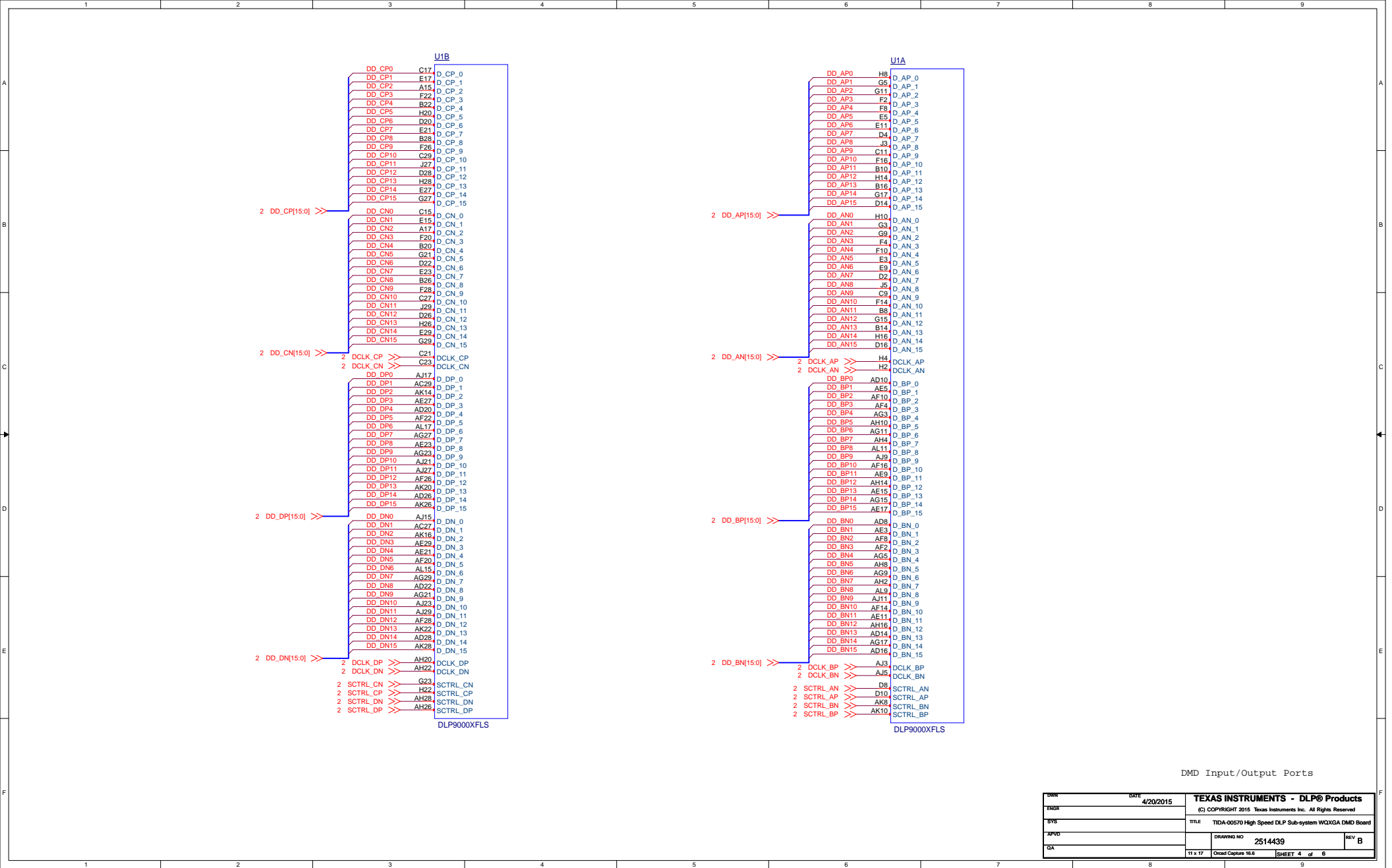
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REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	EOD Initial release	4/20/2015	NG
B		9/1/2015	MR

DWN	DATE	TEXAS INSTRUMENTS - DLP® Products	
ESOR	4/20/2015	(C) COPYRIGHT 2015 Texas Instruments Inc. All Rights Reserved	
SYS		TITLE	TIDA-00570 High Speed DLP Sub-system WQXGA DMD Board
APVD		DRAWING NO	2514439
DA		REV	B
11 x 17		Sheet Capture V16.0	SHEET 1 of 6





DMD Input/Output Ports

DATE	4/20/2015	TEXAS INSTRUMENTS - DLP® Products	
ENGR		(C) COPYRIGHT 2015 Texas Instruments Inc. All Rights Reserved	
SVS		TITLE TIDA-00570 High Speed DLP Sub-system WQXGA DMD Board	
APVD		DRAWING NO	2514439
QA		11 x 17	Sheet Capture 16.6
		SHEET 4 of 6	
		REV B	



DATE	4/20/2015		TEXAS INSTRUMENTS - DLP® Products	
ENGR			(C) COPYRIGHT 2015 Texas Instruments Inc. All Rights Reserved	
SYS			TITLE TIDA-00570 High Speed DLP Sub-system WQXGA DMD Board	
APVD			DRAWING NO 2514439	
QA			REV B	
			11 x 17 Onceal Capture 16.6 SHEET 5 of 6	

Revisions

Rev. A : Initial Release
Rev. B : Pinnout of Q1 corrected, Reversed pin1 and pin2 on symbol. 9/1/2015.
Rev. B : Pinnout of Q4 corrected, Reversed pin1 and pin2 on symbol. 9/1/2015.
Rev. B : Pinnout of Q7 corrected, Reversed pin1 and pin2 on symbol. 9/1/2015.

REVISION NOTES

DRN	DATE	TEXAS INSTRUMENTS - DLP® Products		
ENGR	4/20/2015	(C) COPYRIGHT 2015 Texas Instruments Inc. All Rights Reserved		
BYB		TITLE TIDA-00570 High Speed DLP Sub-system WQXGA DMD Board		
APVD		DRAWING NO	2514439	REV B
QA		11 x 17	Overall Capture 10.6	SHEET 6 of 6