

TAPE AND REEL INFORMATION

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE


*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
SN65LVDS31DR	SOIC	D	16	2500	330.0	16.4	6.5	10.3	2.1	8.0	16.0	Q1
SN65LVDS31NSR	SO	NS	16	2000	330.0	16.4	8.2	10.5	2.5	12.0	16.0	Q1
SN65LVDS31PWR	TSSOP	PW	16	2000	330.0	12.4	6.9	5.6	1.6	8.0	12.0	Q1
SN65LVDS9638DGKR	VSSOP	DGK	8	2500	330.0	12.4	5.3	3.4	1.4	8.0	12.0	Q1
SN65LVDS9638DGNR	HVSSOP	DGN	8	2500	330.0	12.4	5.3	3.4	1.4	8.0	12.0	Q1
SN65LVDS9638DR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1

TAPE AND REEL BOX DIMENSIONS


*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
SN65LVDS31DR	SOIC	D	16	2500	340.5	336.1	32.0
SN65LVDS31NSR	SO	NS	16	2000	356.0	356.0	35.0
SN65LVDS31PWR	TSSOP	PW	16	2000	350.0	350.0	43.0
SN65LVDS9638DGKR	VSSOP	DGK	8	2500	358.0	335.0	35.0
SN65LVDS9638DGNR	HVSSOP	DGN	8	2500	358.0	335.0	35.0
SN65LVDS9638DR	SOIC	D	8	2500	340.5	336.1	25.0

TUBE


*All dimensions are nominal

Device	Package Name	Package Type	Pins	SPQ	L (mm)	W (mm)	T (μm)	B (mm)
5962-9762101Q2A	FK	LCCC	20	55	506.98	12.06	2030	NA
5962-9762101QFA	W	CFP	16	25	506.98	26.16	6220	NA
SN55LVDS31W	W	CFP	16	25	506.98	26.16	6220	NA
SN65LVDS31D	D	SOIC	16	40	507	8	3940	4.32
SN65LVDS31DG4	D	SOIC	16	40	507	8	3940	4.32
SN65LVDS31PW	PW	TSSOP	16	90	530	10.2	3600	3.5
SN65LVDS31PWG4	PW	TSSOP	16	90	530	10.2	3600	3.5
SN65LVDS3487D	D	SOIC	16	40	505.46	6.76	3810	4
SN65LVDS3487DG4	D	SOIC	16	40	505.46	6.76	3810	4
SN65LVDS9638D	D	SOIC	8	75	507	8	3940	4.32
SN65LVDS9638D	D	SOIC	8	75	505.46	6.76	3810	4
SNJ55LVDS31FK	FK	LCCC	20	55	506.98	12.06	2030	NA
SNJ55LVDS31W	W	CFP	16	25	506.98	26.16	6220	NA