

TAPE AND REEL INFORMATION

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE

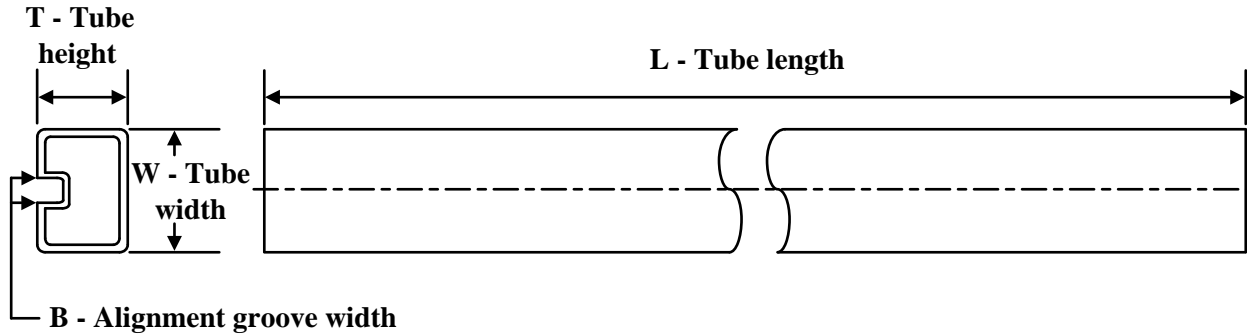

*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TLE2021AIDR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
TLE2021IDR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
TLE2022ACDR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
TLE2022AIDR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
TLE2022AMDR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
TLE2022AMDRG4	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
TLE2022CDR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
TLE2022IDR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
TLE2022MDR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
TLE2022MDRG4	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1

TAPE AND REEL BOX DIMENSIONS


*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TLE2021AIDR	SOIC	D	8	2500	353.0	353.0	32.0
TLE2021IDR	SOIC	D	8	2500	353.0	353.0	32.0
TLE2022ACDR	SOIC	D	8	2500	353.0	353.0	32.0
TLE2022AIDR	SOIC	D	8	2500	353.0	353.0	32.0
TLE2022AMDR	SOIC	D	8	2500	353.0	353.0	32.0
TLE2022AMDRG4	SOIC	D	8	2500	353.0	353.0	32.0
TLE2022CDR	SOIC	D	8	2500	353.0	353.0	32.0
TLE2022IDR	SOIC	D	8	2500	353.0	353.0	32.0
TLE2022MDR	SOIC	D	8	2500	353.0	353.0	32.0
TLE2022MDRG4	SOIC	D	8	2500	353.0	353.0	32.0

TUBE


*All dimensions are nominal

Device	Package Name	Package Type	Pins	SPQ	L (mm)	W (mm)	T (µm)	B (mm)
TLE2021ACP	P	PDIP	8	50	506	13.97	11230	4.32
TLE2021ACP.A	P	PDIP	8	50	506	13.97	11230	4.32
TLE2021AIP	P	PDIP	8	50	506	13.97	11230	4.32
TLE2021AIP.A	P	PDIP	8	50	506	13.97	11230	4.32
TLE2021CP	P	PDIP	8	50	506	13.97	11230	4.32
TLE2021CP.A	P	PDIP	8	50	506	13.97	11230	4.32
TLE2021CPE4	P	PDIP	8	50	506	13.97	11230	4.32
TLE2021IP	P	PDIP	8	50	506	13.97	11230	4.32
TLE2021IP.A	P	PDIP	8	50	506	13.97	11230	4.32
TLE2021MD	D	SOIC	8	75	505.46	6.76	3810	4
TLE2021MD.A	D	SOIC	8	75	505.46	6.76	3810	4
TLE2021MDG4	D	SOIC	8	75	505.46	6.76	3810	4
TLE2021MDG4.A	D	SOIC	8	75	505.46	6.76	3810	4
TLE2022ACP	P	PDIP	8	50	506	13.97	11230	4.32
TLE2022ACP.A	P	PDIP	8	50	506	13.97	11230	4.32
TLE2022AIP	P	PDIP	8	50	506	13.97	11230	4.32
TLE2022AIP.A	P	PDIP	8	50	506	13.97	11230	4.32
TLE2022AMDG4	D	SOIC	8	75	505.46	6.76	3810	4
TLE2022AMDG4.A	D	SOIC	8	75	505.46	6.76	3810	4
TLE2022CP	P	PDIP	8	50	506	13.97	11230	4.32
TLE2022CP.A	P	PDIP	8	50	506	13.97	11230	4.32
TLE2022CPE4	P	PDIP	8	50	506	13.97	11230	4.32
TLE2022IP	P	PDIP	8	50	506	13.97	11230	4.32
TLE2022IP.A	P	PDIP	8	50	506	13.97	11230	4.32
TLE2022IPE4	P	PDIP	8	50	506	13.97	11230	4.32
TLE2024ACN	N	PDIP	14	25	506	13.97	11230	4.32
TLE2024ACN.A	N	PDIP	14	25	506	13.97	11230	4.32
TLE2024AIDW	DW	SOIC	16	40	506.98	12.7	4826	6.6
TLE2024AIDW.A	DW	SOIC	16	40	506.98	12.7	4826	6.6

Device	Package Name	Package Type	Pins	SPQ	L (mm)	W (mm)	T (µm)	B (mm)
TLE2024AIN	N	PDIP	14	25	506	13.97	11230	4.32
TLE2024AIN.A	N	PDIP	14	25	506	13.97	11230	4.32
TLE2024CN	N	PDIP	14	25	506	13.97	11230	4.32
TLE2024CN.A	N	PDIP	14	25	506	13.97	11230	4.32
TLE2024CNE4	N	PDIP	14	25	506	13.97	11230	4.32
TLE2024IN	N	PDIP	14	25	506	13.97	11230	4.32
TLE2024IN.A	N	PDIP	14	25	506	13.97	11230	4.32