Radiation Report

TPS7H60X3-SP Single-Event Effects (SEE) Report

ABSTRACT

The purpose of this study is to characterize the single-event effects (SEE) performance due to heavy-ion irradiation of the TPS7H6003-SP, TPS7H6013-SP, and TPS7H6023-SP. Heavy-ions with LET_{EFF} of 48, 65, and 75MeV × cm^2 / mg was used to irradiate four production devices. Flux of approximately 10^5 ions / cm^2 × s and fluence of approximately 10^7 ions / cm^2 per run were used for the characterization. The results demonstrate the performance of the TPS7H60x3-SP under SEL and SEB and SEGR conditions at T = 125°C and T = 25°C, respectively. SET transients performance for output pulse width excursions ≥ |20%| from the nominal width and positive and negative edge transients on HO and LO are presented and discussed.

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1 Introduction

The TPS7H60X3-SP is a radiation-hardness-assured (RHA) Gallium Nitride (GaN) Field Effect Transistor (FET) gate driver designed for high frequency, high efficiency applications. The driver features:

- Absolute Maximum Voltage ratings
  - TPS7H6003-SP: 200V
  - TPS7H6013-SP: 60V
  - TPS7H6023-SP: 22V
- Adjustable dead time (PWM mode)
- Approximately 30ns propagation delay
- Approximately 5.5ns high-side and low-side matching
- High-side and low-side 5V LDOs independent of supply voltage
- Two control input modes: Independent Input Mode (IIM) and PWM
  - IIM allows for outputs to be controlled by dedicated input
  - PWM allows for two complementary outputs signals to be generated from single input with resistor programmable dead-time

In IIM mode the user also has the ability to enable or disable the turn-on of both outputs when both inputs are on simultaneously (interlock protection). This gives the driver the ability to be used in multiple converter configurations.

The device is offered in a 48-pin ceramic package. General device information and test conditions are listed in Table 1-1. For more detailed technical specifications, user guides, and application notes, see the TPS7H6003-SP, the TPS7H6013-SP, or the TPS7H6023-SP product pages.

Table 1-1. Overview Information

Description(1)	Device Information
TI part number	TPS7H6003-SP, TPS7H6013-SP, TPS7H6023-SP
Orderable number	5962R2220101VXC, 5962R2220102VXC, 5962R2220103VXC
Device function	200, 60, or 22V half-bridge eGaN gate driver
Technology	LBC7 (Linear BiCMOS 7)
Exposure facility	Radiation Effects Facility, Cyclotron Institute, Texas A&M University (15 MeV / nucleon)
Heavy ion fluence per run	$9.97 \times 10^6 - 1 \times 10^7$ ions / cm^2
Irradiation temperature	25°C (for SEB/SEGR testing), 25°C (for SET testing), and 125°C (for SEL testing)

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2 Single-Event Effects (SEE)

SEE testing was performed on an evaluation board designed for testing the TPS7H60x3-SP under heavy-ion radiation. The board was powered up in different input and output conditions at Texas A&M University to cover the spectrum of destructive SEE (DSEE) and Single-Event Transients (SET). The devices were tested at the TAMU Cyclotron Radiation Effects Facility using a superconducting cyclotron and an advanced electron cyclotron resonance (ECR) ion source. DSEE testing included Single-Event Latch-up (SEL), Single-Event Burnout (SEB), and Single-Event Gate Rupture (SEGR). In mixed technologies such as the BiCMOS process used on the TPS7H60x3-SP, the CMOS circuitry introduces a potential for SEL susceptibility.

SEL can occur if excess current injection caused by the passage of an energetic ion is high enough to trigger the formation of a parasitic cross-coupled PNP and NPN bipolar structure (formed between the p-sub and n-well and n+ and p+ contacts) [1,2]. The parasitic bipolar structure initiated by a single-event creates a high-conductance path (inducing a steady-state current that is typically orders-of-magnitude higher than the normal operating current) between power and ground that persists (is *latched*) until power is removed, the device is reset, or until the device is destroyed by the high-current state. The TPS7H60x3-SP was tested for SEL at the maximum recommended input voltage (V_{IN}) of 14V and the maximum recommended boot voltage (V_{BOOT}) of 14V. The ASW (High-Side Driver Signal Return) was set to different voltages depending on variant. The TPS7H6003-SP had ASW set to 150V, the TPS7H6013-SP had ASW set to 45V, and the TPS7H6023-SP had ASW set to 14V as these are the maximum recommended operating conditions for the devices. Three different operation modes were tested during SEL testing. The first mode was PWM mode with the EN (HI) and PWM (LI) inputs in the following configuration:

- EN/HI:
  - 14V DC signal (SEL)
- PWM/LI:
  - 14V square wave switching at 500kHz, 1MHz, and 2MHz (SEL)

The second and third modes of operation were IIM_{EN} (where the optional interlock protection is enabled) and IIM_{DIS} (where the optional interlock protection is disabled) mode (for the IIM modes there are static (IIM_{ST}) and switching (IIM_{SW}) cases) in which EN (HI) and PWM (LI) were configured in the following manner (both cases were tested under the same conditions):

- Case 1 - EN/HI = 0V, PWM/LI = 14V (Static SEL)
- Case 2 - EN/HI = 14V, PWM/LI = 0V (Static SEL)
- Case 3- EN/HI and PWM/LI = 14V square wave switching at 500kHz offset by 180° (Switching SEL)

During testing of the four devices, the TPS7H60X3-SP did not exhibit any SEL with heavy-ions with LET_{EFF} = 75MeV × cm^2 / mg at flux of approximately 10^5 ions / cm^2 × s, fluence of approximately 10^7 ions / cm^2, and a die temperature of 125°C.

The primary concern for SEB and SEGR was the power LDMOS of this device. Because of this, SEB/SEGR was evaluated up to the maximum V_{IN} and V_{BOOT} in both IIM and PWM mode. In IIM mode the TPS7H60X3-SP was also tested in the "Off" case in which both EN/HI and PWM/LI = 0V to determine if either of the outputs incorrectly turned on when the outputs must not have during heavy-ion radiation. Because it has been shown that the MOSFET susceptibility to burnout decrements with temperature (5), the device was evaluated while operating under room temperatures. The specific test conditions the device was tested are as follows:

PWM Mode:

- EN/HI:
  - 14V DC signal (SEB_{ON})
  - 0V DC signal (SEB_{OFF})
- PWM/LI:
  - 14V Square Wave switching at 500kHz, 1MHz, and 2MHz (SEB_{ON})
  - 0V DC signal (SEB_{OFF})

IIM Modes:

- Case 1- EN/HI = 0V, PWM/LI = 14V (Static SEB_{ON})
- Case 2 - EN/HI = 14V, PWM/LI = 0V (Static SEB_{ON})

- Case 3 - EN/HI = 0V, PWM/LI = 0V (SEB_{OFF})
- Case 4 - EN/HI and PWM/LI = 14V square wave switching at 500kHz offset by 180° (Switching SEB_{ON})

During the SEB/SEGR testing, not a single input current event was observed, demonstrating that the TPS7H60x3-SP is SEB/SEGR-free up to LET_{EFF} = 75MeV × cm^2 / mg at a flux of approximately 10^5 ions / cm^2 × s, fluences of approximately 10^7 ions / cm^2, and a die temperature of ≈25°C.

The TPS7H60x3-SP was characterized for SET with LET_{EFF} = 48 to 75MeV × cm^2 / mg at flux of approximately 10^5 ions / cm^2 × s, fluence of approximately 10^7 ions / cm^2, and a die temperature of 25°C. For SET the device operated at nominal operating conditions with a V_{IN} of 12V and V_{BOOT} of 12V with ASW at 150V. The specific test conditions for the devices for SET are as follows:

PWM Mode:

- EN/HI:
- 5V DC signal (SET)

PWM/LI:

- 5V Square Wave switching at 500kHz and 50% duty cycle (SET)

IIM Modes:

- Case 1 – EN/HI = 0V, PWM/LI = 5V (Static SET)
- Case 2 – EN/HI = 5V, PWM/LI = 0V (Static SET)
- Case 3 - EN/HI and PWM/LI = 5V square wave switching at 500kHz offset by 180° (Switching SET)

Under these conditions the device showed on SET signature which was self-recoverable without the need for external intervention in both PWM and IIM mode. In PWM mode and IIM_{SW} mode HO and LO were monitored to see if the output pulse width ever exceeded a 20 % trigger. In IIM_{ST} mode HO and LO were monitored to see if the signals triggered on either a positive or negative edge depending on whether HO or LO were forced high based on the input value on EN/HI and PWM/LI. In all cases transients lasted approximately 5µs before recovering back to normal operation. Transients are further discussed in the Single-Event Transients section. To see the SET results of the TPS7H60x3-SP, see Single-Event Transients (SET).

3 Device and Test Board Information

The TPS7H60x3-SP is packaged in a 48-pin ceramic package as shown in Figure 3-1. A TPS7H60X3-SP evaluation board made specifically for radiation testing was used to evaluate the performance and characteristics of the TPS7H60x3-SP under heavy ion radiation. The TPS7H60x3-SP evaluation board is shown in Figure 3-2. The board schematic is shown in Figure 3-3.

The package was delidded to reveal the die face for all heavy-ion testing.

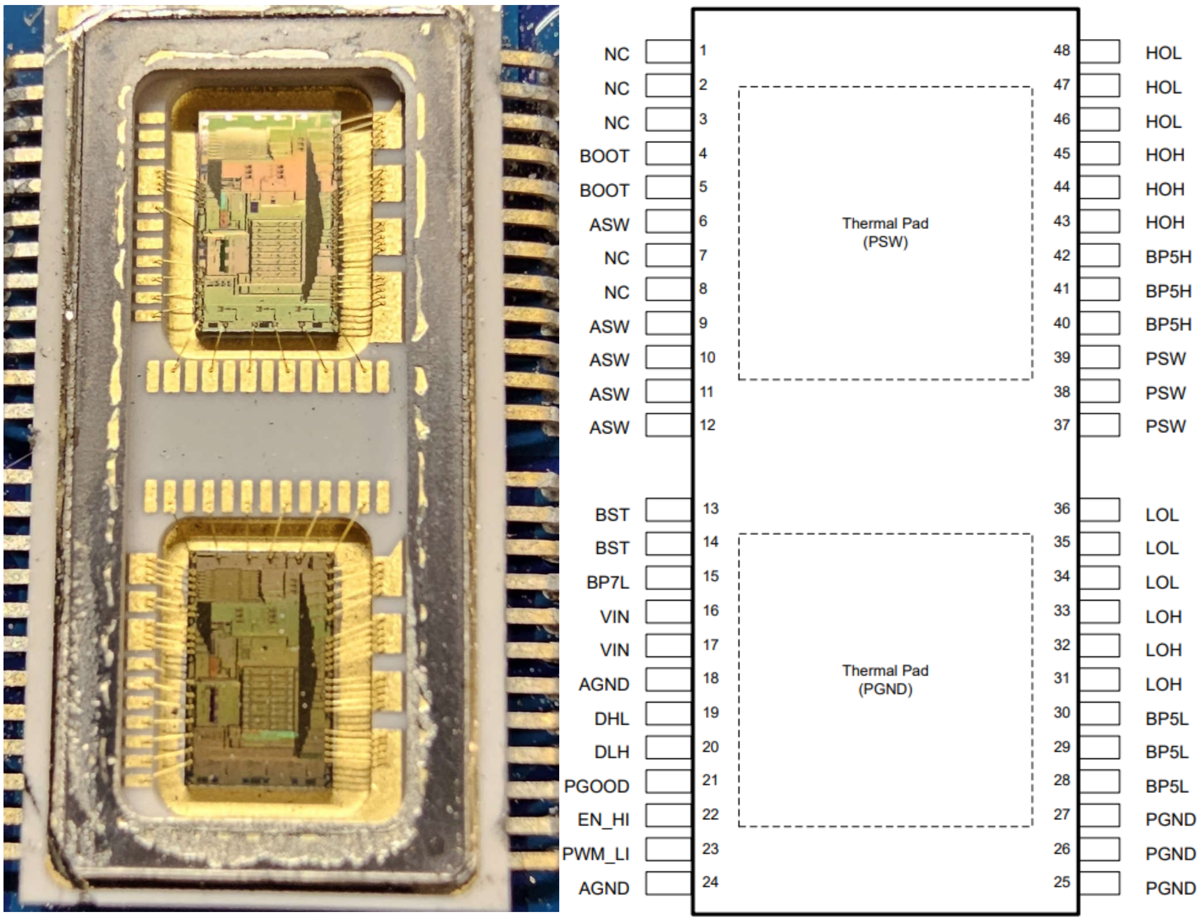

Figure 3-1. Photograph of Delidded TPS7H6003-SP (Left) and Pinout Diagram (Right)

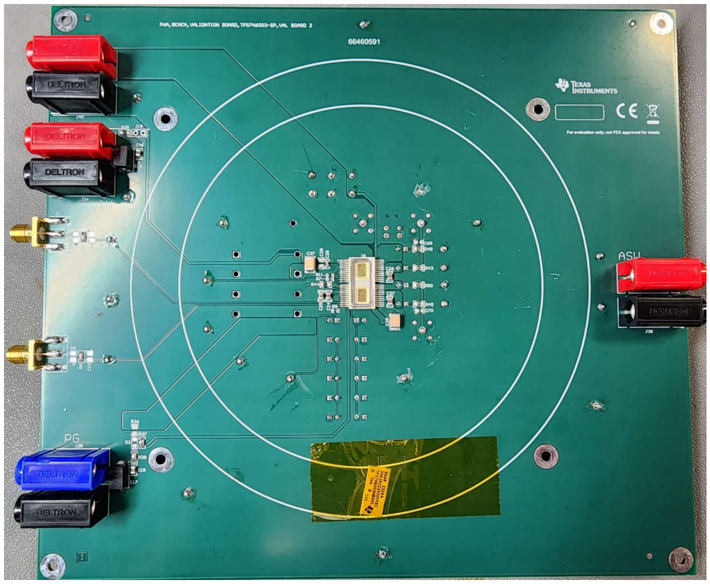

Figure 3-2. TPS7H60X3-SP EVM Top View

Although not shown here, there are 1nF capacitors on the HO and LO outputs. See the block diagram for the setup of the capacitive load.

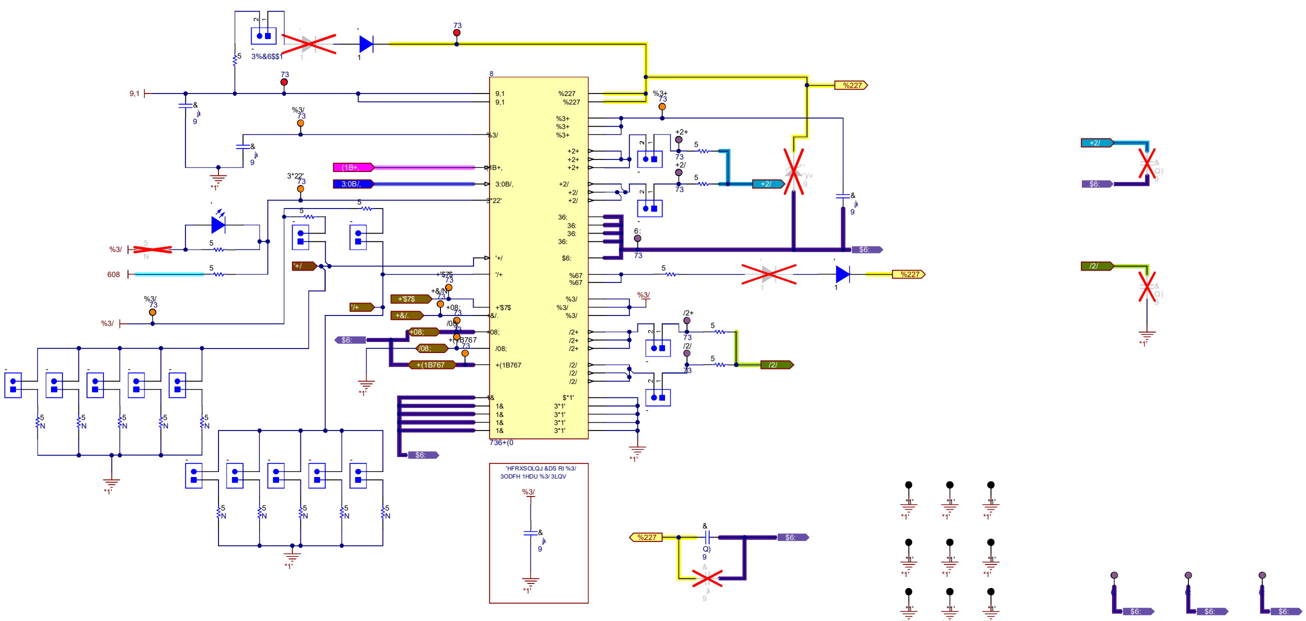

Figure 3-3. TPS7H60x3-SP Evaluation Board Schematics

4 Irradiation Facility and Setup

The heavy-ion species used for the SEE studies on this product were provided and delivered by the TAMU Cyclotron Radiation Effects Facility using a superconducting cyclotron and an advanced electron cyclotron resonance (ECR) ion source. At the fluxes used, ion beams had good flux stability and high irradiation uniformity over a 1-in diameter circular cross-sectional area for the in-air station. Uniformity is achieved by magnetic defocusing. The flux of the beam is regulated over a broad range spanning several orders of magnitude. For these studies, ion flux of 1.02×10^4 to 1.12×10^5 ions / cm^2 × s were used to provide heavy-ion fluences of 9.97×10^6 to 1.00×10^7 ions / cm^2.

For the experiments conducted on this report, there were three ions used, ^{109}Ag, ^{141}Pr, and ^{165}Ho. ^{109}Ag was used to obtain LET_{EFF} of 48MeV × cm^2 / mg. ^{141}Pr was used to obtain LET_{EFF} of 65MeV × cm^2 / mg. ^{165}Ho was used to obtain LET_{EFF} of 75MeV × cm^2 / mg. The total kinetic energies for each of the ions were:

- ^{109}Ag = 1.634 GeV (15 MeV/nucleon)
  - Ion uniformity for these experiments was 94%
- ^{141}Pr = 2.114 GeV (15MeV / nucleon)
- – Ion uniformity for these experiments was between 86 and 94%
- ^{165}Ho = 2.474 GeV (15MeV / nucleon)
  - Ion uniformity for these experiments was between 88 and 92%

Figure 4-1 shows the TPS7H60x3-SP Evaluation Board used for the data collection at the TAMU facility. Although not visible in this photo, the beam port has a 1mil Aramica window to allow in-air testing while maintaining the vacuum within the accelerator with only minor ion energy loss. The in-air gap between the device and the ion beam port window was maintained at 40mm for all runs.

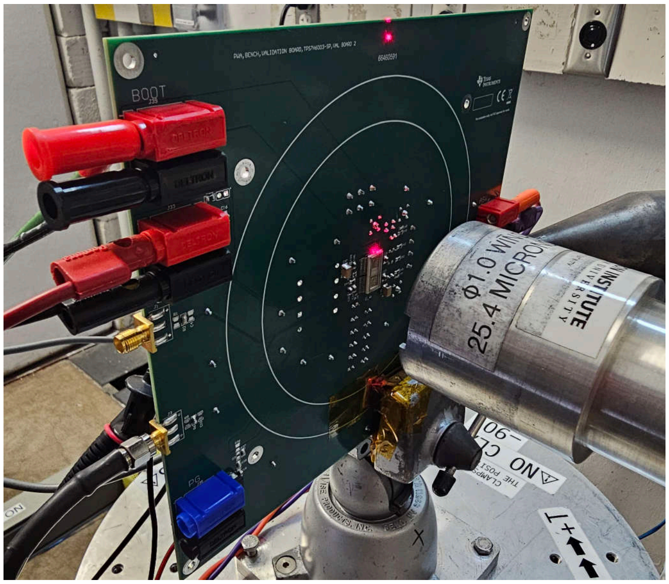

Figure 4-1. Photograph of the TPS7H60X3-SP Evaluation Board in Front of the Heavy-Ion Beam Exit Port at the Texas A&M Cyclotron

5 Depth, Range, and LET$_{EFF}$ Calculation

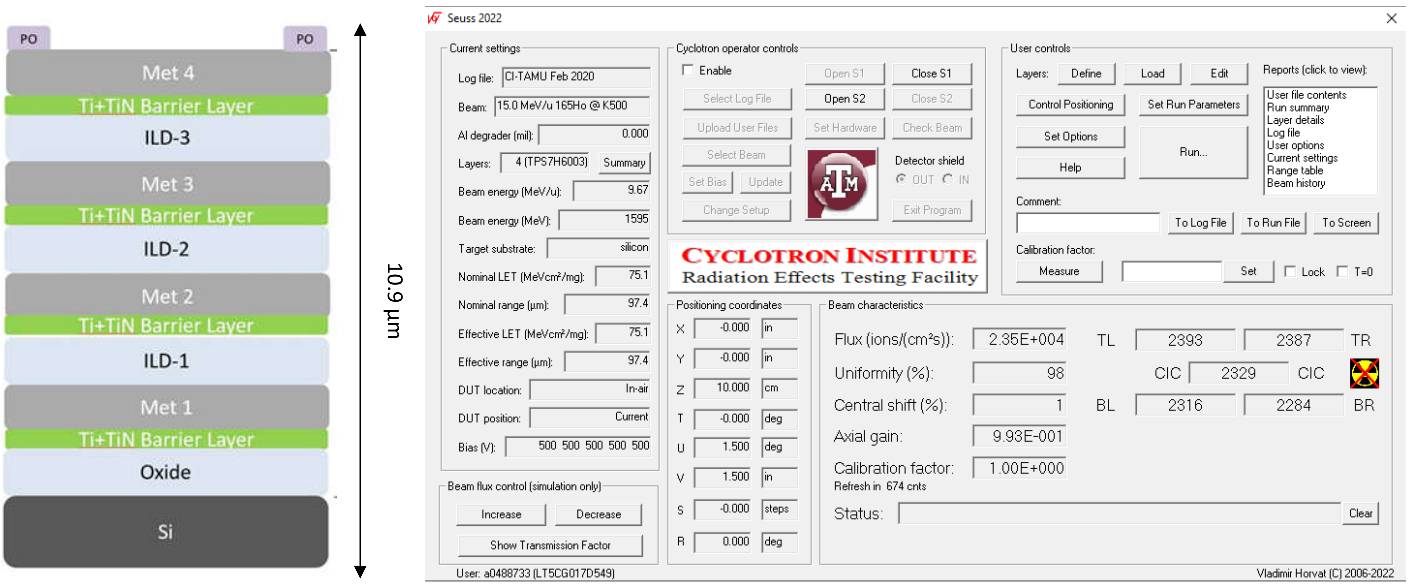

Figure 5-1. Generalized Cross-Section of the LBC7 Technology BEOL Stack on the TPS7H60X3-SP (Left) and SEUSS 2020 Application Used to Determine Key Ion Parameters (Right)

The TPS7H60X3-SP is fabricated in the TI Linear BiCMOS 250nm process with a 4LM back-end-of-line (BEOL) stack. The total stack height from the surface of the passivation to the silicon surface is 9.8µm based on nominal layer thickness as shown in Figure 5-1. Accounting for energy loss through the 1mil thick Aramica beam port window, the 40mm air gap, and the BEOL stack over the TPS7H60x3-SP, the effective LET (LET$_{EFF}$) at the surface of the silicon substrate and the depth was determined with the SEUSS 2020 Software (provided by the Texas A&M Cyclotron Institute and based on the latest SRIM-2013 [7] models). The results are shown in Ion LET$_{EFF}$, Depth, and Range in Silicon.

Table 5-1. Ion LET$_{EFF}$, Depth, and Range in Silicon

Ion Type	Beam Energy (MeV / nucleon)	Angle of Incidence	Degrader Steps (Number)	Degrader Angle	Range in Silicon (µm)	LET$_{EFF}$ (MeV × cm^2/ mg)
^{109}Ag	15	0	0	0	95.1	48
^{141}Pr	15	0	0	0	100.8	65
^{165}Ho	15	0	0	0	97.2	75

6 Test Setup and Procedures

There were five input supplies used to power the TPS7H60x3-SP which provided V_{IN}, V_{BOOT}, EN/HI, PWM/LI, and ASW (ASW with respect to AGND). The VIN for the device was provided through channel 3 of an N6705C power module and ranged from 12V to 14V for SET and DSEE, respectively. The V_{BOOT} for the device was provided by Channel 1 of an N6705C power module and ranged from 12V to 14V SET and DSEE respectively. EN/HI and PWM/LI were provided by a National Instruments PXIe-5433 2-channel AWG or a National Instruments PXIe-4139 depending on the type of test. Lastly, the ASW was provided by a National Instruments PXIe-4137 and forced to 150V.

The primary signals monitored on the EVM were HO and LO and this was done so using two instruments. The first was a NI PXIe-5110 which triggered (based on HO) in two ways, pulse-width at 20% outside width in PWM or IIM_{SW} mode, and window (± 500mV with signal AC coupled) in IIM_{ST} mode. The second instrument was a MSO58B oscilloscope which triggered in a similar manner for the LO signal while also monitoring the BP5L signal.

All equipment other than the MSO58B was controlled and monitored using a custom-developed LabVIEW™ program (PXI-RadTest) running on a HP-Z4® desktop computer. The computer communicates with the PXI chassis through an MXI controller and NI PXIe-8381 remote control module. The MSO58B was used using the manufacturer interface. The MSO was set to fast-frame for all SET data collection.

Table 6-1 lists the connections, limits, and compliance values used during the testing. Figure 6-1 shows a block diagram of the setup used for SEE testing of the TPS7H6003-SP.

Table 6-1. Equipment Settings and Parameters Used During the SEE Testing of the TPS7H60X3-SP

Pin Name	Equipment Used	Capability	Compliance	Range of Values Used
V_{IN}	N6705C (CH # 3)	20.4V, 50A	5A	12 to 14V
V_{BOOT}	N6705C (CH # 1)	60V, 20A	5A	12 to 14V
ASW	PXIe-4137	200V, 1A	.5A	14 to 150V
EN/HI	PXIe-5433 (CH # 0)	$24V_{PK-PK}$, 80MHz	—	5V to 14V, 500kHz to 2MHz
	PXIe-4139	60V, 3A	3A	14V
PWM/LI	PXIe-5433 (CH # 1)	$24V_{PK-PK}$, 80MHz	—	5V to 14V, 500kHz to 2MHz
	PXIe-4139	60V, 3A	3A	14V
LO, BP5L	MSO58B	6.25GS / s	—	1GS / s
HO	PXIe-5110	100MS / s	—	100MS / s

All boards used for SEE testing were fully checked for functionality. Dry runs were also performed to ensure that the test system was stable under all bias and load conditions prior to being taken to the TAMU facility. During the heavy-ion testing, the LabVIEW control program powered up the TPS7H60x3-SP device and set the external sourcing and monitoring functions of the external equipment. After functionality and stability was confirmed, the beam shutter was opened to expose the device to the heavy-ion beam. The shutter remained open until the target fluence was achieved (determined by external detectors and counters). During irradiation, the NI scope cards continuously monitored the signals. When the output exceeded the pre-defined trigger, a data capture was initiated. No sudden increases in current were observed (outside of normal fluctuations) on any of the test runs and indicated that no SEL or SEB/SEGR events occurred during any of the tests.

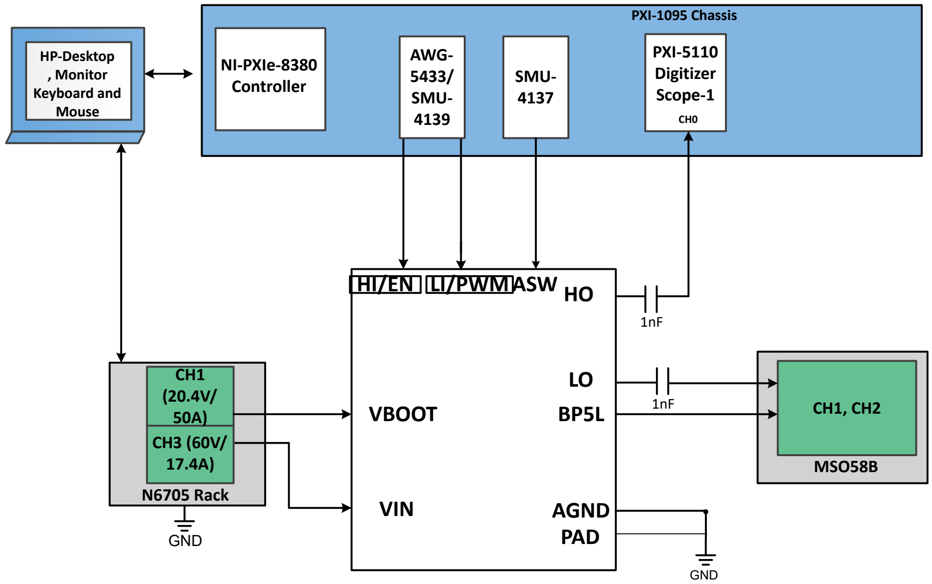

Figure 6-1. Block Diagram of the SEE Test Setup for the TPS7H60x3-SP

7 Destructive Single-Event Effects (DSEE)

7.1 Single-Event Latch-up (SEL) Results

During the SEL testing the device was heated to 125°C by using a Closed-Loop PID controlled heat gun (MISTRAL 6 System (120V, 2400W). The temperature of the die was verified using thermal camera prior to exposure to heavy ions.

The species used for the SEL testing was Homium (^{165}Ho at 15MeV / nucleon). For the ^{165}Ho ion an angle of incidence of 0° was used to achieve an LET_{EFF} = 75MeV × cm^2 / mg (for more details, see Ion LET_{EFF}, Depth, and Range in Silicon). The kinetic energy in the vacuum for this ions is 2.474GeV. Flux of approximately 10^5 ions / cm^2× s and a fluence of approximately 10^7 ions / cm^2 per run was used. Run duration to achieve this fluence was approximately two minutes. The four devices were powered up and exposed to the heavy-ions using the maximum recommended input voltage and boot voltage of 14V. The ASW (High-Side Driver Signal Return) was set to 150V with respect to AGND (low-side driver signal return). The device was set in both PWM and IIM modes during testing. For more information see Single-Event Effects section. No SEL events were observed during all nine runs, indicating that the TPS7H60x3-SP is SEL-free up to 75MeV × cm^2 / mg. Table 7-1 shows the SEL test conditions and results. Figure 7-1 shows a plot of the current versus time for run 1.

Table 7-1. Summary of TPS7H60x3-SP SEL Test Condition and Results

Run Number	Unit Number	Variant	Ion	LET_{EFF} (MeV × cm^2 / mg)	Flux (ions × cm^2 / mg)	Fluence (Number of ions)	V_{IN}	V_{BOOT}	Mode	EN/HI	PWM/LI	SEL (# Events)
1	1	TPS7H6003	^{165}Ho	75	6.22×10^4	1×10^7	14	14	PWM	$14V_{DC}$	$14V_{pk\text{-}pk}$ 500kHz	0
2	1	TPS7H6003	^{165}Ho	75	6.26×10^5	9.99×10^6	14	14	PWM	$14V_{DC}$	$14V_{pk\text{-}pk}$ 1MHz	0
3	1	TPS7H6003	^{165}Ho	75	6.19×10^4	9.99×10^6	14	14	PWM	$14V_{DC}$	$14V_{pk\text{-}pk}$ 2MHz	0
4	2	TPS7H6003	^{165}Ho	75	6.23×10^4	1.00×10^7	14	14	IIM_{ENST}	$14V_{DC}$	0V	0
5	2	TPS7H6003	^{165}Ho	75	5.79×10^4	1.00×10^7	14	14	IIM_{ENST}	0V	$14V_{DC}$	0
6	3	TPS7H6003	^{165}Ho	75	7.46×10^4	1.00×10^7	14	14	IIM_{ENSW}	$14V_{pk\text{-}pk}$ 500kHz	$14V_{pk\text{-}pk}$ 500kHz	0
7	3	TPS7H6003	^{165}Ho	75	6.88×10^4	1×10^7	14	14	IIM_{DISSW}	$14V_{pk\text{-}pk}$ 500kHz	$14V_{pk\text{-}pk}$ 500 kHz	0
8	4	TPS7H6003	^{165}Ho	75	5.64×10^4	1×10^7	14	14	IIM_{DISST}	$14V_{DC}$	0V	0
9	4	TPS7H6003	^{165}Ho	75	5.78×10^4	1×10^7	14	14	IIM_{DISST}	0V	$14V_{DC}$	0
36	5	TPS7H6013	^{165}Ho	75	7×10^4	1×10^7	14	14	PWM	$14V_{DC}$	$14V_{pk\text{-}pk}$ 500kHz	0
37	6	TPS7H6013	^{165}Ho	75	6.42×10^4	1×10^7	14	14	IIM_{ENSW}	$14V_{pk\text{-}pk}$ 500 kHz	$14V_{pk\text{-}pk}$ 500 kHz	0
38	7	TPS7H6013	^{165}Ho	75	8.58×10^4	1×10^7	14	14	IIM_{DISSW}	$14V_{pk\text{-}pk}$ 500kHz	$14V_{pk\text{-}pk}$ 500kHz	0
39	8	TPS7H6023	^{165}Ho	75	6.94×10^4	1×10^7	14	14	PWM	$14V_{DC}$	$14V_{pk\text{-}pk}$ 500kHz	0
40	9	TPS7H6023	^{165}Ho	75	6.67×10^4	1×10^7	14	14	IIM_{ENSW}	$14V_{pk\text{-}pk}$ 500kHz	$14V_{pk\text{-}pk}$ 500kHz	0
41	10	TPS7H6023	^{165}Ho	75	5.59×10^4	1×10^7	14	14	IIM_{DISSW}	$14V_{pk\text{-}pk}$ 500kHz	$14V_{pk\text{-}pk}$ 500kHz	0

Using the MFTF method shown in *Single-Event Effects (SEE) Confidence Interval Calculations* and combining (or summing) the fluences of the four runs at 125°C (4 × 10^7), the upper-bound cross-section (using a 95% confidence level) is calculated as:

$$\sigma_{SEL} \leq 4.11\ x\ 10^{-8}\ cm^2 / device\ for\ LET_{EFF}\ =\ 75\ MeV \cdot cm^2 / mg\ and\ T\ =\ 125°C \quad (1)$$

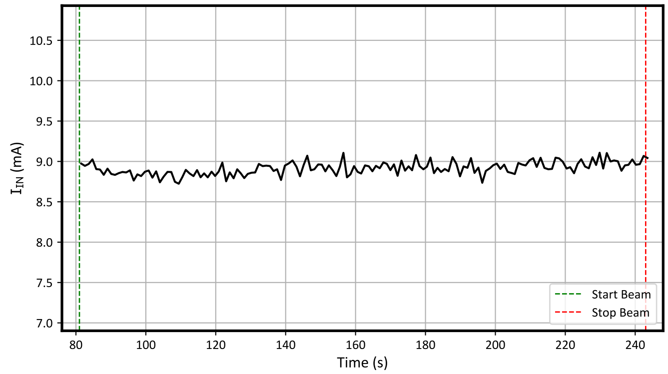

Figure 7-1. SEL Run 1 (PWM Mode, f_{SW} = 500kHz)

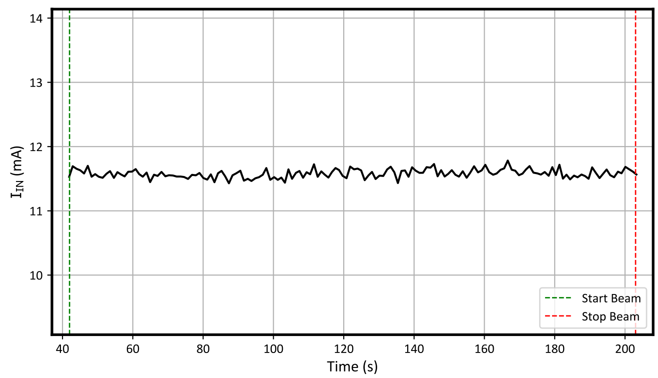

Figure 7-2. SEL Run 2 (PWM Mode, f_{SW} = 1MHz)

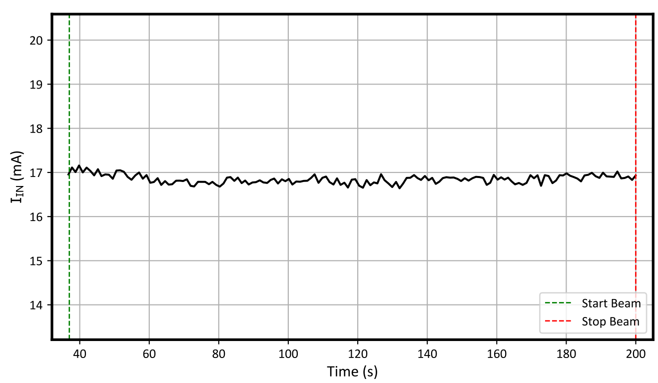

Figure 7-3. SEL Run 3 (PWM Mode, f_{SW} = 2MHz)

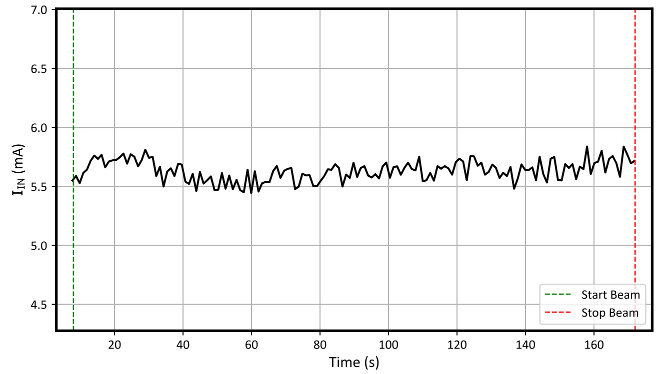

Figure 7-4. SEL Run 4 (IIM Enabled Mode, PWM/LI = 14V)

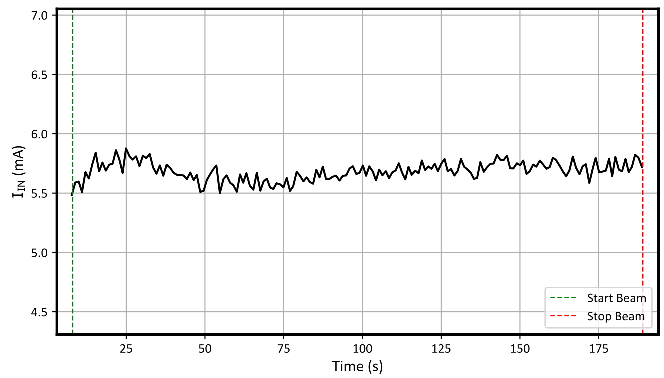

Figure 7-5. SEL Run 9 (IIM Disabled Mode, EN/HI = 14V)

7.2 Single-Event Burnout (SEB) and Single-Event Gate Rupture (SEGR) Results

During the SEB/SEGR characterization, the device was tested at room temperature of approximately 25°C. The device was tested under both the enabled and disabled mode. For the SEB-OFF mode the device was disabled using the EN-pin by forcing 0V while in PWM mode and by holding both inputs low during the IIM mode testing. During the SEB/SEGR testing with the device enabled or disabled, not a single input current event was observed.

The species used for the SEB testing was Homium (^{165}Ho at 15MeV / nucleon). For the ^{165}Ho ion an angle of incidence of 0° was used to achieve an LET_{EFF} = 75MeV × cm^2 / mg (for more details, see Ion LET_{EFF}, Depth, and Range in Silicon). The kinetic energy in the vacuum for this ion is 2.474GeV (15-MeV / amu line). Flux of approximately 10^5 ions / cm^2 × s and a fluence of approximately 10^7 ions / cm^2 was used for the run. Run duration to achieve this fluence was approximately two minutes. The four devices (same as used in SEL testing) were powered up and exposed to the heavy-ions using the maximum recommended input voltage and boot voltage of 14V. The ASW (High-Side Driver Signal Return) was set to 150 V. The device was set in both PWM and IIM modes during testing. For more information, see Single-Event Effects section. No SEB/SEGR current events were observed during the 12 runs, indicating that the TPS7H60x3-SP is SEB/SEGR-free up to LET_{EFF} = 75MeV × cm^2/ mg and across the full electrical specifications. Summary of TPS7H60x3-SP SEB/SEGR Test Condition and Results shows the SEB/SEGR test conditions and results.

Table 7-2. Summary of TPS7H60x3-SP SEB/SEGR Test Condition and Results

Run Number	Unit Number	Variant	Ion	LET_{EFF} (MeV × cm^2 / mg)	Flux (ions × cm^2/ mg)	Fluence (number of ions)	Enabled Status	V_{IN}	V_{BOOT}	Mode	Switching Frequency	SEB Event?
10	1	TPS7H6003	^{165}Ho	75	6.11 × 10^4	9.98 × 10^6	EN	14	14	PWM	500kHz	No
11	1	TPS7H6003	^{165}Ho	75	6.59 × 10^4	1.00 × 10^7	EN	14	14	PWM	1MHz	No
12	1	TPS7H6003	^{165}Ho	75	6.50 × 10^4	1.00 × 10^7	EN	14	14	PWM	2MHz	No
13	1	TPS7H6003	^{165}Ho	75	6.44 × 10^4	1.00 × 10^7	DIS	14	14	PWM	N/A	No
14	2	TPS7H6003	^{165}Ho	75	6.09 × 10^4	1.00 × 10^7	EN	14	14	IIM_{ENST}	N/A	No
15	2	TPS7H6003	^{165}Ho	75	6.14 × 10^4	1 × 10^7	EN	14	14	IIM_{ENST}	N/A	No
16	2	TPS7H6003	^{165}Ho	75	6.26 × 10^4	1 × 10^7	DIS	14	14	IIM_{ENST}	N/A	No
17	2	TPS7H6003	^{165}Ho	75	6.49 × 10^4	9.99 × 10^6	DIS	14	14	IIM_{DISST}	N/A	No
18	3	TPS7H6003	^{165}Ho	75	8.27 × 10^4	1 × 10^7	EN	14	14	IIM_{ENSW}	500kHz	No
19	3	TPS7H6003	^{165}Ho	75	7.25 × 10^4	1 × 10^7	EN	14	14	IIM_{DISSW}	500kHz	No
20	4	TPS7H6003	^{165}Ho	75	5.68 × 10^4	1 × 10^7	EN	14	14	IIM_{DISST}	N/A	No
21	4	TPS7H6003	^{165}Ho	75	6.03 × 10^4	1 × 10^7	EN	14	14	IIM_{DISST}	N/A	No
42	5	TPS7H6013	^{165}Ho	75	8 × 10^4	1 × 10^7	EN	14	14	PWM	500kHz	No
43	5	TPS7H6013	^{165}Ho	75	7.65 × 10^4	1 × 10^7	DIS	14	14	PWM	N/A	No
44	6	TPS7H6013	^{165}Ho	75	6.38 × 10^4	1 × 10^7	EN	14	14	IIM_{ENSW}	500kHz	No
45	7	TPS7H6013	^{165}Ho	75	7.17 × 10^4	1 × 10^7	EN	14	14	IIM_{DISSW}	500kHz	No
46	8	TPS7H6023	^{165}Ho	75	6.63 × 10^4	1 × 10^7	EN	14	14	PWM	500kHz	No

Table 7-2. Summary of TPS7H60x3-SP SEB/SEGR Test Condition and Results (continued)

Run Number	Unit Number	Variant	Ion	LET_{EFF} (MeV × cm^2 / mg)	Flux (ions × cm^2/ mg)	Fluence (number of ions)	Enabled Status	V_{IN}	V_{BOOT}	Mode	Switching Frequency	SEB Event?
47	8	TPS7H6023	^{165}Ho	75	6.21 × 10^4	1 × 10^7	DIS	14	14	PWM	N/A	No
48	9	TPS7H6023	^{165}Ho	75	6.86 × 10^4	1 × 10^7	EN	14	14	IIM_{ENSW}	500kHz	No
49	10	TPS7H6023	^{165}Ho	75	5.95 × 10^4	1 × 10^7	EN	14	14	IIM_{DISSW}	500kHz	No

Using the MFTF method described in *Single-Event Effects (SEE) Confidence Interval Calculations*, the upper-bound cross-section (using a 95% confidence level) is calculated as:

$$\sigma_{SEB} \leq 3.08 \, x \, 10^{-8} \, cm^2 / device \; for \; LET_{EFF} = 75 \, MeV \cdot cm^2 / mg \; and \; T = 25°C \quad (2)$$

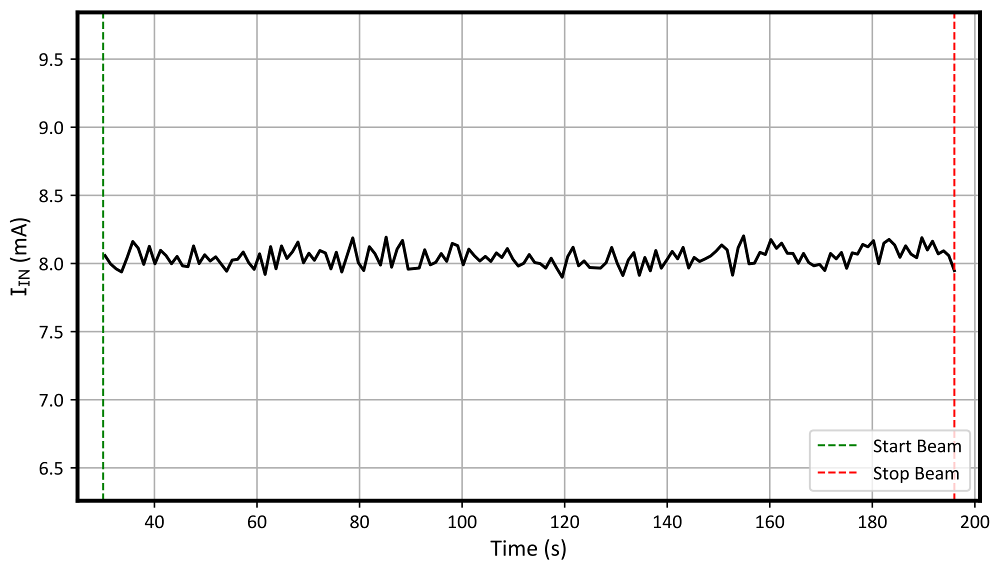

Figure 7-6. SEB On Run 10 (PWM Mode, f_{sw}= 500kHz)

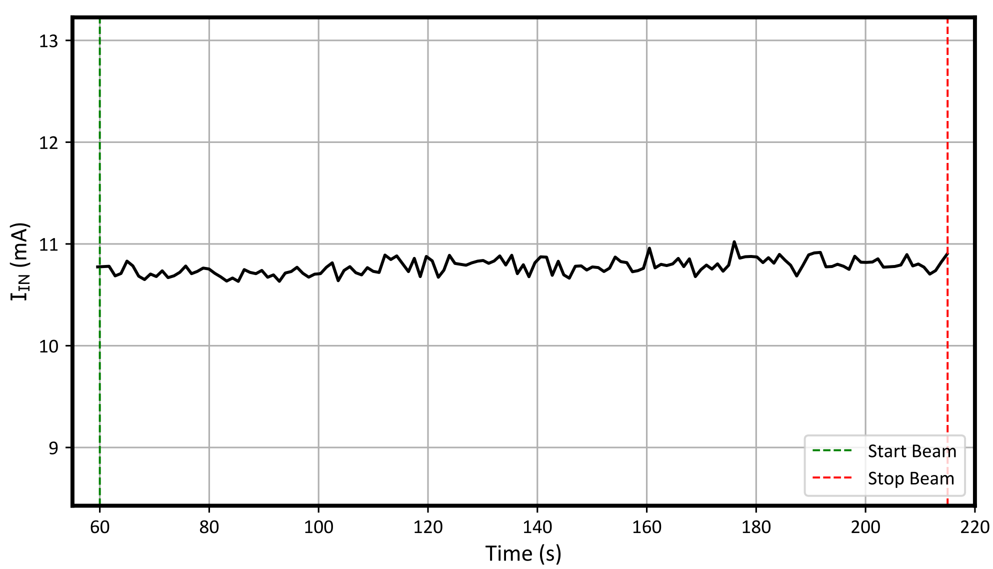

Figure 7-7. SEB On Run 11 (PWM Mode, f_{sw} = 1MHz)

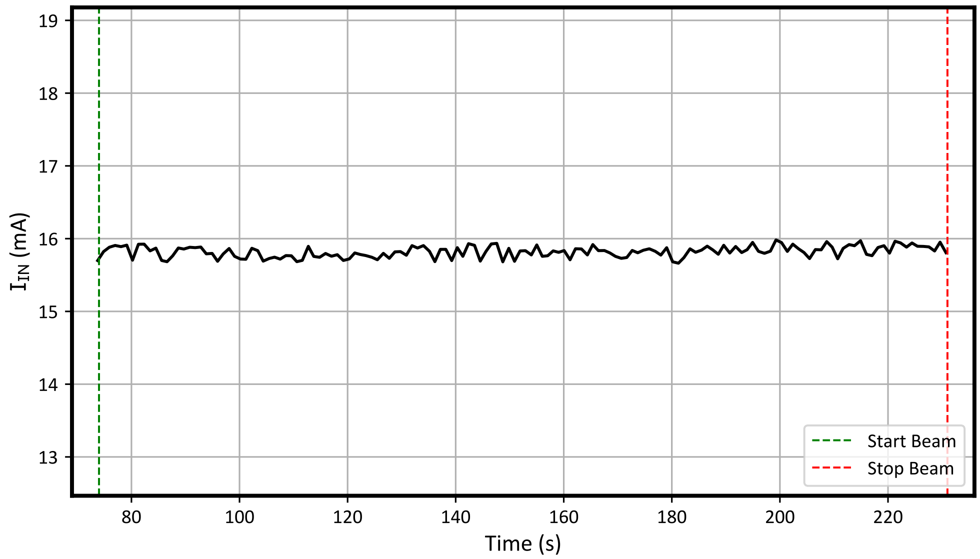

Figure 7-8. SEB On Run 12 (PWM Mode, f_{sw}= 2MHz)

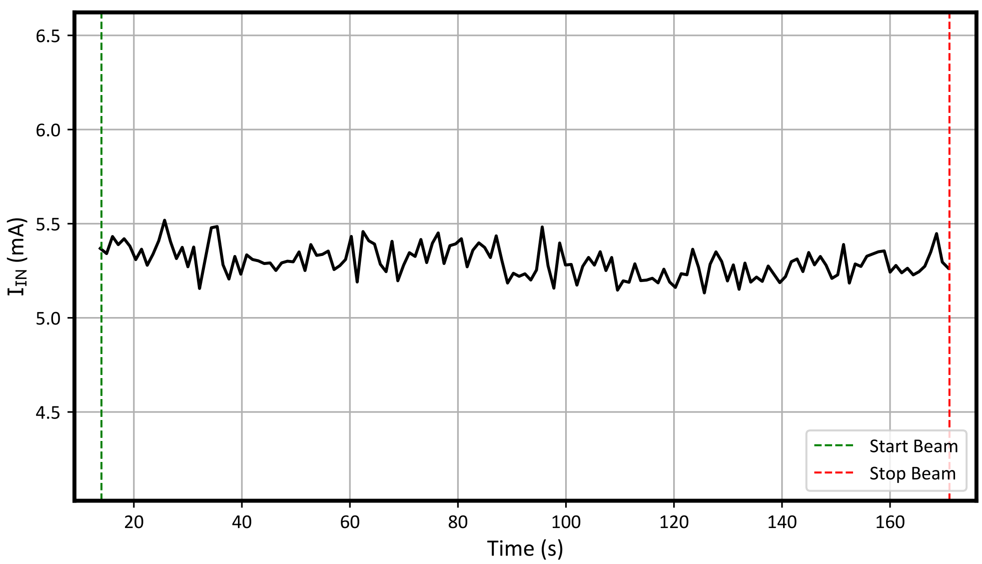

Figure 7-9. SEB Off Run 13 (PWM Mode)

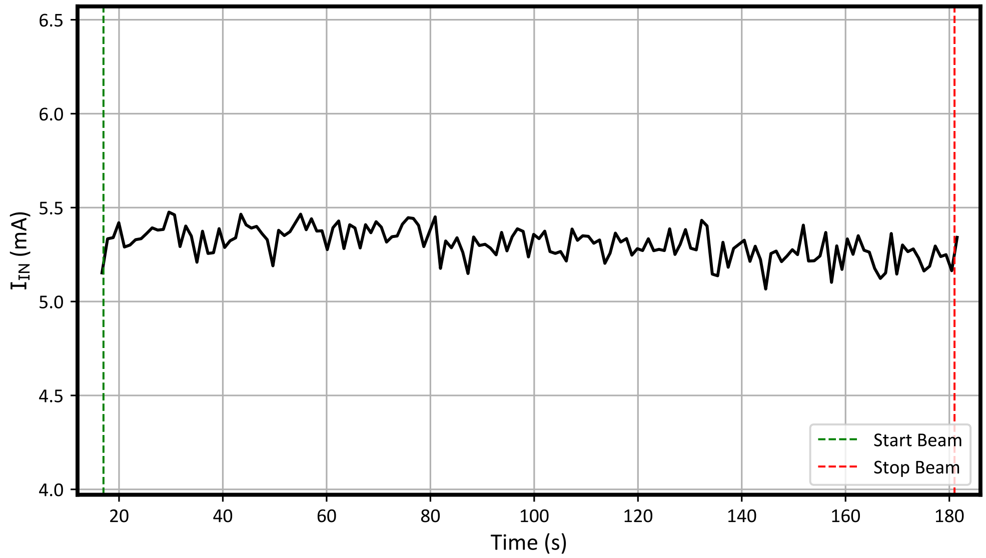

Figure 7-10. SEB On Run 14 (IIM-Enabled Mode, EN/HI = 14V)

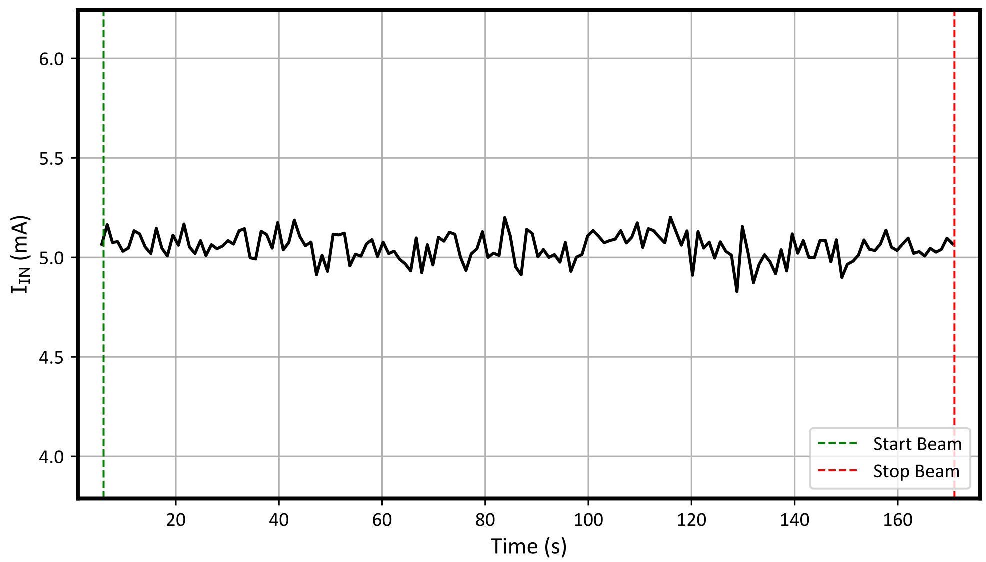

Figure 7-11. SEB Off Run 16 (IIM-Enabled Mode)

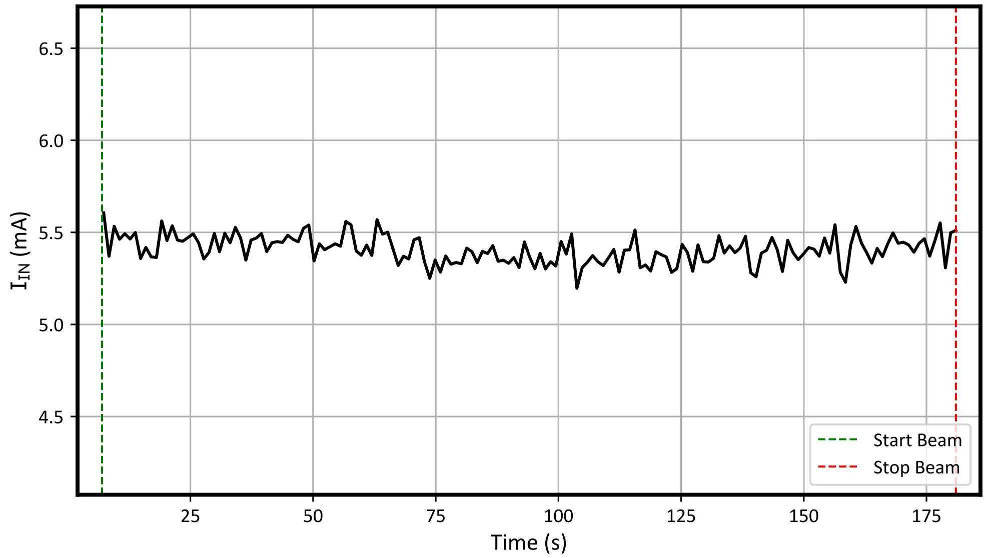

Figure 7-12. SEB On Run 21 (IIM-Disabled Mode, PWM/LI = 14V)

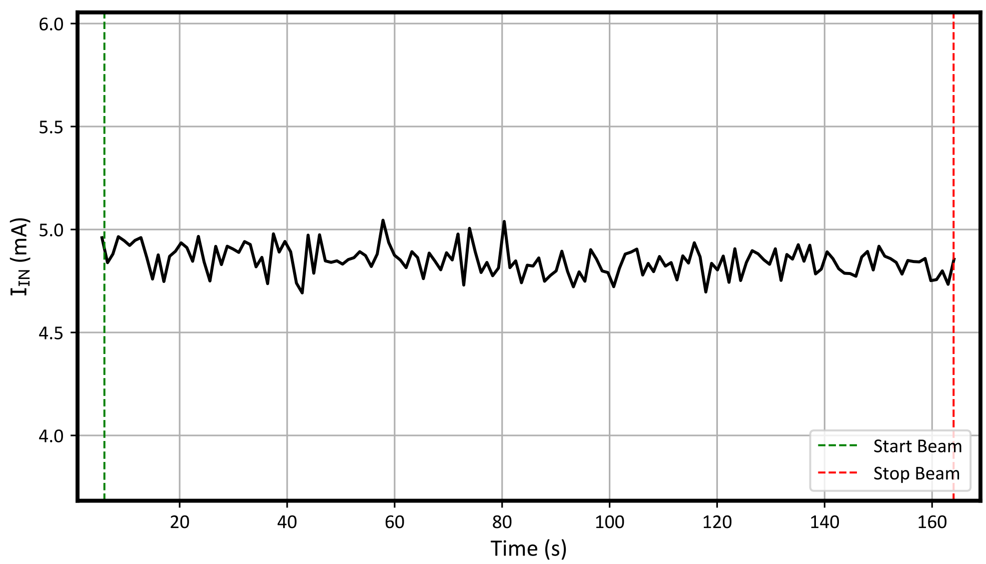

Figure 7-13. SEB Off Run 17 (IIM-Disabled Mode)

8 Single-Event Transients (SET)

The primary focus of SETs were heavy-ion-induced transient upsets on the output signals HO and LO (with a 1nF capacitive load on the outputs as seen in block diagram). SET testing was done at room temperature across three ion species, ^{109}Ag, ^{141}Pr, and ^{165}Ho which produced a range of LET_{EFF} of 48 to 75MeV × cm^2 / mg for more details, see Ion LET_{EFF}, Depth, and Range in Silicon. HO and LO were monitored by two different scopes, a NI PXIe-5110 and a MSO58B oscilloscope. During PWM and IIM_{SW} mode testing, each scope was configured to trigger based on an *outside* pulse width measurement, where the window for the output signal was 20% (±200ns). During the IIM_{ST} modes, the same two scopes were used, however, the trigger was a window which was 500mV above or 500mV below 0V with the signals AC-coupled. The signals in this mode were monitored to see if the signal ever went low when it should have been high, or high when it should have been low. During all SET testing, there was no cross-conduction in either PWM or IIM mode and in IIM mode the only transient that occurred was a high to low transient, no signals ever *turned on* when not required. For all recorded transients, there were never cases where the pulse deviated to be greater than 20%. The only captured SETs were missed pulses. During the IIM mode testing with LO high and during the PWM mode testing during a LO transient, the signature of the transient shows that there is some overshoot (approximately 410mV during static and approximately 480mV during switching) before leveling back out to 5V during the turn on. This is consistent across all transients, but all signals do recover back to nominal after the overshoot in the order of µs. Because of this BP5L was monitored on the MSO58B to show that this overshoot is from the internal LDO.

Waveform size, sample rate, trigger type, value, and signal for all scopes used is listed in Table 8-1.

Table 8-1. Scope Settings

Scope Model	Trigger Signal	Trigger Type	Trigger Value	Record Length	Sample Rate
MSO58B	LO	Pulse Width and Window	± 20% / ±500mV	20µs / div	250MS / s
	BP5L	N/A	N/A		
PXIe-5110	HO	Pulse Width and Window	± 20% / ±500mV	20k	100MS / s

Table 8-2. Summary of TPS7H60x3-SP SET Test Condition and Results

Run Number	Unit Number	Variant	Ion	LET_{EFF} (MeV × cm^2 / mg)	Flux (ions × cm^2/ mg)	Fluence (number of ions)	Mode	MSO58B LO Number	PXIe-5110 HO Number
22	1	TPS7H6003	^{165}Ho	75	6.33 × 10^4	1 × 10^7	PWM	7	5
23	3	TPS7H6003	^{165}Ho	75	8.12 × 10^4	1× 10^7	IIM_{ENSW}	5	6
24	3	TPS7H6003	^{165}Ho	75	7.14 × 10^4	1 × 10^7	IIM_{DISSW}	2	2
25	4	TPS7H6003	^{165}Ho	75	6.87 × 10^4	1 × 10^7	IIM_{DISST}	0	4
26	4	TPS7H6003	^{165}Ho	75	6.50 × 10^4	1 × 10^7	IIM_{DISST}	4	0
27	1	TPS7H6003	^{141}Pr	65	1.13 × 10^5	9.99 × 10^6	PWM	1	3
28	3	TPS7H6003	^{141}Pr	65	1.09 × 10^5	1 × 10^7	IIM_{DISST}	2	0
29	3	TPS7H6003	^{141}Pr	65	1.07 × 10^5	1 × 10^7	IIM_{DISST}	0	1
30	4	TPS7H6003	^{141}Pr	65	1.11 × 10^5	1 × 10^7	IIM_{DISSW}	0	0
31	4	TPS7H6003	^{141}Pr	65	1.28 × 10^5	1 × 10^7	IIM_{ENSW}	0	1
32	1	TPS7H6003	^{109}Ag	48	9.79 × 10^4	1 × 10^7	PWM	0	0
33	3	TPS7H6003	^{109}Ag	48	1 × 10^5	1 × 10^7	IIM_{DISST}	0	0
34	3	TPS7H6003	^{109}Ag	48	1 × 10^5	1 × 10^7	IIM_{DISST}	0	0
50	5	TPS7H6013	^{165}Ho	75	7.3 × 10^4	1 × 10^7	PWM	1	1
51	6	TPS7H6013	^{165}Ho	75	6.46 × 10^4	1 × 10^7	IIM_{ENSW}	2	4
52	7	TPS7H6013	^{165}Ho	75	6.03 × 10^4	1 × 10^7	IIM_{DISSW}	5	2

Table 8-2. Summary of TPS7H60x3-SP SET Test Condition and Results (continued)

Run Number	Unit Number	Variant	Ion	LET_{EFF} (MeV × cm² / mg)	Flux (ions × cm²/ mg)	Fluence (number of ions)	Mode	MSO58B LO Number	PXIe-5110 HO Number
53	8	TPS7H6023	^{165}Ho	75	6.96×10^4	1×10^7	PWM	6	6
54	9	TPS7H6023	^{165}Ho	75	6.72×10^4	1×10^7	IIM_{ENSW}	2	2
55	10	TPS7H6023	^{165}Ho	75	5.8×10^4	1×10^7	IIM_{DISSW}	0	0

Upper and lower bound cross-sections were calculated to 95% confidence. Weibull fit was done to calculate the onset value. The onset was only found for the TPS7H6003-SP device.

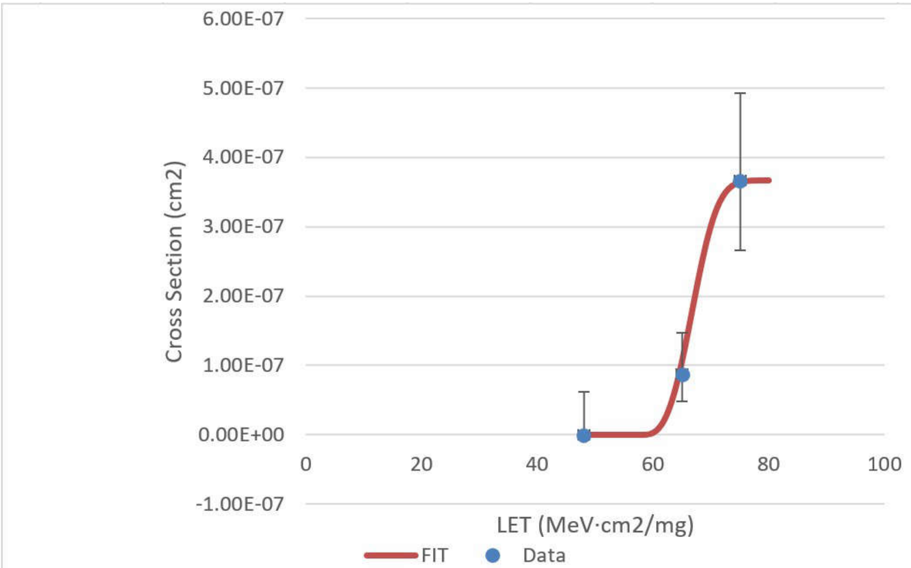

Figure 8-1. Cross-Section and Weibull Fit for HO and LO SET Test Cases

A Weibull fit was conducted to determine the best estimation of onset since there were transients at 65MeV, but none at 48MeV. Because of the gap between LET levels there is high probability that the true onset is somewhere between the two tested levels. Based on this fit the estimated true onset for the TPS7H6003-SP is 58MeV.

Table 8-3. Weibull Parameters for HO and LO SET Test Cases

Parameter	Units
Upper Bound Cross-Section (cm²)	4.87×10^{-7}
Cross-Saturation (cm²)	3.50×10^{-7}
Onset (MeV-cm²/ mg)	58
w	10
s	3

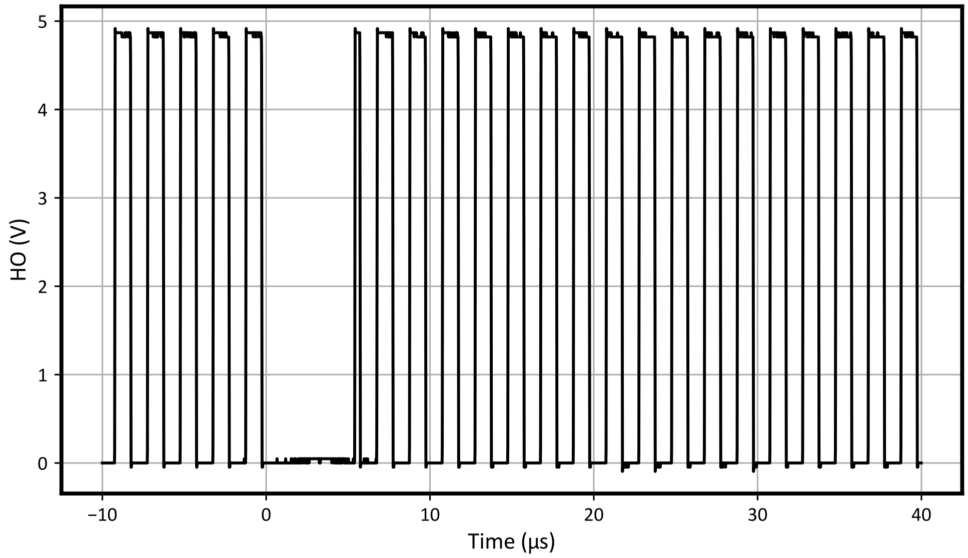

Figure 8-2. HO SET Run 22 (PWM Mode, f_{sw} = 500kHz)

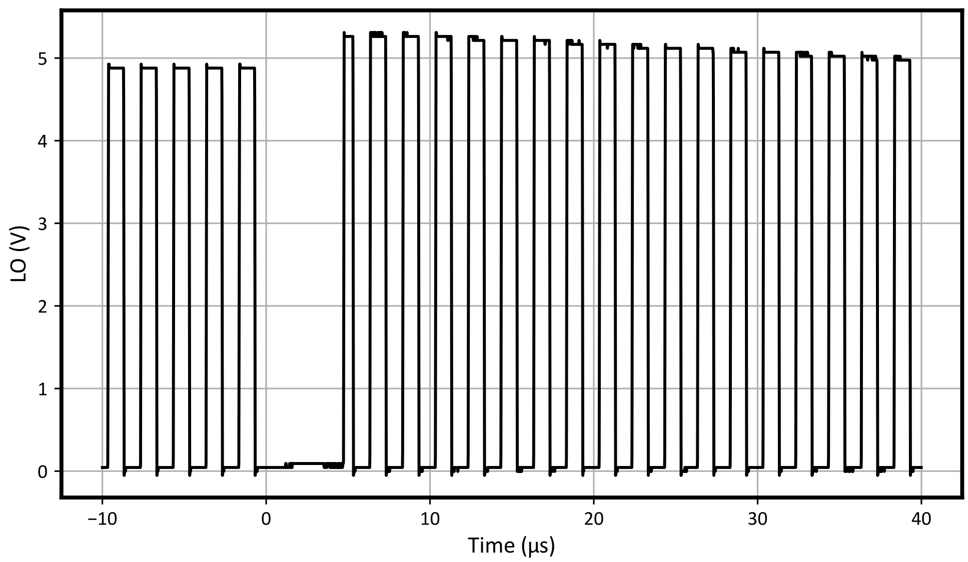

Figure 8-3. LO SET Run 22 (PWM Mode, f_{sw} = 500kHz)

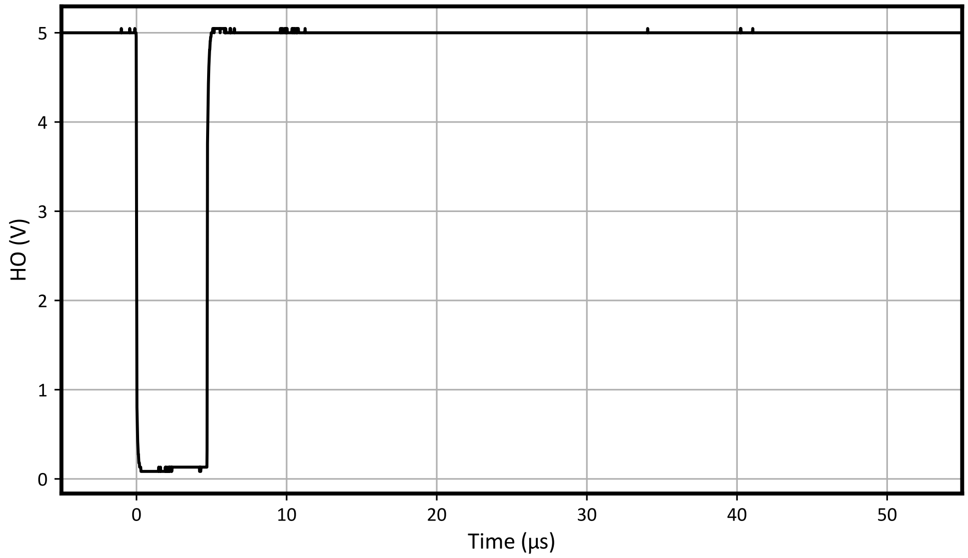

Figure 8-4. HO SET Run 25 (IIM-Disabled Mode, EN/HI = 5V)

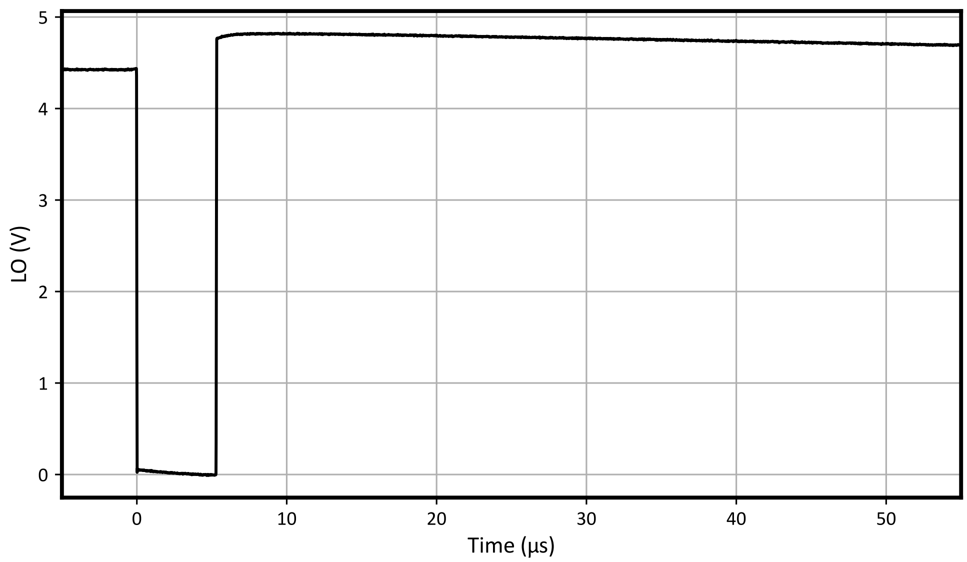

Figure 8-5. LO SET Run 26 (IIM-Disabled Mode, PWM/LI = 5V)

9 Event Rate Calculations

Event rates were calculated for LEO (ISS) and GEO environments by combining CREME96 orbital integral flux estimations and simplified SEE cross-sections according to methods shown in *Heavy Ion Orbital Environment Single-Event Effects Estimations*. Assume a minimum shielding configuration of 100mils (2.54mm) of aluminum, and *worst-week* solar activity (this is similar to a 99% upper bound for the environment). Using the 95% upper-bounds for the SEL and the SEB/SEGR, the event rate calculation for the SEL and the SEB/SEGR is shown on Table 9-1 and Table 9-2, respectively. It is important to note that this number is for reference since no SEL or SEB/SEGR events were observed. SET orbit rate for the TPS7H6003-SP device is listed in SEB/SEGR Event Rate Calculations for Worst-Week LEO and GEO Orbits.

Table 9-1. SEL Event Rate Calculations for Worst-Week LEO and GEO Orbits

Orbit Type	Onset LET_{EFF} (MeV-cm^2/mg)	CREME96 Integral FLUX (/day/cm^2)	σSAT (cm^2)	Event Rate (/day)	Event Rate (FIT)	MTBE (Years)
LEO (ISS)	75	6.26×10^{-5}	4.11×10^{-8}	2.57×10^{-12}	1.07×10^{-4}	1.07×10^{9}
GEO		1.77×10^{-4}		7.26×10^{-12}	3.03×10^{-4}	3.77×10^{8}

Table 9-2. SEB/SEGR Event Rate Calculations for Worst-Week LEO and GEO Orbits

Orbit Type	Onset LET_{EFF} (MeV-cm^2/mg)	CREME96 Integral FLUX (/day/cm^2)	σSAT (cm^2)	Event Rate (/day)	Event Rate (FIT)	MTBE (Years)
LEO (ISS)	75	6.26×10^{-5}	3.08×10^{-8}	1.93×10^{-12}	8.04×10^{-5}	1.42×10^{9}
GEO		1.77×10^{-4}		5.45×10^{-12}	2.27×10^{-4}	5.03×10^{8}

Table 9-3. SET Event Rate Calculations for Worst-Week LEO and GEO Orbits

Orbit Type	Onset LET_{EFF} (MeV-cm^2/ mg)	CREME96 Integral FLUX (/ day / cm^2)	σSAT (cm^2)	Event Rate (/ day)	Event Rate (FIT)	MTBE (Years)
LEO (ISS)	58	2.02×10^{-4}	4.87×10^{-7}	9.84×10^{-11}	4.10×10^{-3}	2.78×10^{7}
GEO		6.14×10^{-4}		2.99×10^{-10}	1.24×10^{-2}	9.17×10^{6}

10 Summary

The purpose of this study was to characterize the effect of heavy-ion irradiation on the single-event effect (SEE) performance of the TPS7H6003-SP 200V half-bridge eGaN gate driver. Heavy-ions with LET_{EFF} = 48 to 75MeV × cm^2 / mg were used for the SEE characterization campaign. Flux of approximately 10^5 ions / cm^2 × s and fluences of approximately 10^7 ions / cm^2 per run were used for the characterization. The SEE results demonstrated that the TPS7H60x3-SP is free of destructive SEL and SEB LET_{EFF} = 75MeV × cm^2 / mg and across the full electrical specifications. Transients at LET_{EFF} = 48 to 75MeV × cm^2 / mg on V_{OUT} are presented and discussed. CREME96-based worst week event-rate calculations for LEO(ISS) and GEO orbits for the DSEE and SET are presented for reference.

A References

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12 Revision History

Changes from Revision * (November 2023) to Revision A (June 2024) — Page