

DISCLAIMER

* TS3DS10224

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File Description

‘TS3DS10224_tb_linux.sp’ -which is the test bench- invokes other 2 files: ‘TS3DS10224_process_linux.sp’ –nominal process library, and ‘TS3DS10224_ckt_linux.sp’ –subcircuits’ definition.

Instructions

- 1) Open your simulator HSPICE -- E-2010.12 32-BIT on Linux
- 2) On the file menu, open and run file 'TS3DS10224_tb_linux.sp'.

Plots of the test-setup schematic and simulation results are shown below:



