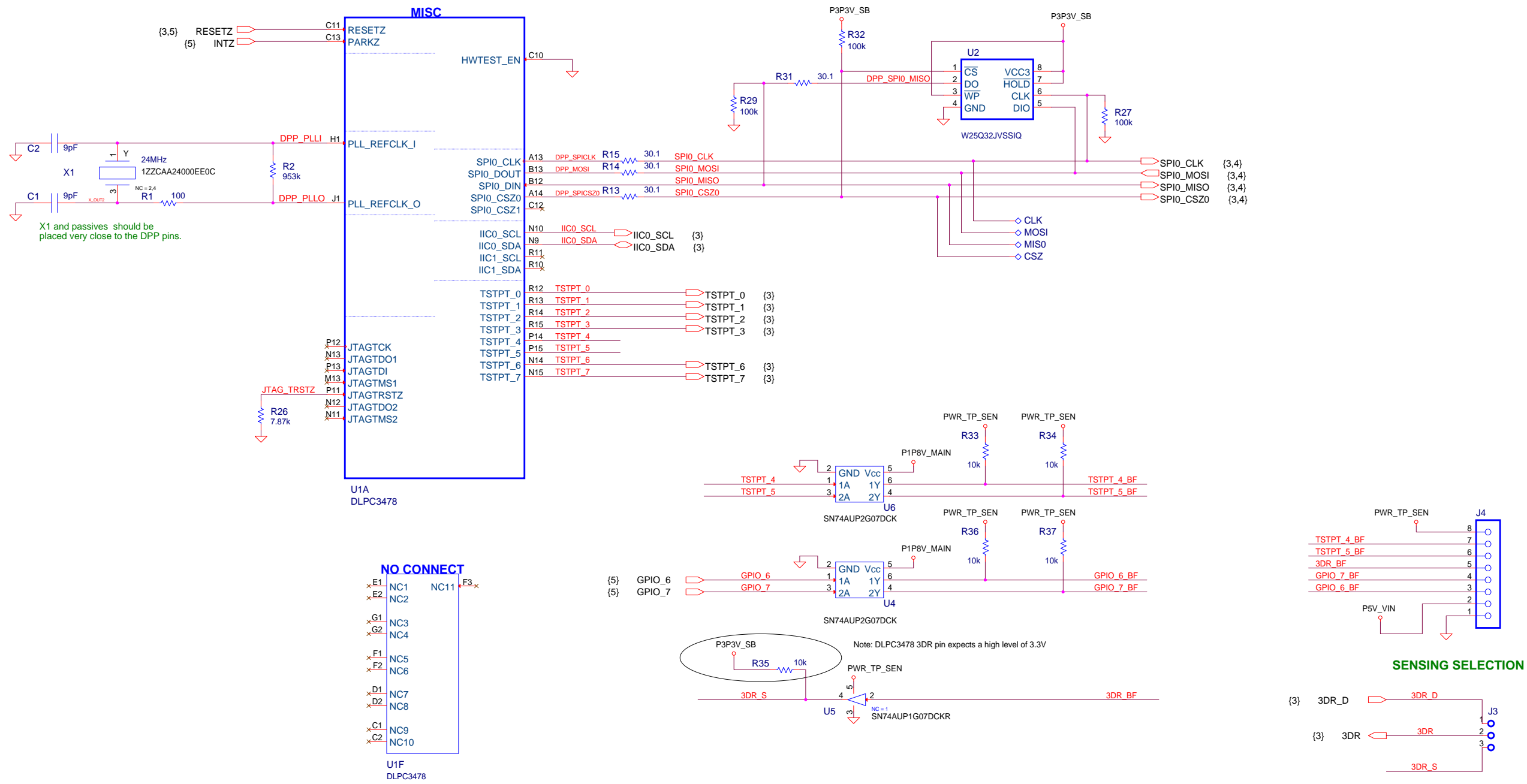


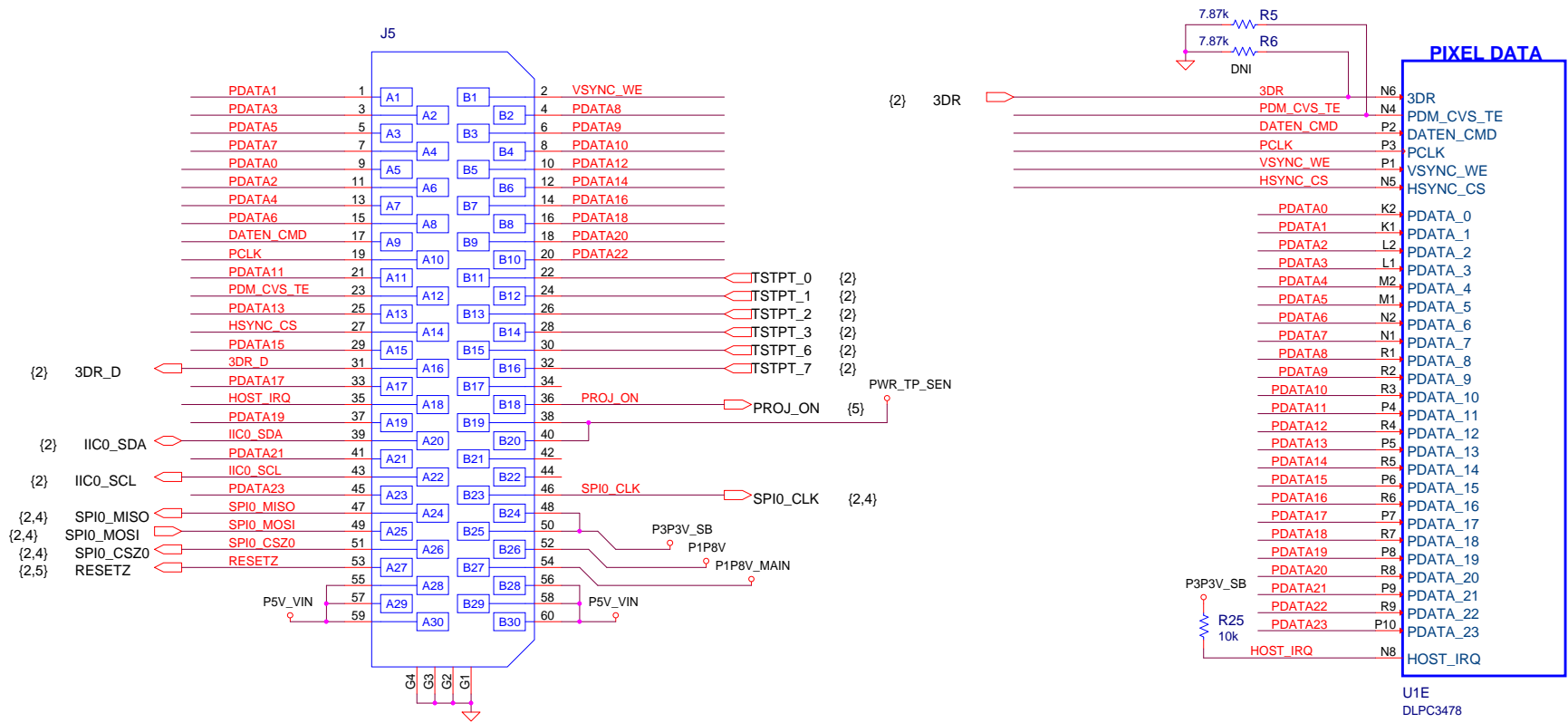
Revisions

Rev. E.0 : Development version
Rev. E.1 : Marked R16 as DNI; Changed C53 cap value to 4.7uF; Changed U2 part number to W25Q32JVSSIQ
Rev. E.2 : Changed title to accommodate new part-number
Rev. A : Removed ESD number, General clean-up, change revision to A
Rev. A.1 : Changed R6 to DNI for light control input
Rev. B : Changed pull-up voltage for 3DR_S on R35 to 3.3V

DWN	Nadine Estermann	DATE	11/02/2018	TEXAS INSTRUMENTS - DLP Products			
ENGR	Nadine Estermann	11/02/2018	(C) COPYRIGHT 2009 Texas Instruments Inc. All Rights Reserved				
SYS	Dan Morgan	---	TITLE TIDA-080003 DLP® 3010 Light Control Display Board				
APVD	Jeff Dennis	---	DRAWING NO		DLP017-002	REV	B
QA	---	---	11 x 17	Orcad Capture 16.2	SHEET 1 of 7		

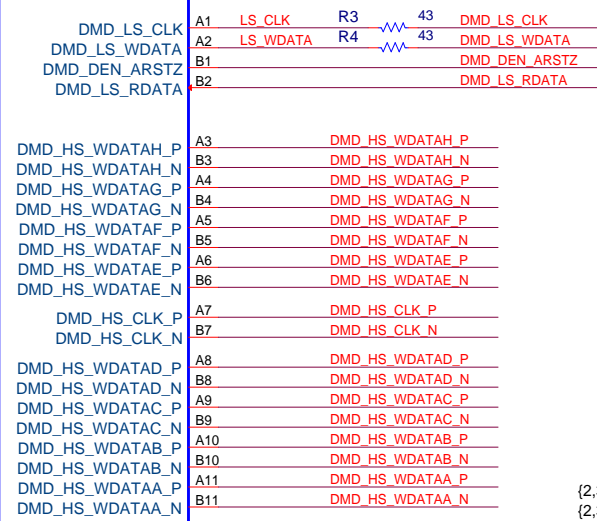
DLPC3478 Clock, Reset, SPI and Flash Memory



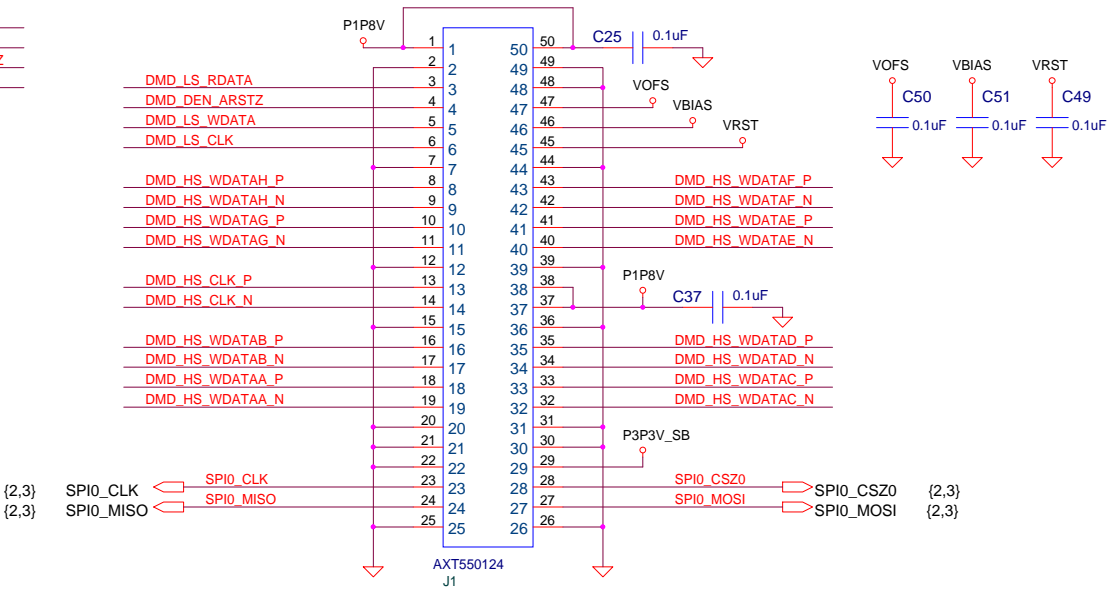


(Mate with QTH-060-05-L-D-A)
(Place on card bottom side)

DMD INTERFACE



U1B
DLPC3478



DMD Interface

