Welcome! Texas Instruments New Product Update

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- Phone lines will be muted
- Please post questions in the chat or contact your sales person or field applications engineer



New Product Update: Low ripple & Low Iq DC/DC point-of-load buck converters

Thomas Lewis

April 1, 2021

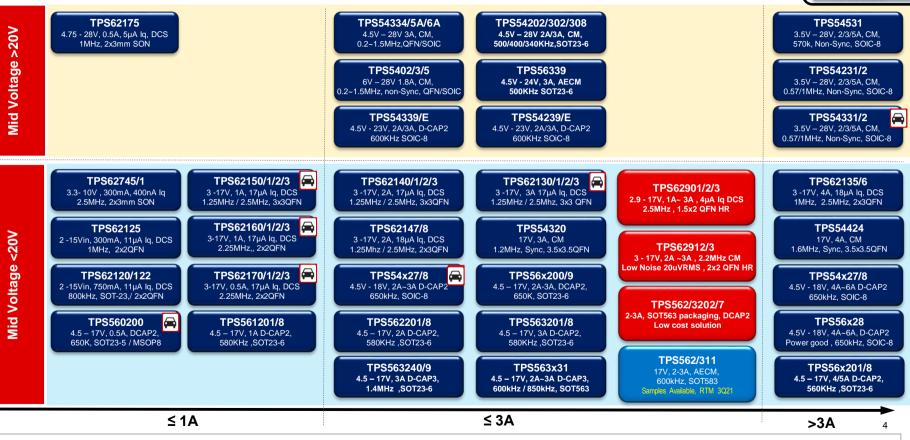


Agenda

- 7-30V, <6A, Buck Converter Portfolio
- Introducing TPS62912/3
 - o Industry's first low-noise buck converters with integrated ferrite-bead compensation
 - Customer Benefit: Low 20 µVRMS noise performance (from 100 Hz to 100 kHz) and ultra-low output-voltage ripple of 10 µVRMS, enables engineers the ability to remove one or more low-dropout regulators (LDOs) from their designs, reduce power losses by up to 76%, and save 36% of board space.
 - o Target Applications: Telecom infrastructure, Test & Measurement, Aerospace & Defense, Medical
- Introducing TPS62901/2/3
 - 3-V to 17-V, high efficiency and low IQ buck converter in 1.5-mm × 2-mm QFN package
 - Customer Benefit: Power savings from a 4-uA lq combined with high efficiency across all load conditions
 - o Target Applications: Battery operated end equipment, motor drives and Enterprise applications



BSR-MV LC Buck Converter Portfolio





Automotive

Released

Newly Released Preview

Introducing the TPS62912/3

Industry's 1st low-noise buck converters w/ integrated ferrite-bead compensation



Problem Statement & Common Solutions

Problem statement

Powering noise sensitive (meaning low ripple & low noise) point-of-load rails in high precision systems such as high speed ADCs/DACs, AFEs or clocks and minimizing interference with sensitive signal chain components.

Common solutions today

Using a DC/DC from VIN and filter output of DC/DC

- □ With a secondary LDO thermal rise, system efficiency performance, solution size
- □ With an external LC filter may have problems due to stability issues



TPS62912/13 2A/3A low noise and low ripple buck converter with integrated ferrite bead compensation

FEATURES

- 3.0V to 17V input voltage range
- 0.8V to 5.5V output voltage
- $57m\Omega/20m\Omega$ RDSon
- 1% output voltage accuracy
- 2.2MHz/1MHz switching frequency
- + Low noise <20 μV_{RMS} 100Hz to 100kHz
- Low voltage ripple <10 μ V $_{RMS}$ after ferrite bead
- Integrated 2nd stage filter compensation
- PSRR >65dB (up to 100kHz)
- Spread spectrum modulation (optional)
- 40°C to 150°C junction temperature range
- 2.0mmx2.0mm Hotrod QFN

APPLICATIONS

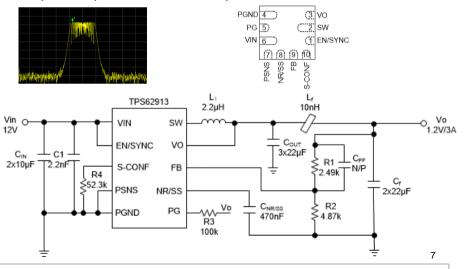
- Telecom infrastructure
- Test & Measurement
- Radar, Avionics
- Medical

е	Configuration	Function

S-CONFIG 2.2MHz/1MHz S-CONFIG Spread Spectrum S-CONFIG Output discharge

BENEFITS

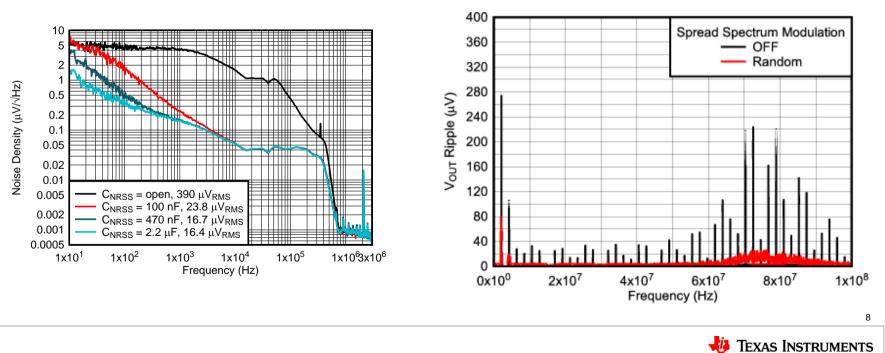
- · Eliminates LDO and passive post filtering
- Remote sense FB improves load regulation
- 1% output voltage regulation reduces bulk capacitance
- Low output voltage ripple minimizes RF spurs.
- Low 1/f noise for optimum phase noise
- Spread spectrum reduces spurious noise and EMI





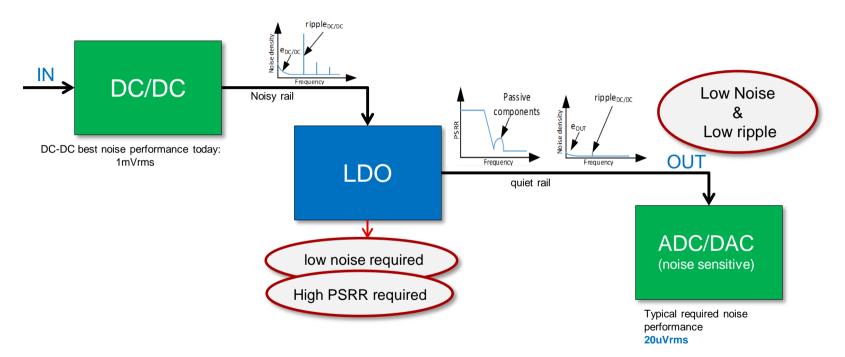
Defining Low Noise & Ripple Example with 12Vin to 3.3Vout, 2.2MHz

Noise (less than 100kHz)



Ripple (DC-DC f_{sw} and above)

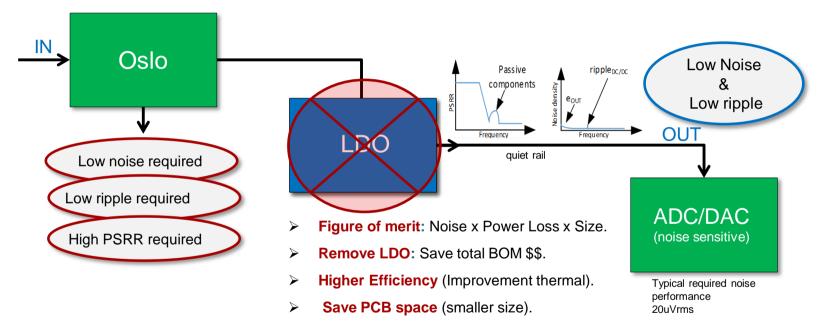
Typical solution for low-noise systems Low Noise, High PSRR, LDO



- LDO is the most critical element in a high performance system
- Low noise and High PSRR is a must to achieve a low noise power supply



The TPS62912/3 Answer No LDO



- LDO is not required anymore
- Allows system cost reduction, reduced LDOs power losses and heat dissipation



Available Marketing Collateral As of March 2020

Collateral	Content Type	Link
TPS62912EVM-077: 17-V, 2-A low noise/low ripple buck converter with integrated ferrite bead filter compensation		https://www.ti.com/tool/TPS62912EVM-077
TPS62913EVM-077: 17-V, 3-A low noise/low ripple buck converter with integrated ferrite bead filter compensation		https://www.ti.com/tool/TPS62913EVM-077
TPS6291xEVM-077 Evaluation Modules User's Guide	EVM User's Guide	https://www.ti.com/lit/ug/slvubx2/slvubx2.pdf
TPS6291x 3-V to 17-V, 2-A/3-A Low Noise and Low Ripple Buck Converter with Integrated Ferrite Bead Filter Compensation (SLVSFP4A)	Datasheet	https://www.ti.com/lit/ds/symlink/tps62913.pdf
WEBENCH Designs	Webench Model	https://www.ti.com/product/TPS62912#design- development https://www.ti.com/product/TPS62913#design- development
Product Folder Live, Pricing set-up, Device visible parametric search, EVM Tool Folder Live, Set-up maximum units samples order/price, Customer initial sample requirement	APL setup	https://www.ti.com/product/TPS62912 https://www.ti.com/product/TPS62913
"Powering Sensitive ADC Designs with the TPS62913 Low-Ripple and Low-Noise Buck Converter"	App Note	https://www.ti.com/lit/an/slvaew7/slvaew7.pdf
News Release	Campaign Kick-off	https://news.ti.com/industrys-first-low-noise-buck- converters-with-integrated-ferrite-bead- compensation-simplify-high-precision-designs
"Minimize noise and ripple with a low-noise buck converter" Blog	Technical Article	https://e2e.ti.com/blogs /b/powerhouse/archive/202 0/10/21/minimize-noise-and-ripple-with-a-low-noise- buck-converter
Virtual Electronica: "Powering Sensitive ADC Designs with TPS62913 Low-Ripple and Low-Noise Buck Converter" (promotional)	Video	https://training.ti.com/powering-sensitive-adcs-low- noise-low-ripple-dcdc-buck-converter
Low Noise and low ripple buck converter with integrated ferrite bead compensation (training)	Video	https://training.ti.com/achieve-low-noise-and-low- output-ripple-high-efficiency-dcdc-converter
"Powering the AFE7920 with the TPS62913 Low Ripple & Low Noise Buck Converter"	App Note	https://www.ti.com/lit/an/slvaf16/slvaf16.pdf
New Product Update (NPU) - Introducing Oslo and Vespa	Webinar	https://training.ti.com/node/1147267

Introducing the TPS62901/2/3

High efficiency and low IQ buck converter in 1.5-mm × 2-mm QFN package



Problem Statement & Common Solutions

Problem statement

This highly flexible family leverages features to cover a wide variety of applications with just one part, such as: lower iQ to extend the lifetime of battery operated systems, increased efficiency across all load conditions to decrease power loss and thermal constraints, and finally a smaller solution size for power density.

Common solutions today

Using a different DC/DC optimized for size, solution cost or performance for each unique application



Vespa (TPS6290x)

3-V to 17-V, 1-3A, High Efficiency and Low IQ Buck Converter in 1.5-mm × 2-mm QFN Package

RELEASED

FEATURES

BENEFITS

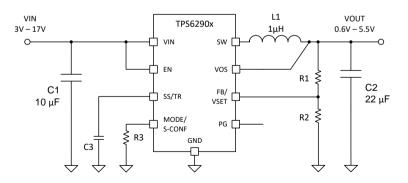
- Input Voltage: 3V 17V
- Output Voltage: 0.4V 5.5V
 - Adj. via external divider, 0.6V to 5.5V
 - Internal Divider: 16 options from 0.4 to 5.5
- 3A/2A/1A pin-to-pin family
- 1% Output Accuracy with selectable Forced-PWM operation
- T_J up to 150°C
- Precise-Enable, CONFIG-Pin, Window-PG Comparator
- 1uH Inductor, 22uF Cout, no external Bootstrap
- Small QFN package (1.5mm x 2.0 mm) with 0.5mm pitch
- DCS-Control[™] with AEE
- 4µA Quiescent Current
- Selectable output discharge
- Highly Flexible- & Easy-to-Use
 - Optimized Pinout for Single-Layer Routing
 - Precise Enable Input
 - Optional Forced PWM- or Auto-Power-Save-Mode
 - Power Good Output and active output discharge

APPLICATIONS

- Industrial PC & FPGA Supply
- SSD
- Factory, Building Automation
- Portable Electronics
- Point-of-Load regulation

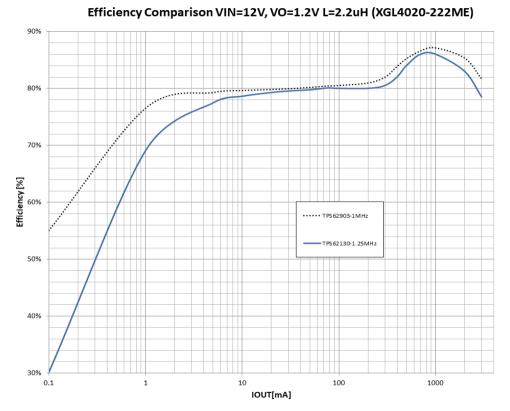
Saves PCB Area by Smallest Solution size

- No Bootstrap-C, fixed Vo options, 1uH, 22uF Co
- Small QFN package with flexible single layer routing
- Output Monitoring by Window-PG comparator
- Input Monitoring by precise EN-Threshold and Hysteresis (POC-Filter)
- 1% Accurate and Low Ripple Output in Forced-PWM configuration
- Optimizes Battery Lifetime & Energy Budget
- High efficiency at light load





Efficiency comparison TPS6290x vs. TPS6213x

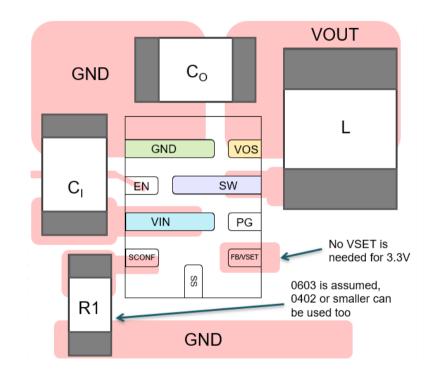




Smallest Solution size

- 1.5mm x 2.0mm HotRod package
- <u>Single-Layer PCB layout</u> saves cost, space & easy layout optimized layout for lower noise / EMI
- Total solution size is: 23.71mm²

	Size	
Inductor (mm ²)	3.2	DFE201612E
Input cap (mm ²)	2.5	10uH (805)
Output cap (mm ²)	2.5	22uH (805)
Package (mm ²)	3	QFN (2.0mmx1.5mm)
other compoenents (mm ²)	1.28	no VSET Res, 1 S-CONFG Res (603 size each)
Routing estimates (mm ²)	11.232	Estimated 90% of components size for routing
Total size (mm²)	23.712	Total comp size + 90% for routing





Flexibility by SCON: Smart-Config-Pin

<u>User can choose device setting via Resistor</u>

- FB/VSEL: select between V_O Version (Adj or fixed)
- MODE: select Auto-Mode or Forced-PWM
- Discharge: Active Discharge YES/NO
- FSW: Choose one of two frequencies
- Apply High or Low level to get PFM/PWM behavior like in the past

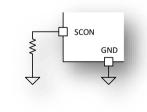
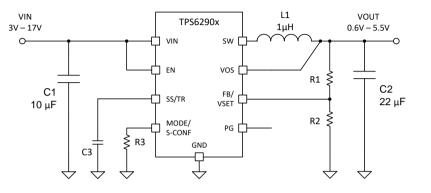


	Table 7-1. SmartConfig Setting Table										
#	LEVEL OR RESISTOR VALUE [Ω] ⁽¹⁾	FB/VSET- PIN	F _{SW} (MHz)	OUTPUT DISCHARGE	MODE (AUTO OR FORCED PWM)	DYNAMIC MODE CHANGE					
	Setting Options by Level										
1	GND	external FB	2.5	yes	Auto PFM/PWM with AEE	active					
2	HIGH (>VIH_MODE)	external FB	2.5	yes	Forced PWM						
	Setting Options by Resistor										
3	7.15 k	external FB	2.5	no	Auto PFM/PWM with AEE						
4	8.87 k	external FB	2.5	no	Forced PWM						
5	11.0 k	external FB	1	yes	Auto PFM/PWM						
6	13.7 k	external FB	1	yes	Forced PWM	-					
7	16.9 k	external FB	1	no	Auto PFM/PWM						
8	21.0 k	external FB	1	no	Forced PWM						
9	26.1 k	VSET	2.5	yes	Auto PFM/PWM with AEE	not active					
10	32.4 k	VSET	2.5	yes	Forced PWM						
11	40.2 k	VSET	2.5	no	Auto PFM/PWM with AEE						
12	49.9 k	VSET	2.5	no	Forced PWM	-					
13	61.9 k	VSET	1	yes	Auto PFM/PWM						
14	76.8 k	VSET	1	yes	Forced PWM	-					
15	95.3 k	VSET	1	no	Auto PFM/PWM						
16	118 k	VSET	1	no	Forced PWM						

(1) E96 Resistor Series, 1% Accuracy, Temperature Coefficient better or equal than ±200 ppm/°C



FB (Adj) & VSEL device setting



L1 VIN VOUT TPS6290x 3V – 17V 1uH 0.4V – 5.5V SW 0 VIN VOS ΕN C2 C1 FB/ SS/TR 22 µF 10 μF VSET Ş R2 MODE/ РGГ S-CONF **₹** R3 GND C3 \triangleleft \triangleleft イフ 77

- Adjustable Version for
 - High flexibility (Vout)
 - LED Application
 - Customers are familiar

- VSEL option for
 - higher accuracy
 1% VOUT directly
 - lower System I_Q
 ~3µA saving at 3.3V_o
 - Smaller & cheaper solution up to 2 components less



Visit <u>www.ti.com/npu</u>

For more information on the New Product Update series, calendar and archived recordings





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