Non-Inverting Op Amp with Inverting Positive Reference Voltage Circuit

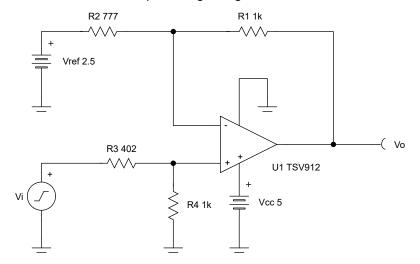


Design Goals

Input		Output		Supply		
V _{iMin}	V _{iMax}	V _{oMin}	V _{oMax}	V _{cc}	V _{ee}	V _{ref}
2 V	5 V	0.05 V	4.95 V	5 V	0 V	2.5 V

Design Description

This design uses a non-inverting amplifier with an inverting positive reference to translate an input signal of 2 V to 5 V to an output voltage of 0.05 V to 4.95 V. This circuit can be used to translate a sensor output voltage with a positive slope and offset to a usable ADC input voltage range.



Design Notes

- 1. Use op amp linear output operating range. Usually specified under A_{OL} test conditions.
- 2. Check op amp input common mode voltage range. The common mode voltage varies with the input voltage.
- 3. V_{ref} must be low impedance.
- 4. Input impedance of the circuit is equal to the sum of R₃ and R₄.
- 5. Choose low-value resistors to use in the feedback. It is recommended to use resistor values less than 100 $k\Omega$. Using high-value resistors can degrade the phase margin of the amplifier and introduce additional noise in the circuit.
- 6. The cutoff frequency of the circuit is dependent on the gain bandwidth product (GBP) of the amplifier.
- 7. Adding a capacitor in parallel with R₁ will improve stability of the circuit if high-value resistors are used.

Design Steps

$$V_{o} = V_{i} \times \left(\frac{R_{4}}{R_{3} + R_{4}}\right) \left(\frac{R_{1} + R_{2}}{R_{2}}\right) - V_{ref} \times \left(\frac{R_{1}}{R_{2}}\right)$$

Calculate the gain of the input to produce the largest output swing

$$\begin{split} &V_{0_max} - V_{0_min} = \left(V_{i_max} - V_{i_min}\right) \!\! \left(\frac{R_4}{R_3 + R_4}\right) \!\! \left(\frac{R_1 + R_2}{R_2}\right) \\ &\frac{V_{0_max} - V_{0_min}}{V_{i_max} - V_{i_min}} = \left(\frac{R_4}{R_3 + R_4}\right) \!\! \left(\frac{R_1 + R_2}{R_2}\right) \\ &\frac{4.95V - 0.05V}{5V - 2V} = \left(\frac{R_4}{R_3 + R_4}\right) \!\! \left(\frac{R_1 + R_2}{R_2}\right) \\ &1.633 \frac{V}{V} = \left(\frac{R_4}{R_3 + R_4}\right) \!\! \left(\frac{R_1 + R_2}{R_2}\right) \end{split}$$

2. Select a value for R₁ and R₄ and insert the values into the previous equation. The other two resistor values must be solved using a system of equations. The proper output swing and offset voltage cannot be calculated if more than two variables are selected.

$$\begin{split} R_1 &= R_4 = 1 \quad k\Omega \\ 1.633 \frac{V}{V} &= \left(\frac{1}{R_3 + 1} \frac{k\Omega}{k\Omega}\right) \left(\frac{1}{R_2} \frac{k\Omega + R_2}{R_2}\right) \end{split}$$

3. Solve the previous equation for R₃ in terms of R₂.

$$R_3 = \frac{1 M\Omega + (1 k\Omega \times R_2)}{1.633 \times R_2} - 1 k\Omega$$

4. Select any point along the transfer function within the linear output range of the amplifier to set the proper offset voltage at the output (for example, the minimum input and output voltage).

$$\begin{split} &V_{o_min} = V_{i_min} \times \left(\frac{R_4}{R_3 + R_4}\right) \!\! \left(\frac{R_1 + R_2}{R_2}\right) - V_{ref} \times \left(\frac{R_1}{R_2}\right) \\ &0.05V = 2V \times \left(\frac{1}{R_3 + 1} \frac{k\Omega}{k\Omega}\right) \!\! \left(\frac{1}{R_2} \frac{k\Omega + R_2}{R_2}\right) - V_{ref} \times \left(\frac{1}{R_2} \frac{k\Omega}{R_2}\right) \end{split}$$

Insert R₃ from step 3 into the equation from step 4 and solve for R₂.

$$0.05V = 2V \times \left(\frac{\frac{1 \text{ k}\Omega}{1 \text{ M}\Omega + 1 \text{ k}\Omega \times R_2} - 1 \text{ k}\Omega + 1 \text{ k}\Omega}\right) \left(\frac{1 \text{ k}\Omega + R_2}{R_2}\right) - V_{\text{ref}} \times \left(\frac{1 \text{ k}\Omega}{R_2}\right)$$

$$R_2 = 777.2\Omega \approx 777\Omega$$

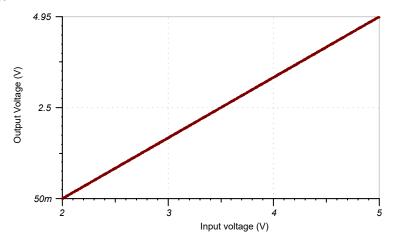
6. Insert R₂ calculation from step 5, and solve for R₃.

$$\begin{split} R_3 &= \frac{1 - M\Omega + \left(1 - k\Omega \times R_2\right)}{1.633 \times R_2} - 1 - k\Omega \\ R_3 &= \frac{1 - M\Omega + 1 - k\Omega \times (777\Omega)}{1.633 \times (777\Omega)} - 1 - k\Omega = 400.49\Omega \approx 402\Omega \end{split}$$

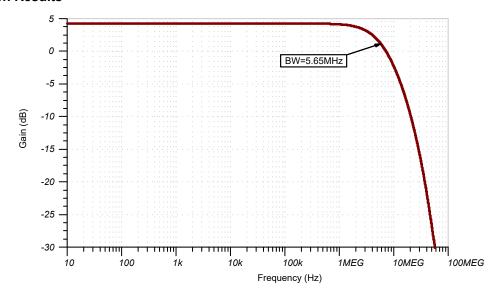
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Design Simulations

DC Simulation Results



AC Simulation Results



Revision History Www.ti.com

Design References

See Analog Engineer's Circuit Cookbooks for TI's comprehensive circuit library.

See circuit SPICE simulation file SBOC512.

See TI Precision Lab Videos on Input and Output Limitations.

Design Featured Op Amp

TSV912				
V _{ss}	2.5 V to 5.5 V			
V _{inCM}	Rail-to-rail			
V _{out}	Rail-to-rail			
V _{os}	0.3 mV			
Iq	550 μA			
l _b	1 pA			
UGBW	8 MHz			
SR	4.5 V/µs			
#Channels	1, 2, and 4			
TSV912				

Design Alternate Op Amp

OPA191				
V _{ss}	4.5 V to 36 V			
V _{inCM}	Rail-to-rail			
V_{out}	Rail-to-rail			
V _{os}	5 μV			
Iq	140 μA/Ch			
l _b	5 pA			
UGBW	2.5 MHz			
SR	5.5 V/μs			
#Channels	1, 2, and 4			
OPA191				

Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from February 4, 2019 to February 5, 2019

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