

Design Goals

Input		Output		Supply	
l _{iMin}	l _{iMax}	V _{oMin}	V _{oMax}	V _{cc}	V _{ee}
50 mA	1 A	0.25 V	5 V	36 V	0 V

Design Description

This single–supply, high–side, low–cost current sensing solution detects load current between 50 mA and 1 A and converters it to an output voltage from 0.25 V to 5 V. High–side sensing allows for the system to identify ground shorts and does not create a ground disturbance on the load.



Design Notes

- 1. DC common mode rejection ratio (CMRR) performance is dependent on the matching of the gain setting resistors, R₂-R₅.
- 2. Increasing the shunt resistor increases power dissipation.
- 3. Ensure that the common–mode voltage is within the linear input operating region of the amplifier. The common mode voltage is set by the resistor divider formed by R₂, R₃, and the bus voltage. Depending on the common–mode voltage determined by the resistor divider a rail–to–rail input (RRI) amplifier may not be required for this application.
- 4. An op amp that does not have a common-mode voltage range that extends to V_{cc} may be used in low–gain or an attenuating configuration.
- 5. A capacitor placed in parallel with the feedback resistor will limit bandwidth, improve stability, and help reduce noise.
- 6. Use the op amp in a linear output operating region. Linear output swing is usually specified under the A_{OL} test conditions.

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Design Steps

1. The full transfer function of the circuit is provided below.

$$V_{o} = I_{in} \times R_{1} \times \frac{R_{5}}{R_{4}}$$

Given $R_{2} = R_{4}$ and $R_{3} = R_{5}$

2. Calculate the maximum shunt resistance. Set the maximum voltage across the shunt to 100 mV.

$$R_1 = \frac{V_{iMax}}{I_{iMax}} = \frac{100 mV}{1A} = 100 m\Omega$$

3. Calculate the gain to set the maximum output swing range.

$$Gain = \frac{V_{0}Max - V_{0}Min}{(I_{i}Max - I_{i}Min) \times R_{1}} = \frac{5V - 0.25V}{(1A - 0.05A) \times 100m\Omega} = 50\frac{V}{V}$$

4. Calculate the gain setting resistors to set the gain calculated in step 3.

Choose $R_2 = R_4 = 1.01 \text{ k} \Omega$ (Standard value) $R_3 = R_5 = R_2 \times \text{Gain} = 1.01 \text{ k} \Omega \times 50 \frac{\text{V}}{\text{V}} = 50.5 \text{ k} \Omega$ (Standard value)

5. Calculate the common-mode voltage of the amplifier to ensure linear operation.

$$V_{\rm cm} = V_{\rm CC} \times \frac{R_3}{R_2 + R_3} = 36V \times \frac{50.5k}{1.01k + 50.5k} = 35.294 V$$

6. The upper cutoff frequency (f_H) is set by the non–inverting gain (noise gain) of the circuit and the gain bandwidth (GBW) of the op amp.

$$f_{H} = \frac{GBW}{Noise \; Gain} = \frac{10MHz}{51\frac{V}{V}} = 196.1 \quad kHz$$



Design Simulations

DC Simulation Results



AC Simulation Results



References:

- 1. Analog Engineer's Circuit Cookbooks
- 2. SPICE Simulation File SBOMAV4
- 3. TI Precision Labs

Design Featured Op Amp

OPA192				
V _{cc}	4.5 V to 36 V			
V _{inCM}	Rail–to–rail			
V _{out}	Rail–to–rail			
V _{os}	5 µV			
l _q	1 mA			
l _b	5 pA			
UGBW	10 MHz			
SR	20 V/µs			
#Channels	1, 2, and 4			
OPA192				

Design Alternate Op Amp

OPA2990				
V _{cc}	2.7 V to 40 V			
V _{inCM}	Rail–to–rail			
V _{out}	Rail–to–rail			
V _{os}	250 μV			
l _q	120 µA			
l _b	10 pA			
UGBW	1.25 MHz			
SR	5V/µs			
#Channels	2			
OPA2990				

Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from December 30, 2018 to February 13, 2019

•	Downstyle title. Added <i>Design Alternate Op Amp</i> table1	
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