

Application Report SCAA121A–September 2012–Revised December 2012

# A Step-by-Step Guide on Using the MSP430 as a Bootloader for the CDCM6208VxEVM

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### ABSTRACT

This application note describes how to use the MSP430 bootloader feature from the CDCM6208VxEVM in an easy-to-follow, step-by-step guide.

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Required Software and Hardware

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#### 1 **Required Software and Hardware**

In order to use the MSP430G2001 as a bootloader for the CDCM6208 the following items are required:

- MSP430 USB-Debug-Interface (MSP-FET430UIF) ٠
- USB Cable (male A to male B)
- 14-pin JTAG cable
- Code Composer Studio v5
- Programming files (including .INI to .H file converter) (SLAC541)

#### 2 Installing Code Composer Studio v5 (CCS)

If CCS is already installed, please proceed with the next step.

Code Composer Studio is available on TI's website for free – this is a code-size limited distribution designed to work ONLY with the MSP430. This download is available at the following URL: http://processors.wiki.ti.com/index.php/Download CCS.

A login account is required to verify download availability on TI's servers. Once the .ZIP file is successfully downloaded and extracted, the installation can begin by opening the file named setup CCS MC Core 5.x.x.exe where the x refers to a specific version number. The CCS install screen appears - proceed to the following screen which displays the EULA. Accepting the EULA then prompts for an installation path. By default this path is C:\Program Files\Texas Instruments.

In the Product Configuration Menu Screen, select the MSP430-only Core Tools option unless support for the C2000 is needed. The components page is next, where the choice of which components of the CCSv5 to install - make a custom installation and make sure the MSP430 USB FET option is selected in order to install the appropriate drivers. The following pictures help to select the right packages (Figure 1 through Figure 3). Otherwise, make a complete installation.

Code Composer Studio v5 Setup	Code Composer Studio v5 Setup
Processor Support Select Processor Architectures to be installed	Select Components Select the components you want installed and deselect components you want to leave out.
MSP430 Low Power MCUs     C28x 32-bit Real-time MCUs     Stellaris Cortex M MCUs     Cortex-R4F MCUs     Adbx Cortex-A and ARM9 processors     C6x DSP + ARM processors     DaVinci Video Processors	☑ Compiler Tools       Description         ☑ II.MSP430 Compiler Tools       MSP430 CGT Installer for default target libraries         ☑ Documentation       ✓         ☑ Device Software       ✓         ☑ Grace       SYS/BIOS v6
Select All Download size: 490 MB. Install size: 2205.0 MB. Texas Instruments	Download size: 463 MB. Install size: 2083.5 MB. Special:
<pre>&lt; Back Next &gt; Cancel</pre>	< Back Next > Cancel

Figure 1. Processor Support Selection

**Figure 2. Component Selection** 



Select Emulators Select the emulators you want installed ar leave out.	nd deselect emulators you want to
JTAG Emulator Support     MSP430 Emulators     MSP430 Parallel Port FET     MSP430 USB FET	Description System driver for the MSP430 Flash Emulation Tool (MSP-FET430UIF and eZ430)
, Download size: 463 MB. Install size: 2083.5 'exas Instruments	мв.

Figure 3. Installation: Driver Selection

# 3 Using Code Composer Studio v5

At the start of CCSv5, the default workspace has to be selected. By default this is C:\Documents and Settings\<user name>\My Documents\workspace\_v5\_x.

Selecting the check box makes this path the default workspace, this can be changed later. To create a new project navigate to File > New > CCS Project and type in a project name (for example, Bootloader Project CDC6208) and select the *MSP430Gxxx Family* and *Empty Project* as shown in Figure 4.

😵 New CCS Project 📃 🗖 🔀				
CCS Project Create a new CCS Project.				
Project name:	Bootloader Projekt CDCM6208			
Output type:	Executable	~		
Use default	location			
Location:	C:\Documents and Settings\x01390	08\workspace_v5_2\Bootloader Prr Browse		
Device				
Family:	MSP430			
Variant.	MSP430Gxxx Family	M5P430G2001		
Connection:	TI MSP430 USB1 [Default]	×		
Advanced s     Project tem     type filter tex     E	ettings plates and examples t mpty Project mpty Project mpty Second (VSP430) Project mpty Grace (VSP430) Project Examples mix The LED ello World	Creates an empty project fully initialized for A		
?	< Back	Next > Finish Cancel		

Figure 4. New Project Setup



#### 4 **Bootloader Environment Setup**

Unzip the programming file (SLAC541).

Add the necessary files to the project using the menu Project > Add Files to Active Project. Navigate to the containing folder and add the following files: spi\_interface.c, spi\_functions\_G2001.h, and reg\_val\_header.h. Note that the top-level file is spi\_interface.c which depends on spi\_functions\_G2001.h, and reg\_val\_header.h.

Double-clicking the **spi\_interface.c** in the C/C++ project window on the left reveals the source code structure.

Right click on the **spi** interface.c and click *Properties*. Various compilation properties have to be set in order to successfully load the program onto the MSP430. Navigate to the Optimization menu (Figure 4) and set the Optimization Level to 4 and Control Speed vs. size trade-offs to 0.

Properties for spi_interface.c		
type filter text	Optimization	
Resource Build Processor Options Optimization Debug Options Include Options ULP Advisor Advanced Options	Configuration: Debug [Active]  (M) Exclude resource from build Optimization level (opt_level, -O) Control speed vs. size trade-offs (0=size, 5=speed) (opt_for_speed, -mf)	anage Configurations)
Show advanced settings		OK Cancel

Figure 5. MSP430 Compiler Settings

At this point, the program is ready to be loaded to the board; however, the header file reg val header.h needs to be modified to reflect customized settings for the CDCM6208. If the register settings of the CDCM6208 are already known, it is possible to simply copy and paste those settings into the reg val header.h. That allows skipping step 5.

Otherwise, the .INI file can be converted to an .h-file by using the converter CDCM6208 InitoH.exe.

#### 5 Creating a Readable Header

The included converter file is used to take an .INI file (which can be created by the CDCM6208 GUI) and convert it into a properly formatted .h file, readable by the MSP430G2001. To start the converter, execute the file named CDCM6208 IniToH.exe. The program opens the window shown in Figure 6.



CDCM6208 INI-H Converter	
Input (*.INI)	
Path:	
	Set File
Output (*.H)	
Path:	
	Set File
	Convert File
	Convertine

Figure 6. CDCM6208\_IniToH.exe Converter program

Click the **Set File** button in the *Input* section to select the desired .INI file to convert. Then press the **Set File** button in the Output section to select a desired output file. To create a seamless transition from the converter to CCSv5, simply overwrite the file **reg\_val\_header.h** in the project folder created in Section 4.

The default location is C:\Documents and Settings\<user name>\My Documents\workspace\_v5\_x.

To execute the conversion, press the **Convert File** button – a successful conversion is indicated with a pop-up prompt.

If a different configuration file name was chosen, ensure that the converted file is included the project. Also, only one formatted header file should be included in the top-level entity, **spi\_interface.c**. Including more than one formatted header file may lead to compilation errors. To do this, simply replace the default line <2> #include "*reg\_val\_header.h*" with an updated name such as #include "**<desired\_name>.h**". Saving an .h file into the project folder automatically updates the project scope.

### 6 Program the Board

The final step in using the MSP430G2001 as a bootloader for the CDCM6208 is to enable SPI communication to the CDCM6208 as well as JTAG communication to the MSP430.



Figure 7. EVM Overview



Program the Board

Enable JTAG communication by switching the DVDD jumper (J5) to 3.3 V (Figure 7). JTAG programming requires a matching device voltage in order to communicate. However, the MSP430 can communicate to and program the CDCM6208 on a lower voltage. The MSP-FET430UIF should be connected to the PC as well as the EVM. Use the USB cable to connect the MSP-FET to the PC and the 14-pin JTAG cable to the EVM. The EVM must also be externally powered via USB or 3.3-V input.

To enable SPI communication from the MSP430G2001 to the CDCM6208, SW1 and SW2 must be in the OFF position while SW3 must be in the ON position. SW1 and SW2 are located next to each other on the top layer (Figure 7).

SW3 is the only switch which is located on the bottom side of the board.

In CCSv5, programming the board is achieved by pressing the DEBUG button, shown in a red circle in Figure 8.

CCS Edit - Bootloader Projekt CDCM6208/reg_val_header.h - Code Composer Studio				
File Edit View Navigate Project Run Scripts Window Help				
📬 • 🖫 🗟   🗞 • ( 🏇 •   🖉   🛷 • • •	Ş -			
Project Explorer		😡 lnk_msp430g2001.cmd	spi_interface.c	🖻 reg_val_header.h 🛛
Sootloader Projekt CDCM6208 [Active - Debug]		1#define reg_0_da	ata OxOOO3	
E 🐝 Binaries		2 #define reg_1_da	ata 0x0000	
		4#define reg_2_da	ata Ox0018 ata Ox08F5	
🕀 🌛 Ink_msp430g2001.cmd		5#define reg 4 da	ata Ox30EF	
🕀 h reg_val_header.h		6#define reg_5_da	ata OxO1BB	
h spi_functions_G2001.h		7#define reg_6_da	ata Ox0004	
Spinter ace.c		9#define reg 8 da	ata OxOOO3	
		10 #define reg_9_da	ata OxOOO3	
		11#define reg_10_c	lata 0x004	0
		12 #define reg_11_c	lata 0x000	0
		14 #define reg 13 c	lata 0x000	0
		15#define reg_14_c	lata 0x000	0
		16#define reg_15_c	lata 0x000	3
		17#define reg_16_c	lata 0x003 lata 0x000	0
		19#define reg 18 c	lata 0x000	3
		20#define reg_19_c	lata 0x003	0
		21#define reg_20_c	lata 0x000	0
		22		
Figure 8. Debug Mode				
To begin the program, press the RUN button . The program is now on the MSP430 – to exit the				
debug mode, press the TERMINATE AL		٦.		
🏂 Debug 🛛 🙀 🕪	) 🛛 🝙 🖾	9. <del>6</del> 8. 6 . 6	学 🌸 🔹	🔗 🖻 🔍 🗖
🖃 🧤 cdcm6208_bootloader [Debug] - TI MSP430 USB1/MSP430 [Project Debug Session]				
E 🔊 Thread [main] (Suspended)				
u main() at spi_interrace.ctb uxreuu				
I c_int00_noinit_noexit() at boot.c:167 0xff5a				
TI MSP430 USB1/MSP430 (4:02:43 PM	1)			
TI MSP430 USB1/MSP430: CIO (4:02:	43 PM)			
	07			
		_		

## Figure 9. Run the Program

A progress information window appears showing the programming status – the window also changes from a program view to a debug view.

If the MSP430 USB-Debug-Interface does not have the right firmware, CCSv5 updates it at this point.

Now, the MSP430G2001 automatically programs the CDCM6208 on device power up. Additionally, SPI communications are limited between the MSP430 and the CDCM6208.

The JTAG board can be disconnected from the EVM. Additionally, DVDD can be set back from 3.3 V to 1.8 V, if needed.

**NOTE:** In order to communicate between the GUI and CDCM6208EVM again, **SW1** must be switched back to the ON position and **SW3** must be switched back to the OFF position.

### References

Additional information about the CDCM6208 EVM is available in the EVM User Guide (SCAU049).

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