Application Brief How to Isolate Signals with Digital Isolators in Emerging LEO Satellite Applications

TEXAS INSTRUMENTS

Sadia Khan Luke Trowbridge System Engineer Marketing Engineer

New satellite constellations and mega-constellations, known as low earth orbit (LEO) satellites, have started the new space race with the goal of providing broadband connectivity to the entire world. These small satellite systems are targeted to grow at an annual rate of 11.7%, reaching 956 units by 2028. This new market is driving a paradigm shift in space applications from historically preferred ceramic packages to the need for radiation-tolerant plastic devices, to take advantage of the performance, weight, size, and cost.

Isolation devices are important in space to enhance system integrity and performance by preventing ground potential differences and improving immunity to noise. Optocouplers and pulse transformers have traditionally been used for isolation. However, optocouplers are known to have poor radiation tolerance on top of already known poor electrical performance and limited channel count per package, while pulse transformers have larger footprints and are harder to design. TI has released the ISOS141-SEP as the industry's first radiation tolerant, SiO2 based digital isolator in the market. This device combines TI's long history of advancements in both digital isolation and qualification requirements for a Space Enhanced Product. Unlike optocouplers and pulse transformers, the ISOS141-SEP provides higher continuous working voltages of 600 V, faster data rates of 100 Mbps, low propagation delay and channel-to-channel skew of 10.7 ns and 4 ns max, and provides 100 kV/µs of CMTI. The radiation tolerance provided by TI's digital isolator of Single-Event Latchup (SEL) and Single-Event Dielectric Rupture (SEDR) Immunity to an LET = 43 MeV·cm²/mg, Total Ionizing Dose (TID) characterization and RLAT to 30 krad(Si), and Displacement Damage (NDD) characterization to 1 × 10¹² n/cm² (1-MeV equivalent) make them a robust choice for low earth orbit satellites.

Following are a few key system use cases where ISOS141-SEP can be used in space applications:

GaN-based DC-DC Converters

To help improve the conversion efficiency of power systems, space customers are designing GaN-based DC-DC converters. A digital isolator can be used to isolate feedback channels as well as the PWM signals to the GaN driver. Isolation is required to separate 100's of volts, converted from solar panels to lower voltages (for example, 28 V, 12 V, 5 V) for onboard electronics, as seen in the Figure 1 block diagram. Power systems are a critical circuit block in space applications in order to power the entire spacecraft reliably while in orbit and have to be the most reliable while in orbit.

1

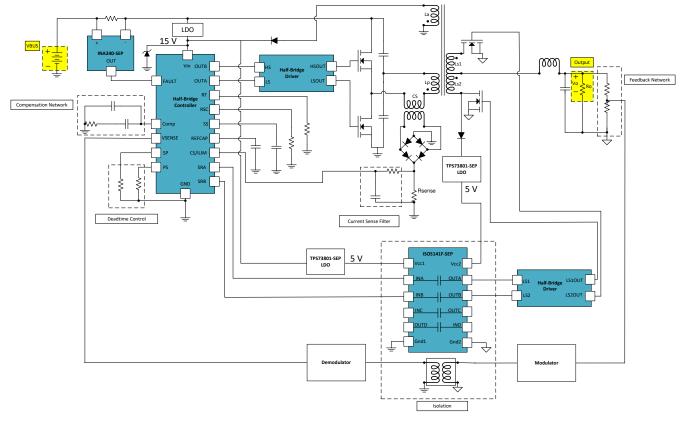


Figure 1. GaN-based DC-DC Converter

Battery Management System

In spacecraft Battery Management System (BMS), isolating I2C, UART, or SPI is needed to protect digital signals between microcontrollers and analog-to-digital converters (ADCs) that are exposed to high commonmode voltages. Isolation is needed to accurately measure the individual cell voltages and monitor battery life, sometimes at millivolts, while rejecting high common-mode noise at several hundred volts. Additionally, for board-to-board communication, interfaces such as CAN, RS-485, and RS-422 are common. These interfaces are used for communication over long distances and should be isolated to prevent large ground potential differences that can cause noise. Since these communication interfaces require isolating multiple channels, digital isolators bring the advantage of small form factor by isolating up to four channels in one package. Traditional optocoupler and pulse transformer solutions only provide one, sometimes two channels of isolation.

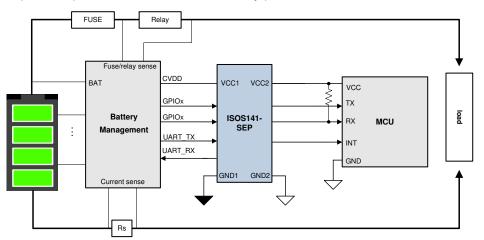


Figure 2. Isolating UART and GPIO signals in Spacecraft Battery Management System



Isolation

Isolation is needed for cold-sparing applications since it works as a redundant device connected to a common data bus without drawing additional power. It stays in a high impedance power-off state. One of the devices can be operational while the other is electrically isolated from the bus. If the redundant device is needed, it can be powered on without causing system level concerns. TI's digital isolator provides up to 600Vrms continuous working voltage and passes single-event dielectric rupture (SEDR) at 500 Vdc. To support cold sparring a secondary isolator can be used which is powered up from a secondary supply in order for the path to be active. Using the enable pin to activate or deactivate the output buffers from that specific side of the isolator for the active communication bus will avoid bus contention.

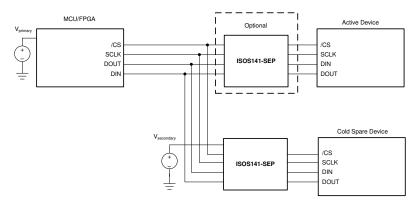


Figure 3. Digital Isolation used in Cold Sparing

For LEO satellites, where cost and reliability go hand in hand, plastic packages are becoming the leading choice. Digital isolators in plastic packages are able to isolate signals seen in multiple space applications from power systems to communications payload. It provides better ease of design, radiation tolerance, a multi-channel solution in a single package, while maintaining high isolation integrity when compared to incumbent solutions.

References

- Isolate Your CAN Systems Without Compromising on Performance or Space
- How to Isolate RS-485 for Smallest Size and Highest Reliability
- How to Isolate Signal and Power for I2C

3

IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATA SHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, regulatory or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to TI's Terms of Sale or other applicable terms available either on ti.com or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

TI objects to and rejects any additional or different terms you may have proposed.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265 Copyright © 2022, Texas Instruments Incorporated