ABSTRACT

In a DC-to-DC circuit design, output capacitance is a critical design parameter, which directly affects the output voltage ripple, loop stability, and the overshoot and undershoot during load transient. The intent of this application report is to show how to estimate the overshoot and undershoot of load transients of a design before PCB fabrication. A typical topology, peak current mode (PCM) control buck regulator circuit, is used as an example to explain the mechanism.

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1 PCM Buck Regulator Block Diagram

For most popular DC/DC regulators available, the switching frequency gets extremely high, may reach several MHz, which allows smaller output inductance and capacitance. In many cases, output capacitance is small, allowing the use of only ceramic capacitors. In this application report, ceramic capacitors with low ESR and ESL are employed for the analysis.

Figure 1 shows a simplified schematic of PCM buck regulator.

![Figure 1. Simplified Schematic of PCM Buck Regulator](image)

2 Load Transient Analysis in Both Frequency and Time Domain

2.1 PCM Buck Regulator Small Signal Model

Figure 2 shows a simplified small signal model of a PCM control buck regulator. This model adopts a voltage controlled current-sourcing concept. In this model, the inductor current is treated as well-controlled current source, which is a simple interpretation of current-loop effects. This model provides excellent physical insight to readers, and is accurate enough for a preliminary design loop analysis.

![Figure 2. PCM Buck Regulator Small Signal Model](image)

Equation 1 through Equation 4 show calculations for some of the elements in Figure 2.

\[ K = \frac{\hat{v}_{FB}}{\hat{v}_{OUT}} = \frac{R_{FBB}}{R_{FBT} + R_{FBB}} = \frac{V_{REF}}{V_{OUT}} \]

where

- K is the feedback divider proportionality coefficient. (1)
The open-loop transfer function \( T(s) \) can be derived by multiplying every element in the loop (see Equation 5):
\[
T(s) = K \times G_{EA}(s) \times G_{CS} \times Z_O(s)
\]
\[
= \frac{V_{REF}}{V_{OUT}} \times \frac{i_L}{v_{COMP}} \times G_m \times \left( \frac{R_{COMP} + \frac{1}{s \times C_{COMP}}}{s \times C_{OUT} \times R_L + 1} \right) \times \frac{R_L}{s \times C_{OUT} \times R_L}
\]

Then the close-loop output impedance can be calculated by Equation 6.
\[
Z_{OC}(s) = \frac{Z_O(s)}{1 + T(s)}
\]

### 2.2 Overshoot and Undershoot Prediction

According to the definition, the output variation \( v_{OUT}(s) \) equals the output impedance \( Z_{OC}(s) \) multiplying the load variation \( i_{OUT}(s) \). For a transient load step of \( \Delta I_{OUT} \), it can be transferred to \( \Delta i_{OUT}/s \) according to the Laplace transform (see Equation 7).
\[
v_{OUT}(s) = \frac{\Delta i_{OUT}(s) \times Z_{OC}(s)}{s} = \frac{Z_O(s)}{1 + T(s)}
\]
The entire small signal analysis is based on frequency domain; however, the system-required overshoot and undershoot limitation specification is defined in time domain. To obtain the time domain behavior of \( v_{\text{OUT}}(t) \), transfer \( v_{\text{OUT}}(s) \) into time domain with inverse Laplace transform. With the help of the mathematic tool Mathcad®, finally a simplified time domain equation is derived by doing inverse Laplace transform of Equation 7 (see Equation 8):

\[
\dot{v}_{\text{OUT}}(t) = \text{InvLaplace}\left( \dot{v}_{\text{OUT}}(s) \right) \approx \begin{cases} 
\Delta I_{\text{OUT}} \times e^{-A t} \times \sin\left( \sqrt{B - A^2} \times t \right) 
\times \frac{C_{\text{OUT}} \times \sqrt{B - A^2}}{2 \times C_{\text{OUT}} \times \sqrt{A^2 - B}} 
\text{while } A^2 < B \\
\Delta I_{\text{OUT}} \times e^{-A t} \times \left( e^{\sqrt{A^2 - B} \times t} - e^{-\sqrt{A^2 - B} \times t} \right) 
\text{while } A^2 > B 
\end{cases}
\]

where

\[
A = \frac{G_m \times G_{CS} \times V_{\text{REF}} \times R_{\text{COMP}}}{2 \times C_{\text{OUT}} \times V_{\text{OUT}}} \\
B = \frac{G_m \times G_{CS} \times V_{\text{REF}}}{C_{\text{OUT}} \times V_{\text{OUT}} \times C_{\text{COMP}}} 
\]

(8)

To get the overshoot peak value or undershoot valley value, calculate the extremum point \( t_{\text{EP}} \) when the derivative of \( v_{\text{OUT}}(t) \) equals 0 (see Equation 9).

\[
\frac{d\dot{v}_{\text{OUT}}(t_{\text{EP}})}{dt} = \begin{cases} 
\Delta I_{\text{OUT}} \times e^{-A t_{\text{EP}}} \times \left[ \sqrt{B - A^2} \times \cos\left( \sqrt{B - A^2} \times t_{\text{EP}} \right) - A \times \sin\left( \sqrt{B - A^2} \times t_{\text{EP}} \right) \right] - A \times \sin\left( \sqrt{B - A^2} \times t_{\text{EP}} \right) 
\times \frac{C_{\text{OUT}} \times \sqrt{B - A^2}}{2 \times C_{\text{OUT}} \times \sqrt{A^2 - B}} 
\text{while } A^2 < B \\
\Delta I_{\text{OUT}} \times e^{-A t_{\text{EP}}} \times \left( \sqrt{A^2 - B} \times e^{\sqrt{A^2 - B} \times t_{\text{EP}}} + \left( \sqrt{A^2 - B} + A \right) \times e^{-\sqrt{A^2 - B} \times t_{\text{EP}}} \right) 
\text{while } A^2 > B 
\end{cases}
\]

(9)

According to Equation 10, \( t_{\text{EP}} \) can be calculated:

\[
t_{\text{EP}} = \begin{cases} 
\arctan\left( \frac{\sqrt{B - A^2}}{A} \right) \times \frac{\sqrt{B - A^2}}{A} 
\text{while } A^2 < B \\
\ln\left( A + \sqrt{A^2 - B} \right) \times \frac{\sqrt{A^2 - B}}{A - \sqrt{A^2 - B}} 
\text{while } A^2 > B 
\end{cases}
\]

(10)

Substitute \( t_{\text{EP}} \) back into Equation 8, calculate the overshoot peak value or undershoot valley value with Equation 11.

\[
V_{\text{OS/US}} = v_{\text{OUT}} + \dot{v}_{\text{OUT}} \left( t_{\text{EP}} \right) \approx \begin{cases} 
V_{\text{OUT}} + \frac{\Delta I_{\text{OUT}} \times e^{-A t_{\text{EP}}} \times \sin\left( \sqrt{B - A^2} \times t_{\text{EP}} \right)}{C_{\text{OUT}} \times \sqrt{B - A^2}} 
\text{while } A^2 < B \\
V_{\text{OUT}} + \frac{\Delta I_{\text{OUT}} \times e^{-A t_{\text{EP}}} \times \left( e^{\sqrt{A^2 - B} \times t_{\text{EP}}} - e^{-\sqrt{A^2 - B} \times t_{\text{EP}}} \right)}{2 \times C_{\text{OUT}} \times \sqrt{A^2 - B}} 
\text{while } A^2 > B 
\end{cases}
\]

(11)

\( V_{\text{OUT}}(t) \) values at every moment can be calculated with Equation 11. So a time-continuous curve can be plotted with a mathematic tool like Mathcad or Excel.
3 Simulation Verification

Here we take TPS54335A as an example. We have made a design of a PCM buck regulator, working in the condition involved: \( V_{\text{IN}} = 12 \text{ V} \), \( V_{\text{OUT}} = 5 \text{ V} \), switching frequency 1 MHz, max load current 3 A. Load transient from 1 A to 3 A (\( \Delta I_{\text{OUT}} = 1 \text{ A} - 3 \text{ A} = -2 \text{ A} \)).

### Table 1. Design Example Specifications

<table>
<thead>
<tr>
<th>Design Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input voltage, ( V_{\text{IN}} )</td>
<td>9 V to 15 V, Typical 12 V</td>
</tr>
<tr>
<td>Output voltage, ( V_{\text{OUT}} )</td>
<td>5.0 V</td>
</tr>
<tr>
<td>Maximum output current ( I_{\text{OUT_MAX}} )</td>
<td>3 A</td>
</tr>
<tr>
<td>Undershoot or overshoot during load transient of 1 A to 3 A</td>
<td>3% of ( V_{\text{OUT}} )</td>
</tr>
<tr>
<td>Switching frequency, ( f_{\text{SW}} )</td>
<td>1 MHz</td>
</tr>
<tr>
<td>TPS54335A reference voltage, ( V_{\text{REF}} )</td>
<td>0.8 V</td>
</tr>
<tr>
<td>TPS54335A error amplifier gain, ( G_{\text{in}} )</td>
<td>1.3 A/V</td>
</tr>
<tr>
<td>TPS54335A power stage gain, ( G_{\text{CS}} )</td>
<td>8 A/V</td>
</tr>
</tbody>
</table>

#### 3.1 Output Filter Components Design

The inductance is based on the desired peak-to-peak ripple current \( \Delta I_{L} \). Because the ripple current increases with the input voltage, the maximum input voltage is always used to calculate the minimum inductance \( L_{\text{MIN}} \). Use **Equation 12** to calculate the minimum value of the output inductor. \( K_{\text{IND}} \) is a coefficient that represents the amount of inductor ripple current relative to the maximum output current. A reasonable value of \( K_{\text{IND}} \) should be 30% to 50%. For this application, choose \( K_{\text{IND}} = 0.5 \) for smaller inductance.

\[
L_{\text{MIN}} = \frac{V_{\text{IN_MAX}} - V_{\text{OUT}}}{I_{\text{OUT_MAX}}} \times \frac{V_{\text{OUT}}}{V_{\text{IN_MAX}} \times f_{\text{SW}}} = \frac{15 - 5}{3 \times 0.5} \times \frac{5}{15 \times 1} = 2.2 \mu\text{H}
\]

**Equation 12**

The desired response to a large load current change is usually the first criterion for output capacitance selection. When a large load step occurs, output capacitors provide the required charge before the inductor current can regulate to the appropriate level. The control loop of the regulator usually needs several clock cycles to regulate the inductor current equal to the new load level. The output capacitance must be large enough to supply or absorb the current difference to maintain the output voltage within the specified range. An engineering empirical formula, **Equation 13**, shows the minimum output capacitance needed for specified output overshoot or undershoot.

\[
C_{\text{OUT}} > \frac{\Delta I_{\text{OUT}} \times \left( \frac{1}{4 \times f_{C}} + \frac{1}{f_{\text{SW}}} \right)}{2 \times \Delta V_{\text{OUT}}} = 2 \times \left( \frac{1}{4 \times 0.05} + \frac{1}{2 \times 5 \times 3\%} \right) = 40 \mu\text{F}
\]

**Equation 13**

where

- \( f_{C} \) = crossover frequency

A good starting value for \( f_{C} \) is 1/20 to 1/10 of the switching frequency, \( f_{\text{SW}} \).

For this application, choose \( f_{C} \) to be 1/20 \times 1 MHz = 0.05 MHz. To leave some margin, choose 47 \( \mu \text{F} \) as the \( C_{\text{OUT}} \).

#### 3.2 Compensation Circuit Design

The general design guidelines for device loop compensation are as follows:

1. Determine the crossover frequency, \( f_{C} \). In this application, choose \( f_{C} = 0.05 \text{ MHz} \).
2. Use **Equation 14** to calculate the value of \( R_{\text{COMP}} \). A standard 8.87-k\( \Omega \) resistor is selected as \( R_{\text{COMP}} \).

\[
R_{\text{COMP}} = \frac{2\pi f_{C} \times C_{\text{OUT}} \times V_{\text{OUT}}}{V_{\text{REF}} \times G_{\text{in}} \times G_{\text{CS}}} = \frac{2\pi \times 0.05 \times 47 \times 5}{0.8 \times 1.3 \times 8} = 8.9 \text{ k}\Omega
\]

**Equation 14**
3. Design the $R_{\text{COMP}}$ and $C_{\text{COMP}}$ zero at 1/4 of the $f_c$. $C_{\text{COMP}}$ can be calculated by Equation 15.

$$C_{\text{COMP}} = \frac{2}{\pi \times f_c \times R_{\text{COMP}}} = \frac{2}{\pi \times 0.051 \times 8.87} = 1.4 \text{nF}$$

A standard 1.5-nF capacitor is selected as $C_{\text{COMP}}$.

### 3.3 Comparison of Simulation and Calculation Result

After completing the previous design procedure, all the design parameters are obtained in Table 2.

<table>
<thead>
<tr>
<th>$L$</th>
<th>$C_{\text{OUT}}$</th>
<th>$R_L$</th>
<th>$V_{\text{REF}}$</th>
<th>$G_m$</th>
<th>$R_{\text{COMP}}$</th>
<th>$C_{\text{COMP}}$</th>
<th>$G_{\text{CS}}$</th>
</tr>
</thead>
<tbody>
<tr>
<td>2.2 µH</td>
<td>47 µF</td>
<td>5 Ω</td>
<td>0.8 V</td>
<td>1.3 mA/V</td>
<td>8.87 kΩ</td>
<td>1.5 nF</td>
<td>8 A/V</td>
</tr>
</tbody>
</table>

With all the known parameters in Table 2, $A$ and $B$ can be calculated by and .

$$A = \frac{G_m \times G_{\text{CS}} \times V_{\text{REF}} \times R_{\text{COMP}}}{2 \times C_{\text{OUT}} \times V_{\text{OUT}}} = 1.57 \times 10^5$$

$$B = \frac{G_m \times G_{\text{CS}} \times V_{\text{REF}}}{C_{\text{OUT}} \times V_{\text{OUT}} \times C_{\text{COMP}}} = 2.36 \times 10^{10}$$

Because $A^2 > B$, the critical point $t_{\text{EP}}$ can be calculated by the second formula in Equation 10.

$$t_{\text{EP}} = \frac{\ln \left( \frac{A + \sqrt{A^2 - B}}{A - \sqrt{A^2 - B}} \right)}{2 \times \sqrt{A^2 - B}} = 6.46 \times 10^{-6}$$

Finally, the undershoot can be calculated by Equation 11.

$$V_{\text{OS/US}} = V_{\text{OUT}} + \hat{V}_{\text{OUT}} (t_{\text{EP}}) \approx V_{\text{OUT}} + \frac{\Delta I_{\text{OUT}} \times e^{-A \times t_{\text{EP}}} \times \left( e^{\sqrt{A^2 - B} \times t_{\text{EP}}} - e^{-\sqrt{A^2 - B} \times t_{\text{EP}}} \right)}{2 \times C_{\text{OUT}} \times \sqrt{A^2 - B}} = 4.9 \text{ V}$$

To verify the calculation result, we built a simulation model of PCM buck regulator according to Figure 1 in Simplis™. Figure 3 shows the Simplis simulation circuit diagram. A load transient from 1 A to 3 A occurs at 0.1 ms (Figure 4 shows the simulation result). The undershoot valley value 4.9 V occurs at 6.5 µs after the load transient happens, which is almost the same as the calculated values in Equation 18 and Equation 19. In Figure 4, an additional time-continuous curve is plotted using Mathcad with Equation 11. The calculated curve matches well with the simulated curve during the entire period.
Figure 3. Simplis™ Simulation Circuit

Figure 4. Comparison of Simulation and Calculation Result

4 References


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