

# Application Brief

## Multi-Function Pins for Easy Designing



Nicole Lemieux

### Multi-Function Pins are Easy... to Understand

A multi-function pinout is when two or more features are integrated into one pin. A table found in the device's data sheet is used to decipher what features are available with guidance of how to select the desired combination. [Table 1](#) is an example of the mode pin selection of [TPS563211](#) data sheet. There are two feature selections with a total of four combinations. A resistor value (in the range of acceptable values) strapped from ground to the mode pin is used to select both feature operations. Not all multi-function pins cover the same features, some are bifunctional, either-or, while others, such as mode or S-config pins, cover 2, 3, 4+ features in just one pin, exemplified in [Table 2](#).

**Table 1. MODE Pin Settings**

Mode Resistor Range	Recommended Mode Resistor Value	Operation Mode in Light Mode	Function of PG/SS Pin
[0, 12] kΩ	0	Eco-mode	Power Good
[30, 50] kΩ	47 kΩ	Eco-mode	Soft Start
[83, 120] kΩ	100 kΩ	FCCM	Soft Start
[180, ∞] kΩ	Float	FCCM	Power Good

### Multi-Function Pins are Easy... to Design With

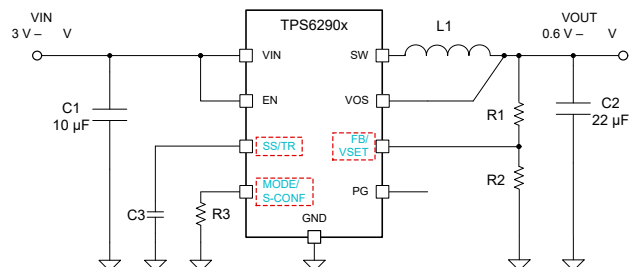
Since multi-function pins combine what used to be individual pins into one, the resulting smaller package has less pins that need to be configured. Less pins mean less time researching to find the right passive, less time calculating to find the right values, and less time placing each component in a layout. The more features a single pin incorporates, the better the flexibility to design time ratio.

**Table 2. Features Configurable by a Single Multifunction Pin**

Feature Covered by one Multi-function Pin (Mode/S-config)	TPS62913/2	TPS62903/2/1	TPS563211 / 2211
Package Size	2 x 2 mm	1.5 x 2 mm	1.2 x 2.1 mm
Function of Vset/FB pin		X	
Fsw	X	X	
Spread Spectrum	X		
Output Discharge	X	X	
Synchronization	X		
Light load or FPWM/ FCCM		X	X
FUNCTION OF PG/SS PIN			X

### Multi-Function Pins are Easy... to Reuse

The ability to reuse a design for various projects with minimal alteration saves time and development cost. Multi-functional pins are ideal when it comes to this copy and paste methodology – a simple resistor change could be all that is needed from one project to another. In some cases, such as the TPS62903, multiple multi-function pins are used for additional flexibility shown in [Figure 1](#) to cover very different applications with just one part, from space constrained smart locks needing low quiescent current, to data centers needing high efficiency.



**Figure 1. Multi-Function Pins**

## Multi-Function Pins are Easy... for Supply Chain

Multi-function pins help simplify a design with fewer external components to configure the feature sets. Fewer passives needed to design-in a given part results in a smaller total solution size and cost. See the second reference in [Additional Resources](#) section. Using the same device on multiple projects and a smaller BOM count both contribute to drastically simplifying the supply chain.

Reducing package size is the driving factor behind the creation of multi-function pinouts, but there are many more benefits that they bring to the table. Using products that incorporate one or more multi-function pins can result in reduced solution size and BOM count, increased application flexibility, quicker time-to-market, and a simplified supply chain. All making life a little easier.

## Additional Resources

- Texas Instruments, [Understanding the Trade-offs and Technologies to Increase Power Density](#)
- Texas Instruments, [Which Pinout is Best? How Individual, Multifunctional and Trimmed Pinouts Help Address Design Challenges](#)

## IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATASHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, or other requirements. These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to TI's Terms of Sale (<https://www.ti.com/legal/termsofsale.html>) or other applicable terms available either on [ti.com](https://www.ti.com) or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265  
Copyright © 2021, Texas Instruments Incorporated