

Setting Up AM37x SDRC Registers

ARM CPU Applications

ABSTRACT

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http://processors.wiki.ti.com/index.php/Setting_up_AM37x_SDRC_registers

This wiki article describes how to set the SDRC registers in the OMAP35x SDRC module depending on the mDDR memory that you are using. Only 32-bit mDDR configurations are addressed in this article.

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