

Using C6Accel: ARM Access to DSP Software on TI SoCs

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ABSTRACT

For application developers of TI's system-on-chips (SoCs), the free CEZ6Accel software development tool makes the development process quick, easy and cost-effective. The tool gives developers access to a framework that allows access to more than 100 ready-to-use digital signal processor (DSP) kernels, enabling DSP programming without writing code. C6EZAccel is a powerful tool that enables ARM® applications to leverage the DSP as an accelerator. With C6EZAccel, system developers of any skill level can leverage the computational power of TI's DSPs to create various well featured end applications. Benefits include:

- Easy to interface: Contains ARM-side APIs that abstract the DSP.
- Availability of tested and benchmarked kernels: Provides easy access to more than 100 optimized DSP kernels, allowing system developers to add differentiation without writing DSP code.
- Reduces learning curve: Enables programming without learning DSP coding or architecture.
- Performance: Utilizes DSP to run ready-to-use compute intense algorithms, allowing more efficient use of the SoC and avoiding processor upgrades. Enables parallel processing on DSP and ARM.

There is more information on C6Accel, including usage examples and a list of frequently asked questions, on TI's Embedded Processors Wiki site located at:

http://processors.wiki.ti.com/index.php/C6Accel:_ARM_access_to_DSP_software_on_TI_SoCs

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