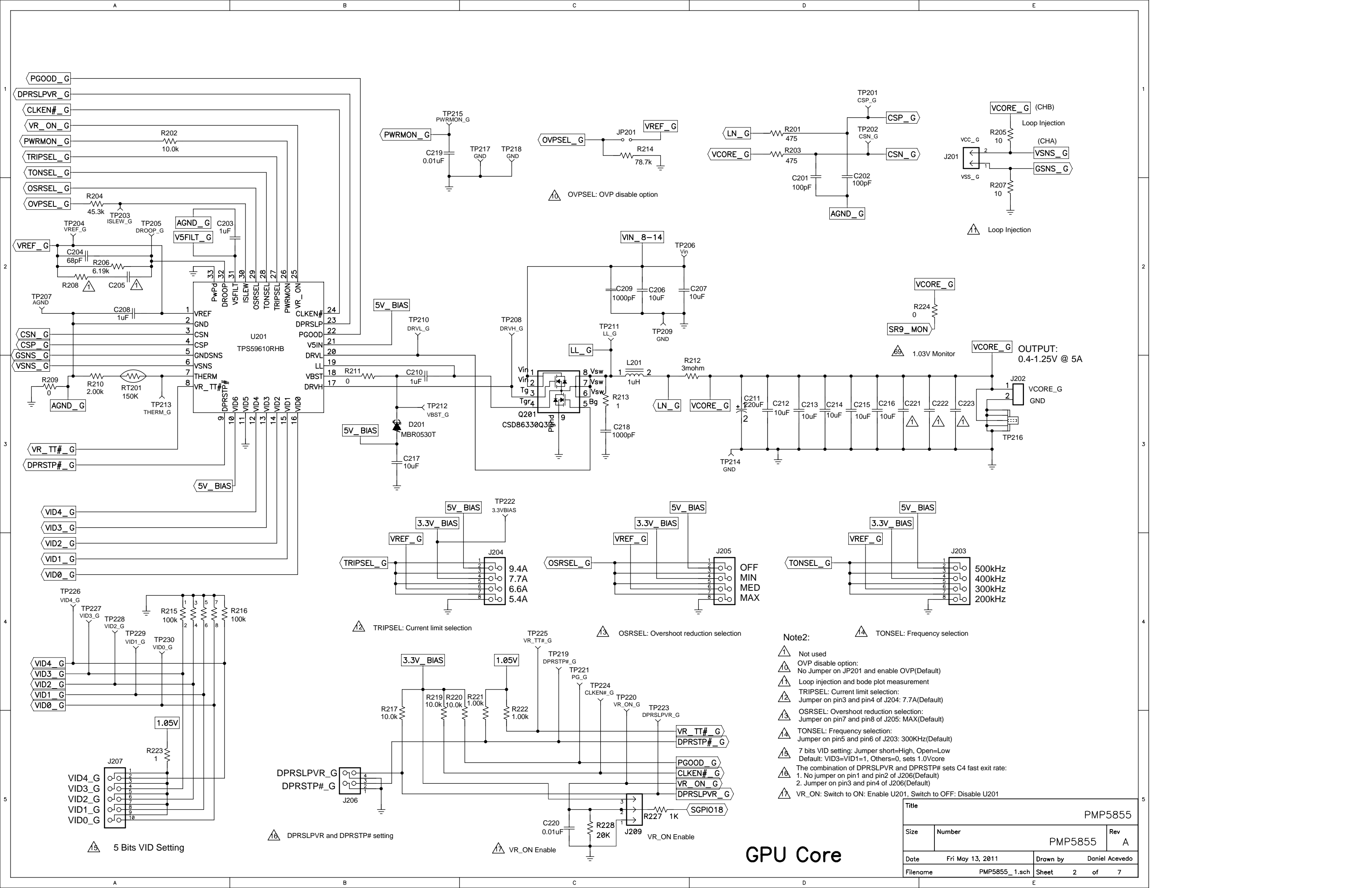


Note1:

- △ Not used
- △ OVP disable option: No Jumper on JP101 and enable OVP(Default)
- △ Loop injection and bode plot measurement
- △ TRIPSEL: Current limit selection: Jumper on pin3 and pin4 of J104: 7.7A(Default)
- △ OSRSEL: Overshoot reduction selection: Jumper on pin7 and pin8 of J105: MAX(Default)
- △ TONSEL: Frequency selection: Jumper on pin5 and pin6 of J103: 300KHz(Default)
- △ 7 bits VID setting: Jumper short=High, Open=Low Default: VID5=VID3=1, Others=0, sets 1.0Vcore
- △ The combination of DPRSLPVR and DPRSTP# sets C4 fast exit rate:
  1. No jumper on pin1 and pin2 of J106(Default)
  2. Jumper on pin3 and pin4 of J106(Default)
- △ VR\_ON: Switch to ON: Enable U101, Switch to OFF: Disable U101

# CPU Core

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OVPSEL: OVP disable option

Loop Injection

1.03V Monitor

VCORE\_G OUTPUT:  
0.4-1.25V @ 5A

TRIPSEL: Current limit selection

OSRSEL: Overshoot reduction selection

TONSEL: Frequency selection

Note2:

- ⚠ Not used
- ⚠ OVP disable option:  
No Jumper on JP201 and enable OVP(Default)
- ⚠ Loop injection and bode plot measurement
- ⚠ TRIPSEL: Current limit selection:  
Jumper on pin3 and pin4 of J204: 7.7A(Default)
- ⚠ OSRSEL: Overshoot reduction selection:  
Jumper on pin7 and pin8 of J205: MAX(Default)
- ⚠ TONSEL: Frequency selection:  
Jumper on pin5 and pin6 of J203: 300KHz(Default)
- ⚠ 7 bits VID setting: Jumper short=High, Open=Low  
Default: VID3=VID1=1, Others=0, sets 1.0Vcore
- ⚠ The combination of DPRSLPVR and DPRSTP# sets C4 fast exit rate:  
1. No jumper on pin1 and pin2 of J206(Default)  
2. Jumper on pin3 and pin4 of J206(Default)
- ⚠ VR\_ON: Switch to ON: Enable U201, Switch to OFF: Disable U201

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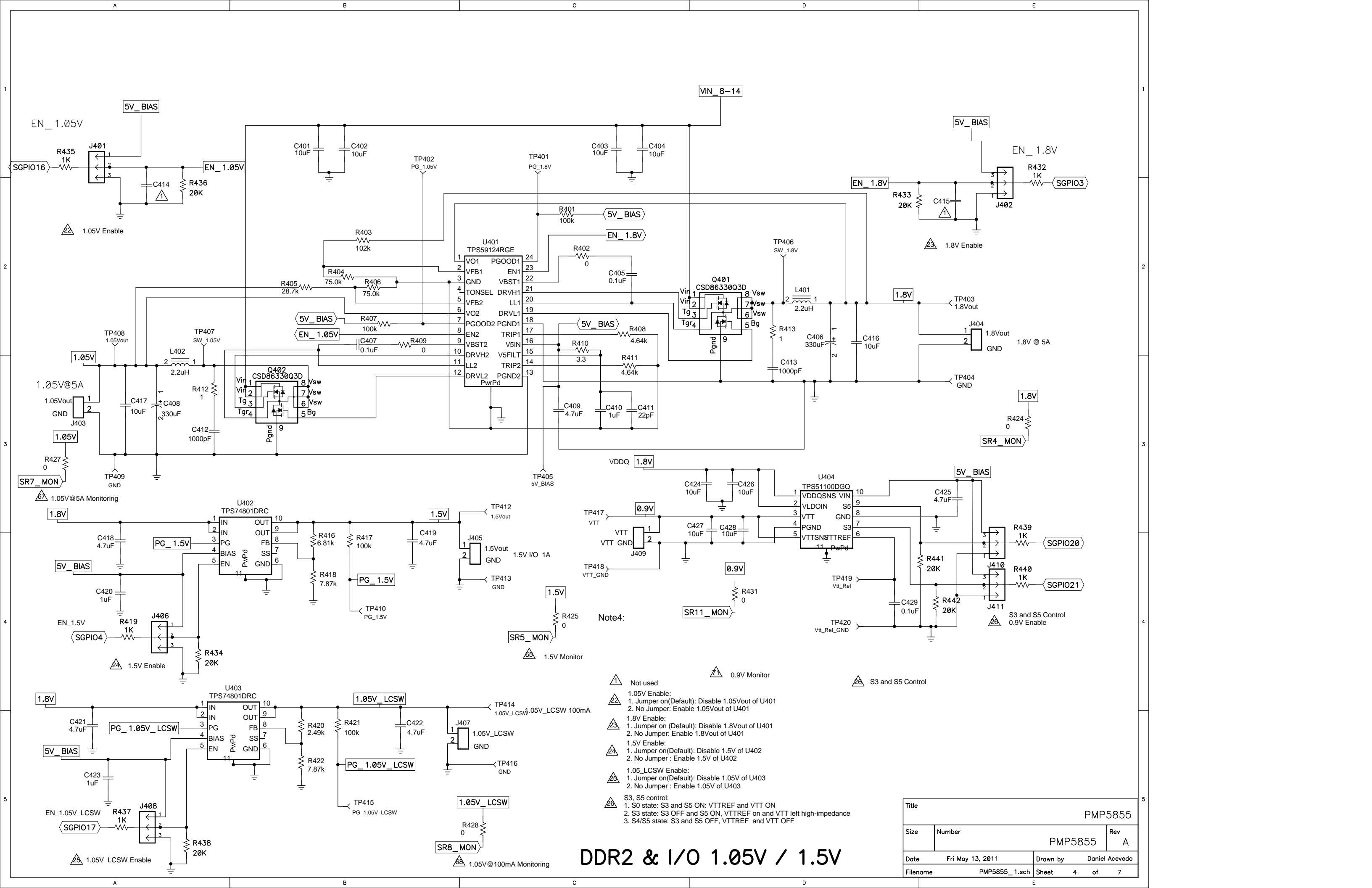
# GPU Core

5 Bits VID Setting

DPRSLPVR and DPRSTP# setting

VR\_ON Enable



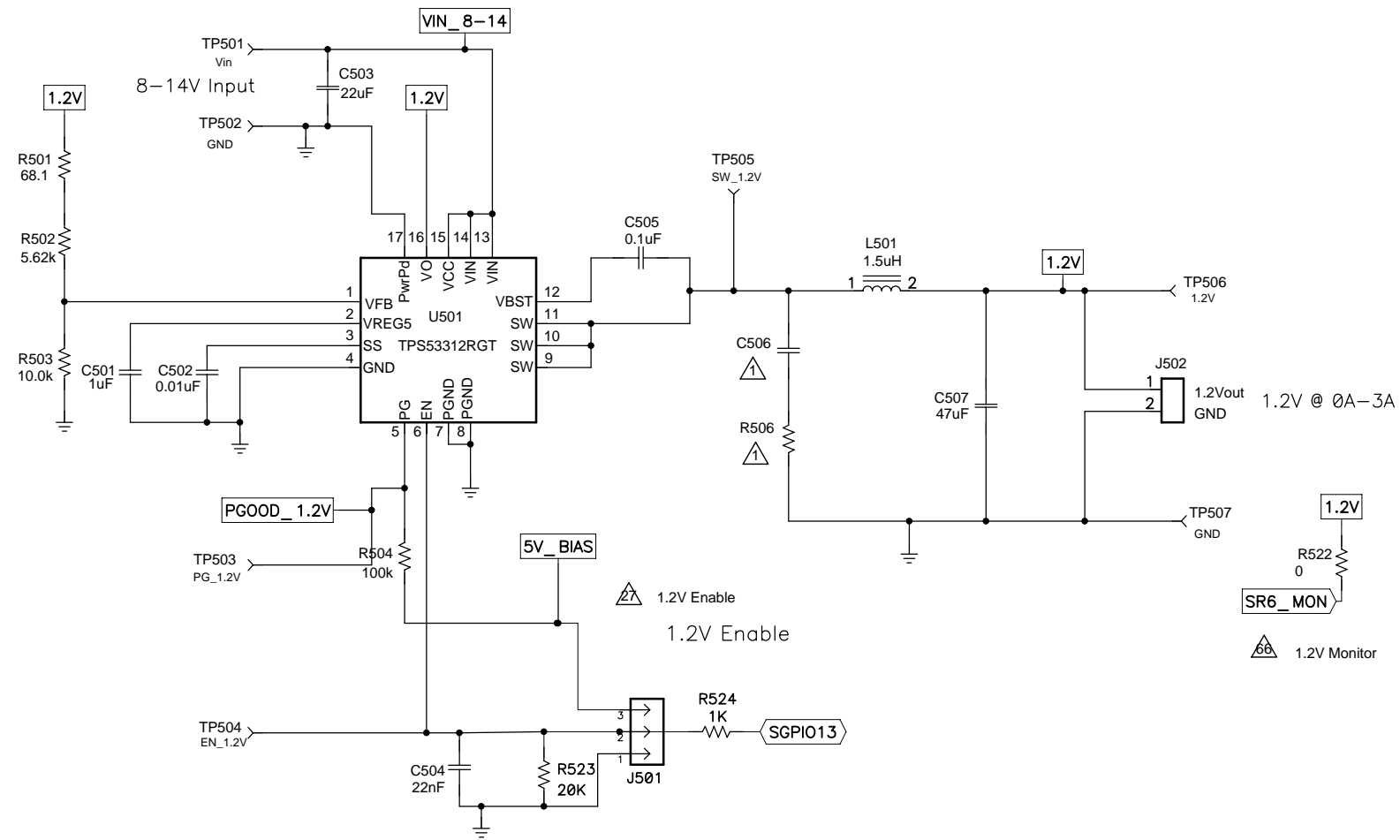


Note4:

- ⚠ Not used
- ⚠ 1.05V Enable:
  1. Jumper on(Default): Disable 1.05Vout of U401
  2. No Jumper: Enable 1.05Vout of U401
- ⚠ 1.8V Enable:
  1. Jumper on (Default): Disable 1.8Vout of U401
  2. No Jumper: Enable 1.8Vout of U401
- ⚠ 1.5V Enable:
  1. Jumper on(Default): Disable 1.5V of U402
  2. No Jumper : Enable 1.5V of U402
- ⚠ 1.05V\_LCSW Enable:
  1. Jumper on(Default): Disable 1.05V of U403
  2. No Jumper : Enable 1.05V of U403
- ⚠ S3, S5 control:
  1. S0 state: S3 and S5 ON: VTTREF and VTT ON
  2. S3 state: S3 OFF and S5 ON, VTTREF on and VTT left high-impedance
  3. S4/S5 state: S3 and S5 OFF, VTTREF and VTT OFF

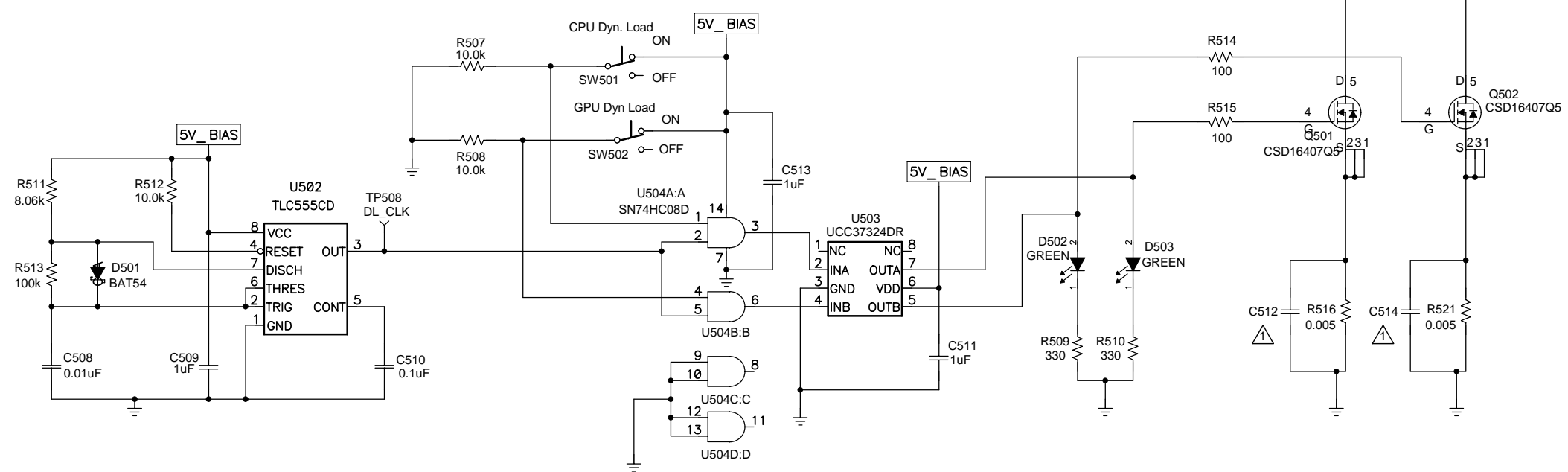
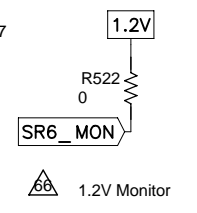
## DDR2 & I/O 1.05V / 1.5V

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**Note5:**

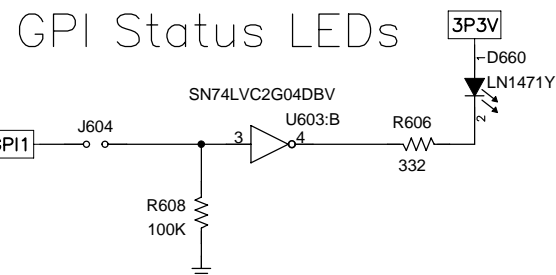
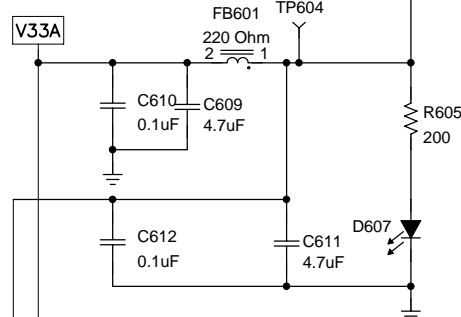
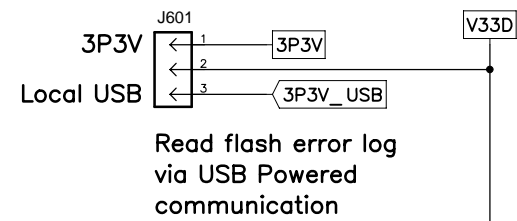
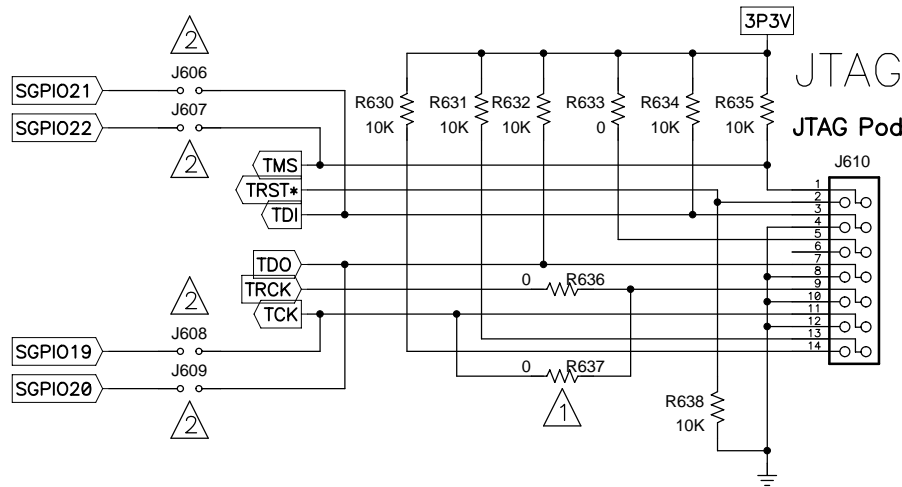
- ⚠ Not used
- ⚠ 1.2V Enable:
  1. Jumper on(Default): Disable 1.2V of U501
  2. No Jumper : Enable 1.2V of U501
- ⚠ On Board Dynamic Load:
  1. SW501: ON Enable CPU Core Dynamic load  
OFF Disable CPU Core Dynamic load
  2. SW502: ON Enable GPU Core Dynamic load  
OFF Disable GPU Core Dynamic load



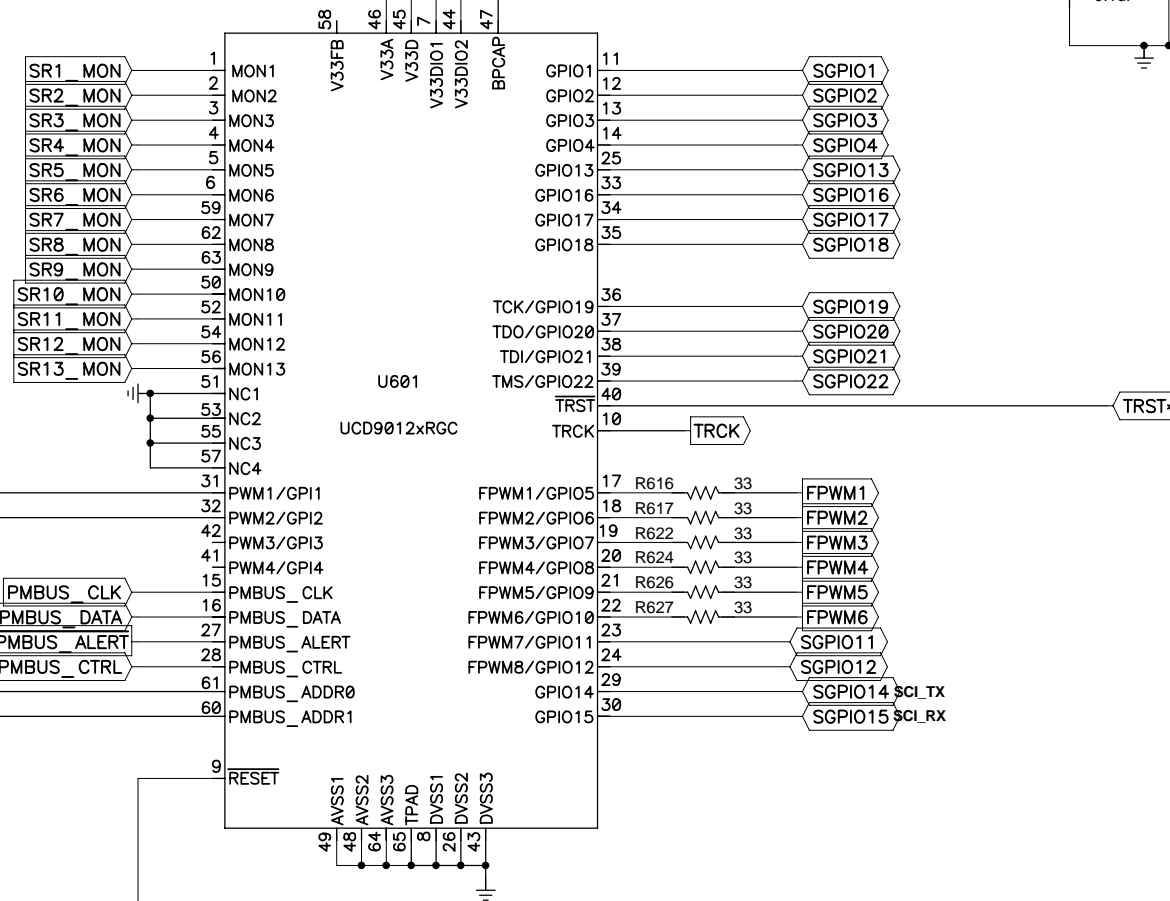
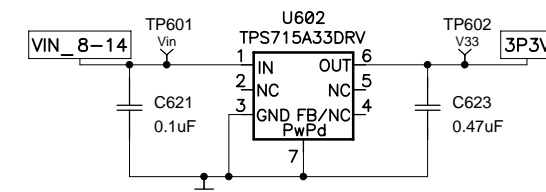
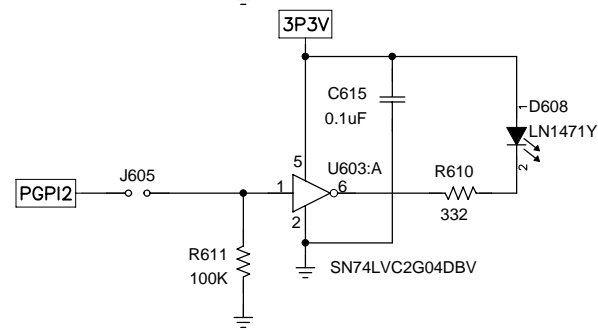
⚠ On Board Dynamic Load

# 1.2V IOH CORE

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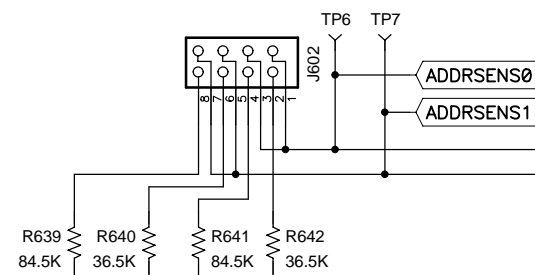


What is V33FB supposed to be connected to?

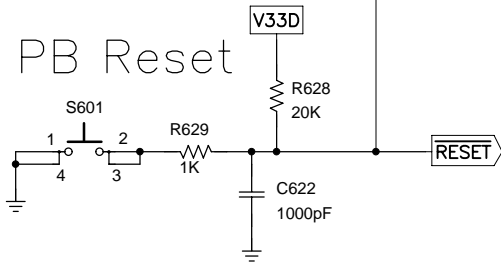


### PMBus Address

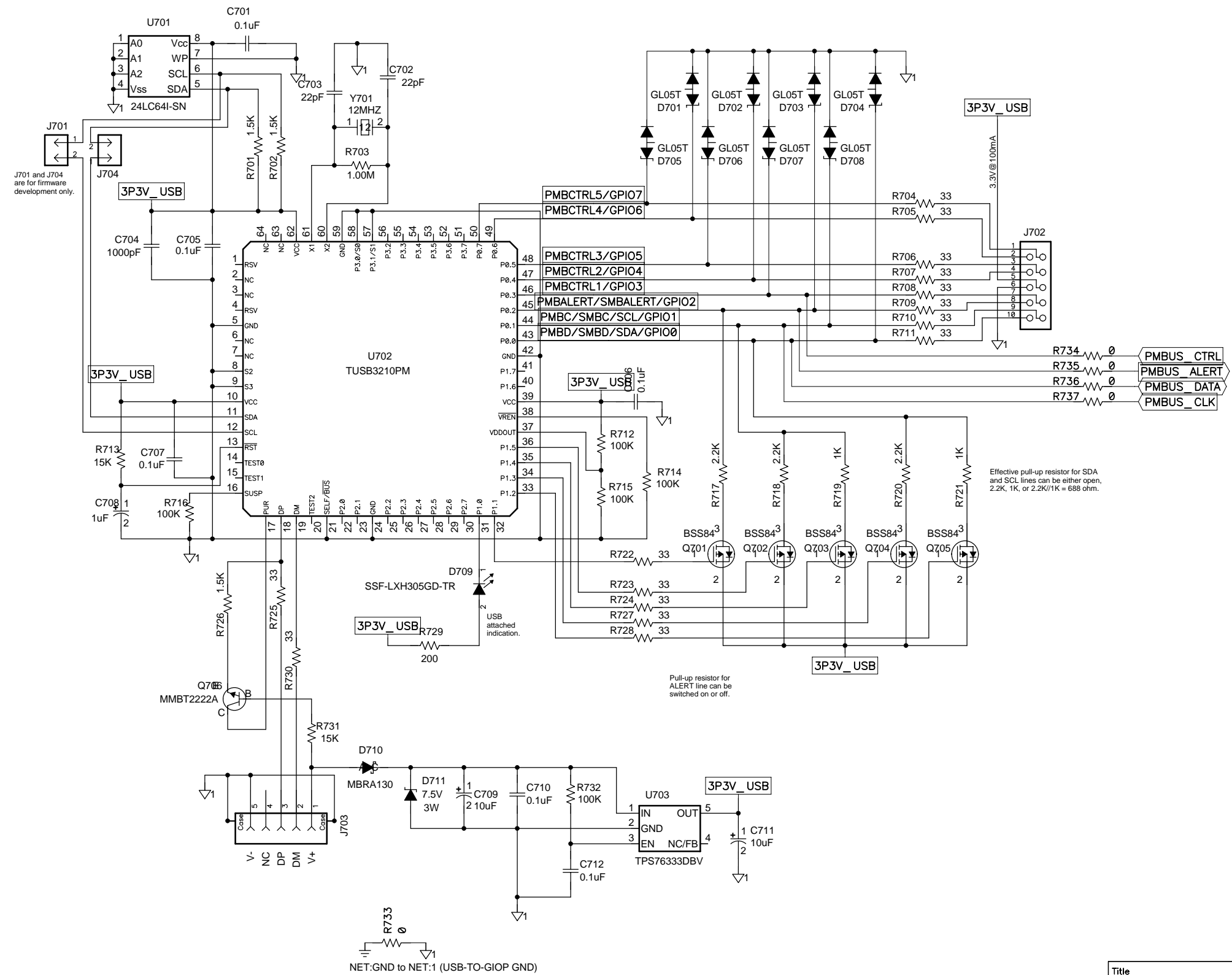
PMBus Address (decimal) = 12\*ADDRSENS1 + ADDRSENS0



- ADDRSENS1 = 8
- ADDRSENS1 = 5
- ADDRSENS0 = 8
- ADDRSENS0 = 5



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