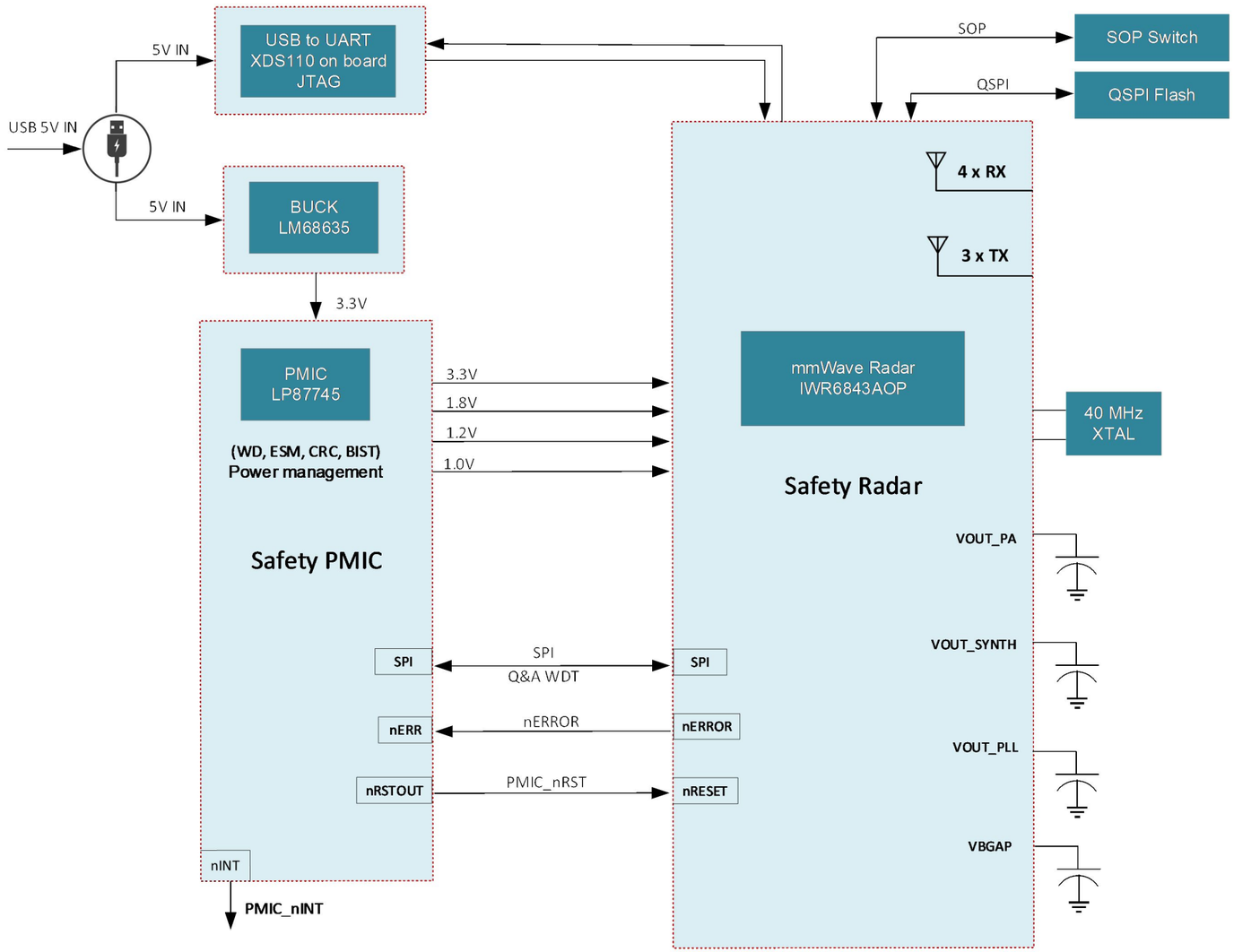
Safety mmWave + Safety PMIC Block Diagram



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TID #: TIDA-010281	Project Title: Safety mmWave radar with safety PMIC design	
Number: N/A Rev: A	Sheet Title: BLOCK_DIAGRAM	
SVN Rev:	Assembly Variant: 001	Sheet: 1 of 10
Drawn By: Antony/Bala	File: TIDA-010281_BLOCK_DIAGRAM.SchDoc	Size: B
Engineer: Preeti Udapudi	Contact: http://www.ti.com/support	

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TID #: TIDA-010281	Project Title: Safety mmWave radar with safety PMIC design	
Number: N/A Rev: A	Sheet Title: TABLE_OF_CONTENTS	
SVN Rev:	Assembly Variant: 001	Sheet: 2 of 10
Drawn By: Antony/Bala	File: TIDA-010281_TABLEOFCONTENTS.SchDoc	Size: B
Engineer: Preeti Udapudi	Contact: http://www.ti.com/support	

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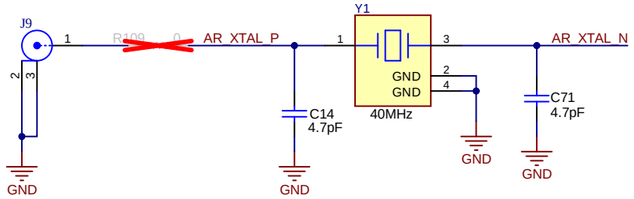
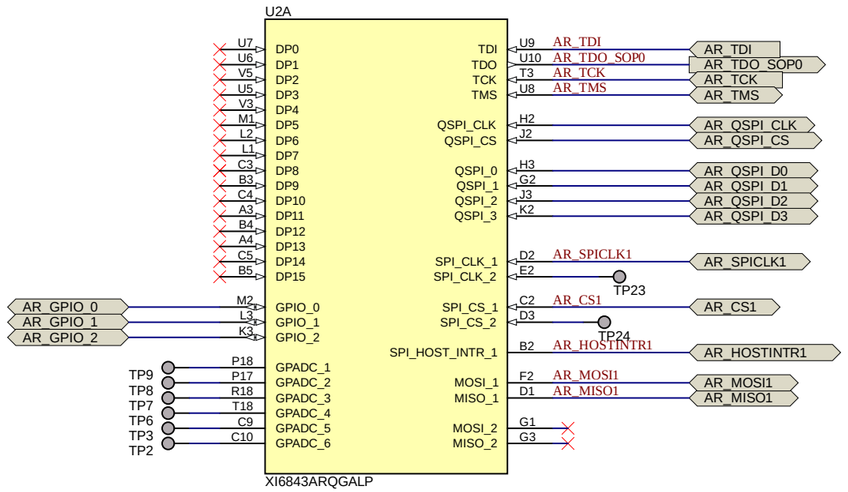
SOP_MODE2 - '011' - DEV/DEBUG
SOP_MODE4 - '001' - FUNCTIONAL MODE
SOP_MODE5 - '101' - FLASH MODE

AOP IO

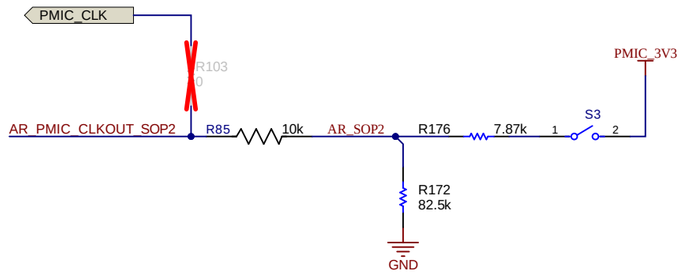
40MHz CRYSTAL

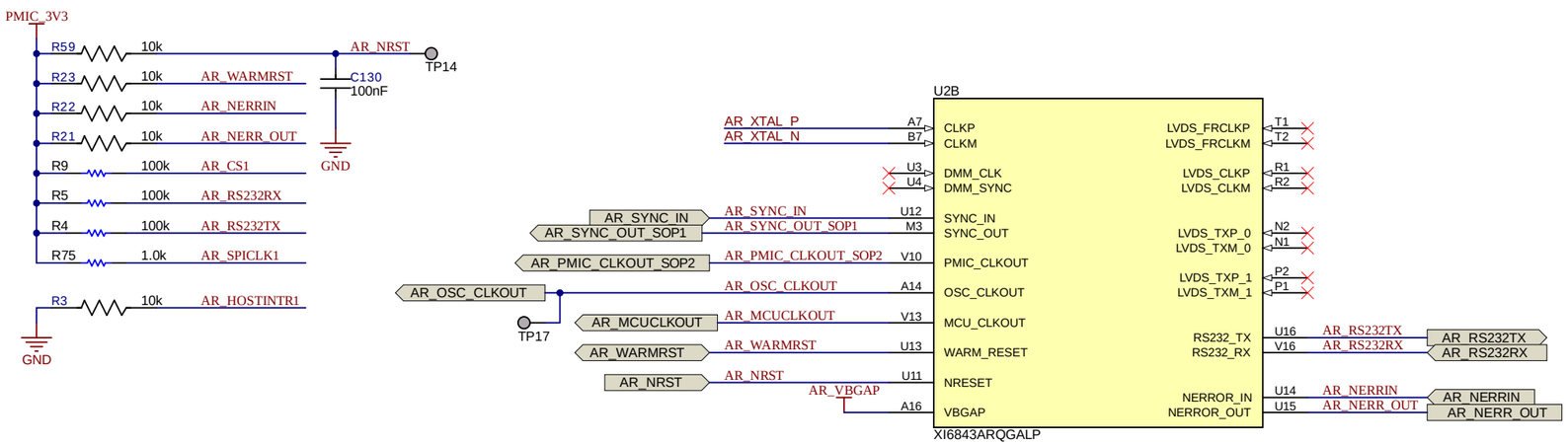


Avoid stubs on XTAL pins.



SOP OPTIONS





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TID #: TIDA-010281		Project Title: Safety mmWave radar with safety PMIC design	
Number: N/A	Rev: A	Sheet Title: AOP_IO	
SVN Rev: 16110		Assembly Variant: 001	Sheet: 3 of 10
Drawn By: Antony/Bala		File: TIDA-010281_AOP_IO.SchDoc	Size: B
Engineer: Preeti Udapudi		Contact: http://www.ti.com/support	

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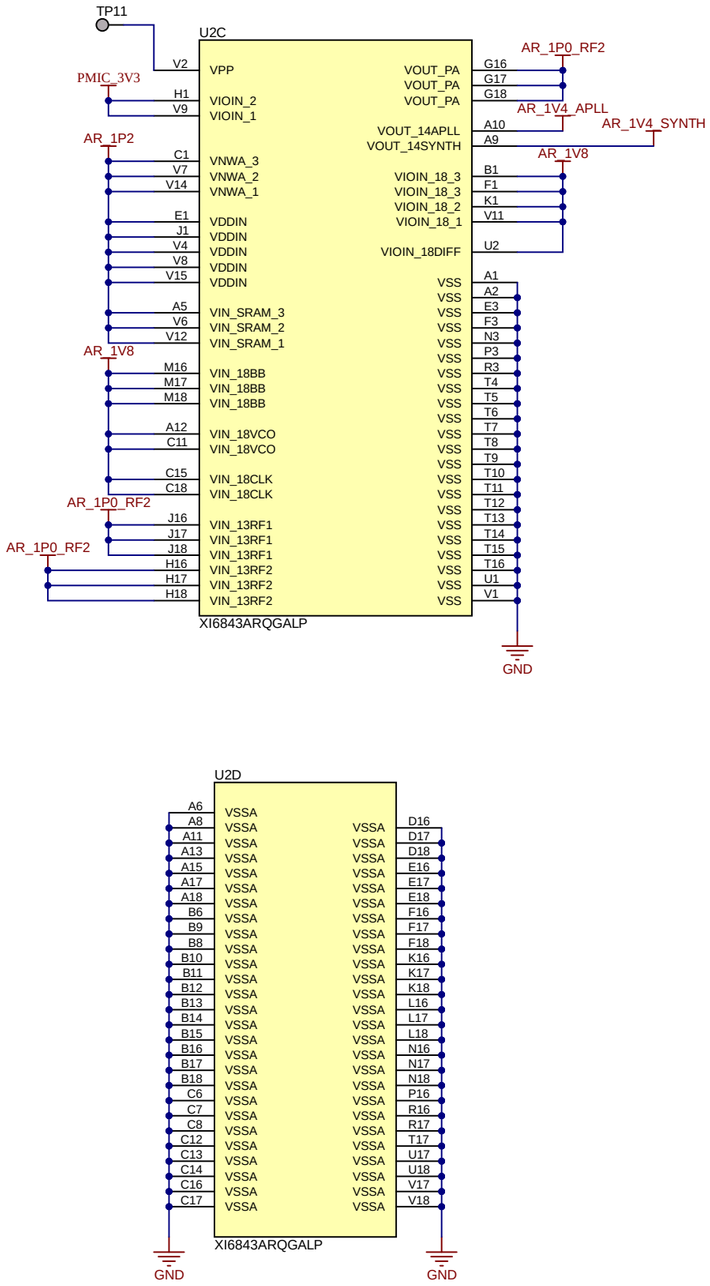
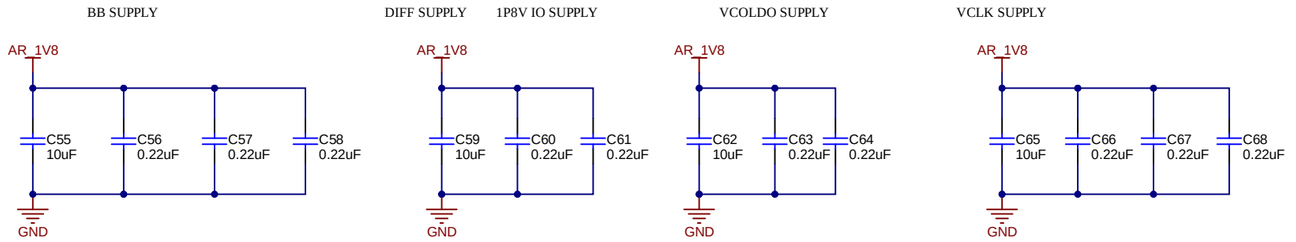
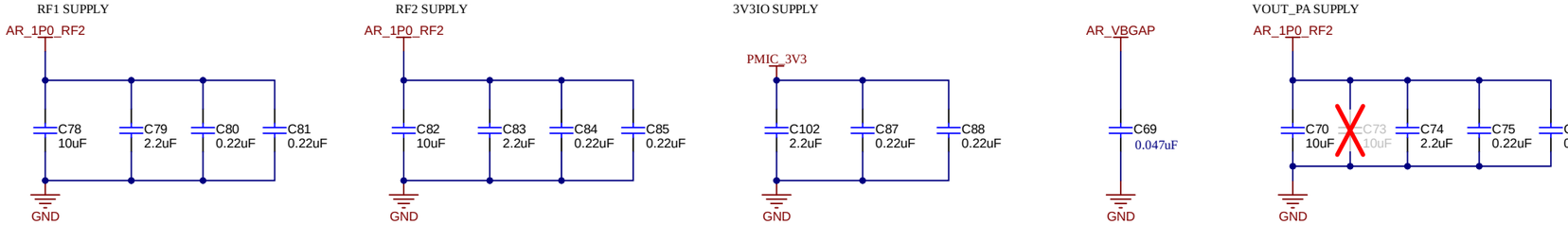
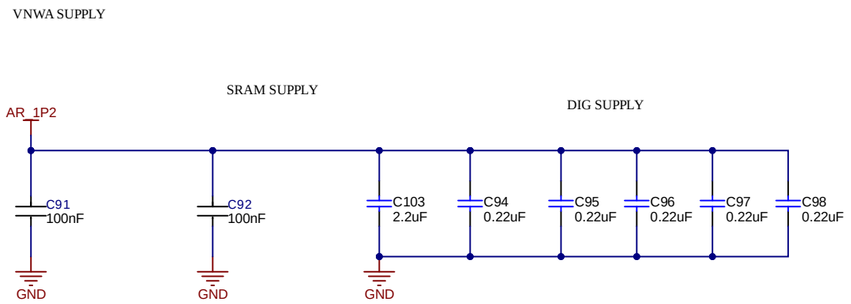
AOP POWER

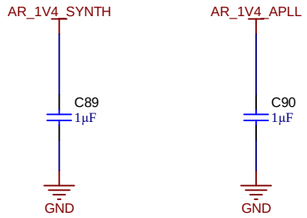
DECOUPLING CAPS











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TID #: TIDA-010281	Project Title: Safety mmWave radar with safety PMIC design	
Number: N/A Rev: A	Sheet Title: AOP_POWER	
SVN Rev:	Assembly Variant: 001	Sheet: 4 of 10
Drawn By: Antony/Bala	File: TIDA-010281_AOP_PWR.SchDoc	Size: B
Engineer: Preeti Udapudi	Contact: http://www.ti.com/support	

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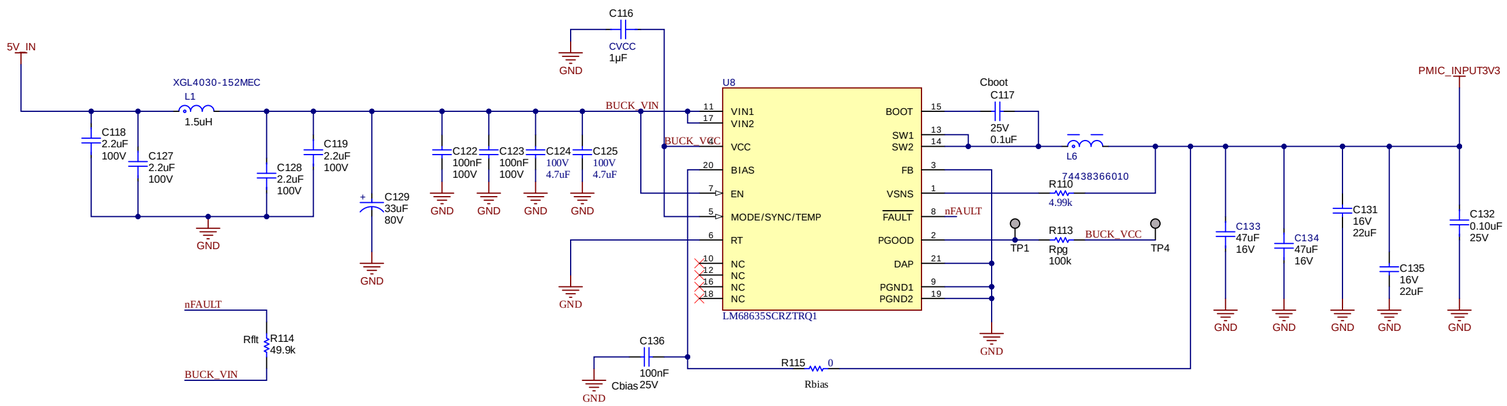
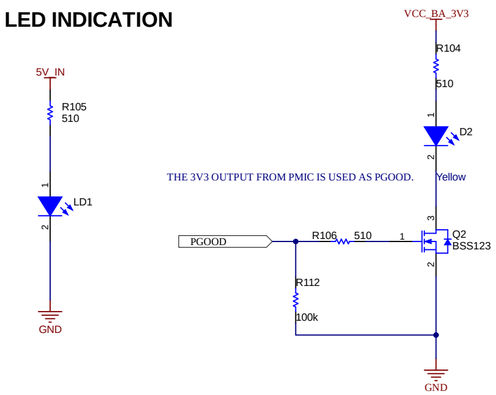
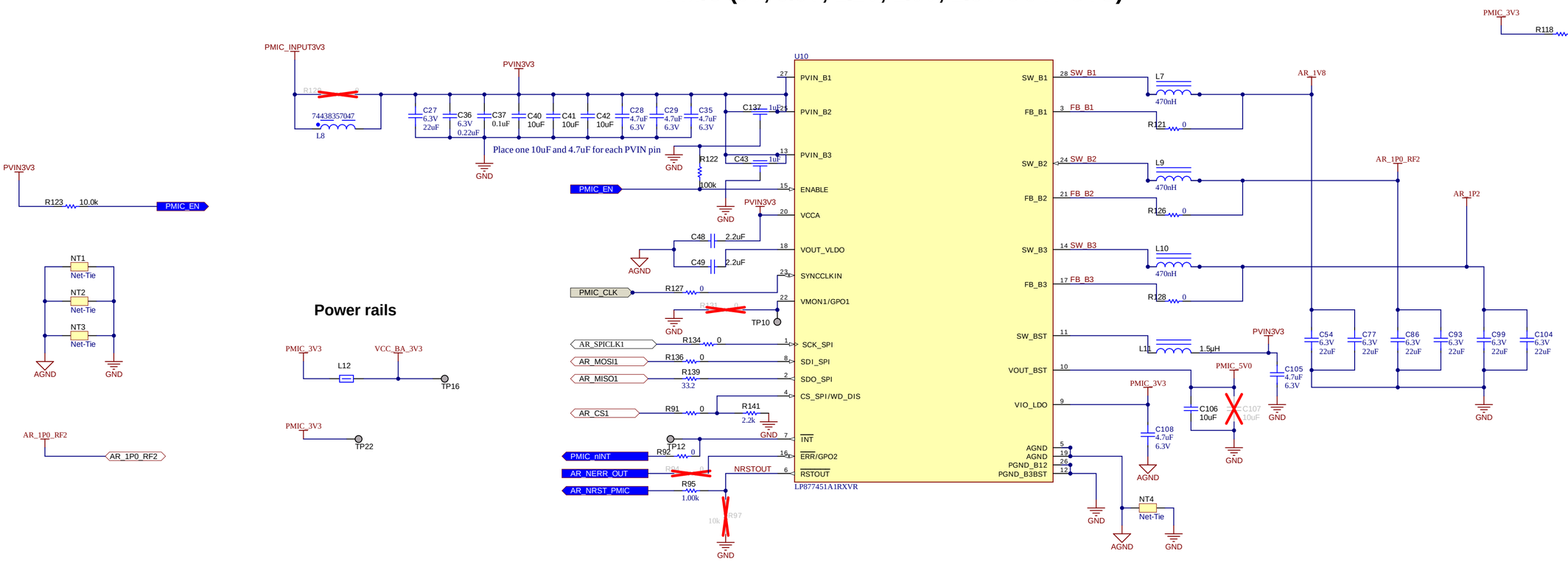
Buck converter (3.3V output)



5V LED INDICATION



PMIC (5V, 3.3V, 1.2V, 1.0V, 1.8V OUTPUTS)



Power rails

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Orderable: IWR6843AOPEVM	Designed for: Public Release	Mod. Date: 10/26/2025	
TID #: TIDA-010281	Project Title: Safety mmWave radar with safety PMIC design		
Number: N/A Rev: A	Sheet Title:		
SVN Rev:	Assembly Variant: 001	Sheet: 5 of 10	
Drawn By:	File: TIDA-010281_PMIC_BUCK.SchDoc	Size: C	
Engineer: Preeti Udapudi	Contact: http://www.ti.com/support		

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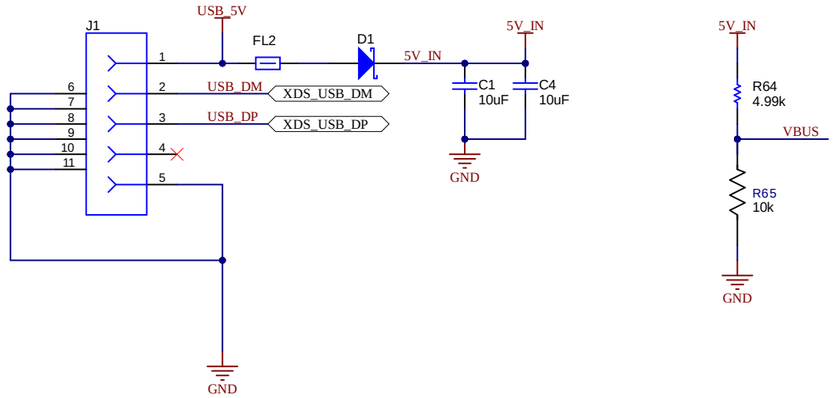
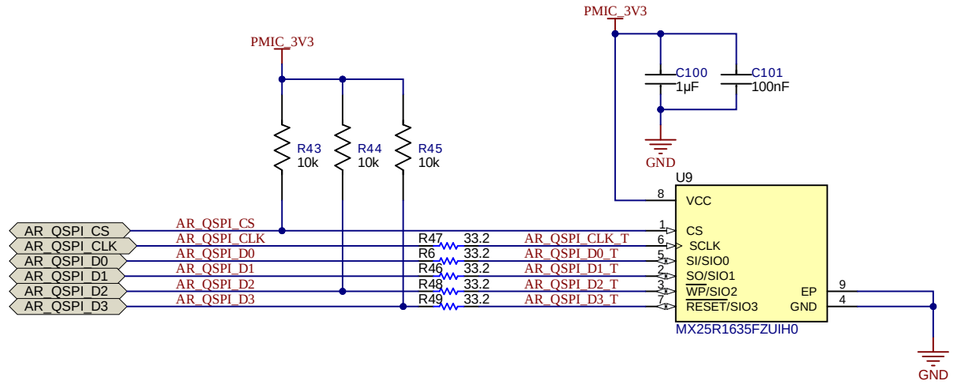
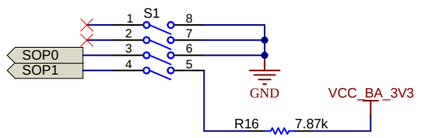
USB CONNECTOR



QSPI FLASH



SWITCH CONTROL MUX SELECTION, SOPs, BT CONTROL



SOP CONFIGURATION

Mode	SOP0 (S1.3)	SOP1 (S1.4)	SOP2 (S3)
Functional Mode	OFF	OFF	OFF
Flash Mode	OFF	OFF	ON
MMWAVEICEBOOST mode (DCA1000, JTAG, and so forth)	OFF	ON	OFF

PIN MUX SETTINGS

Designator	Switch ON	Switch OFF
S1.1	Breakaway UART	CP2105UART
S1.2	60 Pin UART	BT UART
S2.1	CAN	SPI
S2.2	60 Pin CS	BT/LCD CS
S2.3	BT Enable	BT Disable

PIN MUX SETTINGS

	S1.1	S1.2	S2.1	S2.2	S2.3
Stand alone Mode	OFF	N/A	N/A	N/A	N/A
MMWAVEICEBOOST	ON	ON	OFF	OFF	N/A

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TID #: TIDA-010281	Project Title: Safety mmWave radar with safety PMIC design	
Number: N/A Rev: A	Sheet Title: FLASH&USB_TO_UART	
SVN Rev:	Assembly Variant: 001	Sheet: 6 of 10
Drawn By: Antony/Bala	File: TIDA-010281_FLASH_USB_SOP.SchDoc	Size: B
Engineer: Preeti Udapudi	Contact: http://www.ti.com/support	

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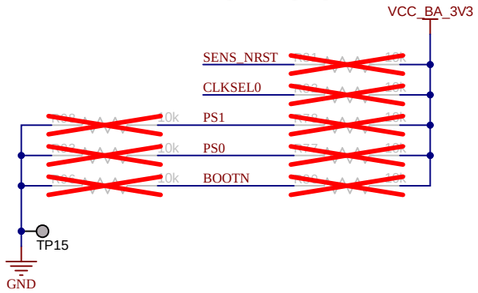
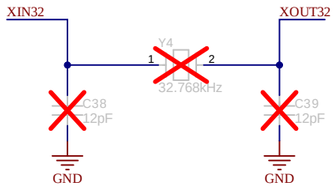
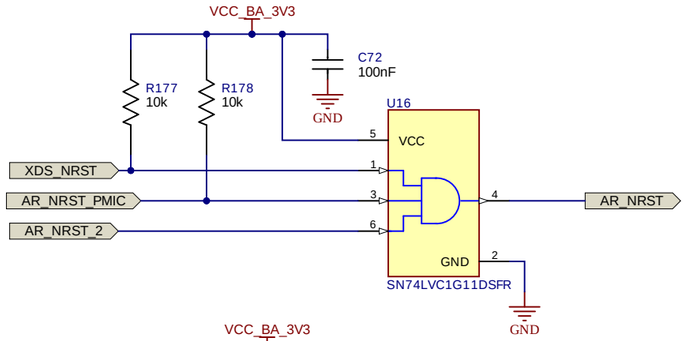
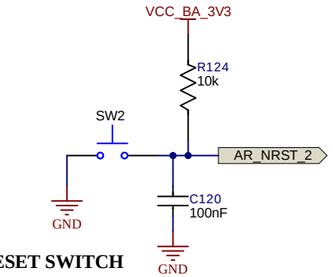
PULL-UPs

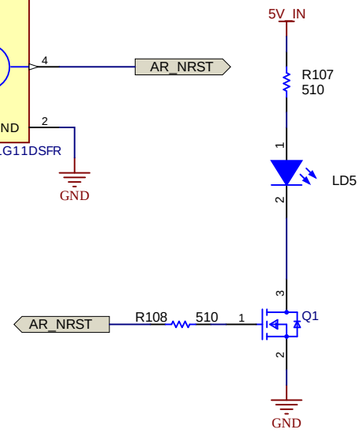


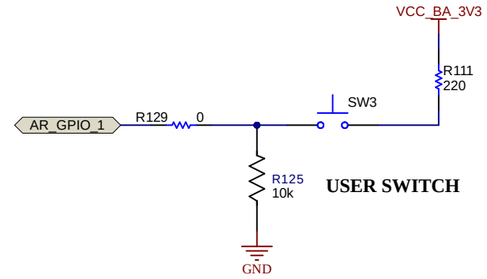




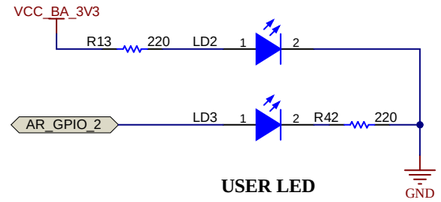


RESET SWITCH

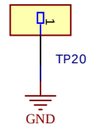




USER SWITCH



USER LED



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Orderable: IWR6843AOPEVM		Designed for: Public Release	Mod. Date: 8/14/2025
TID #: TIDA-010281		Project Title: Safety mmWave radar with safety PMIC design	
Number: N/A	Rev: A	Sheet Title: BREAKAWAY_SECTION2	
SVN Rev:		Assembly Variant: 001	Sheet: 7 of 10
Drawn By: Antony/Bala		File: TIDA-010281_RST_SWITCH.SchDoc	Size: B
Engineer: Preeti Udapudi		Contact: http://www.ti.com/support	

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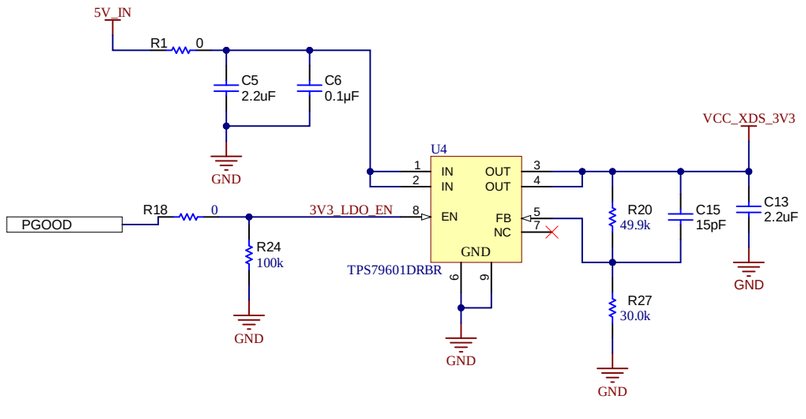
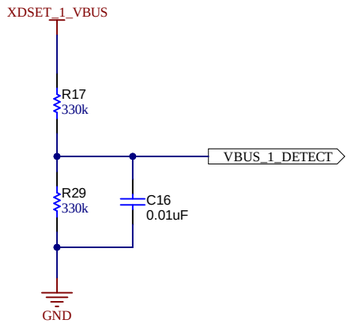
XDS110(1/2)

5V TO 3.3V LDO

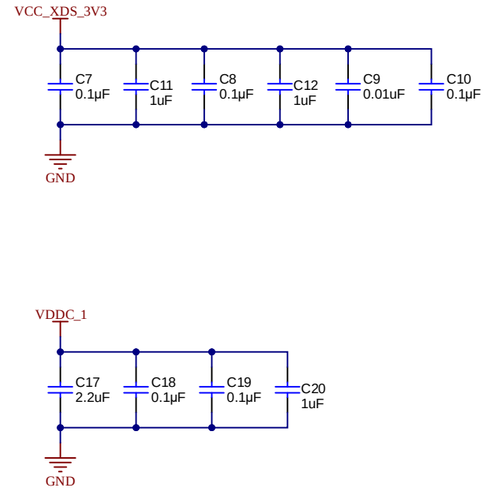


By Default LDO is disabled
When 3V3 DC-DC regulator is powered up, then it gets enabled

VBUS_DETECT



DECOUPLING CAPACITORS - XDS110



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Orderable: IWR6843AOPEVM	Designed for: Public Release	Mod. Date: 10/17/2025
TID #: TIDA-010281	Project Title: Safety mmWave radar with safety PMIC design	
Number: N/A Rev: A	Sheet Title:	
SVN Rev:	Assembly Variant: 001	Sheet: 8 of 10
Drawn By:	File: TIDA-010281_XDS110_interface_1A.SchDoc	Size: B
Engineer: Preeti Udapudi	Contact: http://www.ti.com/support	

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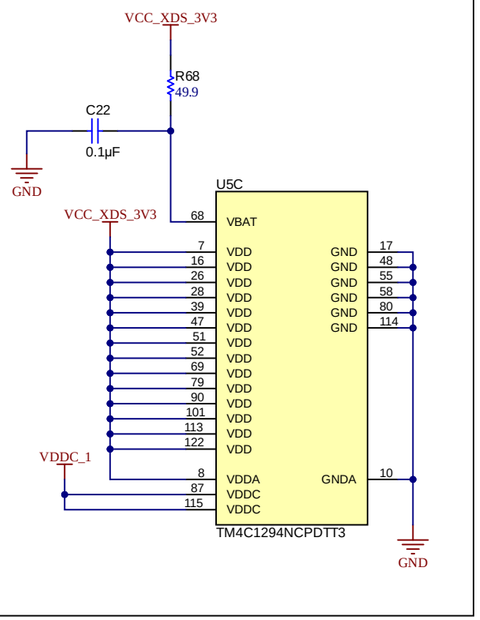
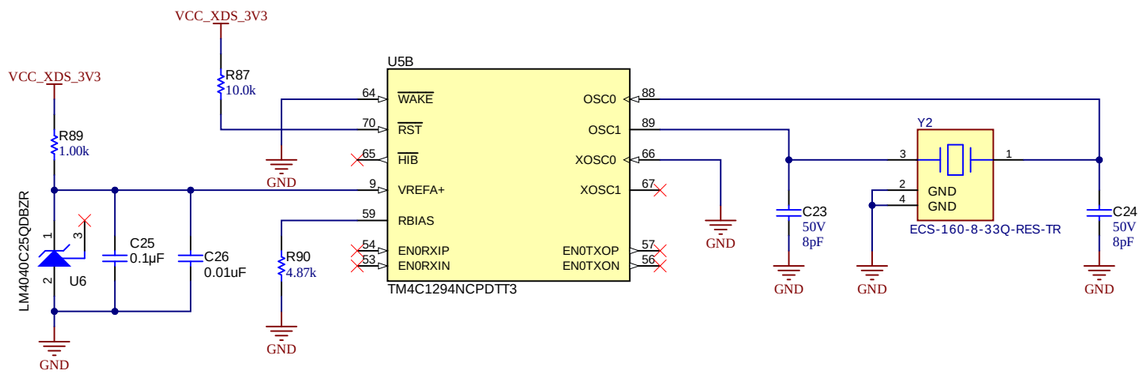
XDS110 - INTERFACE

XDS110 - POWER





Orderable: IWR6843AOPEVM	Designed for: Public Release	Mod. Date: 8/18/2025
TID #: TIDA-010281	Project Title: Safety mmWave radar with safety PMIC design	
Number: N/A Rev: A	Sheet Title:	
SVN Rev:	Assembly Variant: 001	Sheet: 9 of 10
Drawn By:	File: TIDA-010281_XDS110_interface_1B.SchDoc	Size: B
Engineer: Preeti Udapudi	Contact: http://www.ti.com/support	

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HARDWARE

FID1 FID2 FID3 FID4 FID5 FID6

PCB Number: N/A
PCB Rev: A

PCB
LOGO
Texas Instruments

CE Mark

PCB
LOGO
FCC disclaimer

PCB
LOGO
WEEE logo

PCB
LOGO
ESD Susceptible

CAUTION HOT SURFACE

LBL1
PCB Label
THT-14-423-10
Size: 0.65" x 0.20 "

LBL2
PCB Label
THT-14-423-10
Size: 0.65" x 0.20 "

ZZ1
Label Assembly Note
This Assembly Note is for PCB labels only

Variant/Label Table	
Variant	Label Text
001	IWR6843AOPEVM
002	AWR6843AOPEVM

H1
MECH

H2
MECH

H3
MECH

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

ZZ5.1
Assembly Note
Cut the thermal pad(Part Number#GPVOUS-0.125-AC-0816) for the shape and size of the inner surface of the heatsink(Part Number#MCH054) and paste it on the inner surface of the heatsink;

ZZ5.2
Assembly Note
Bring the heatsink onto the PCB bottom side (Opposite side of AOP device). Match the teeth in the heatsink with break-away area in the PCB and press the heatsink onto the PCB slightly so as thermal pad is spread all over the area

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Orderable: IWR6843AOPEVM	Designed for: Public Release	Mod. Date: 9/28/2025
TID #: TIDA-010281	Project Title: Safety mmWave radar with safety PMIC design	
Number: N/A Rev: A	Sheet Title: HARDWARE	
SVN Rev:	Assembly Variant: 001	Sheet: 10 of 10
Drawn By: Antony/Bala	File: TIDA-010281_Hardware.schdoc	Size: B
Engineer: Preeti Udapudi	Contact: http://www.ti.com/support	

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